



# CFP2000 Result

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## Advanced Micro Devices

Tyan Thunder K7 Motherboard, AMD Athlon (TM) MP 1500+

SPECfp\_rate2000 = 5.98

SPECfp\_rate\_base2000 = 5.48

SPEC license #: 49 | Tested by: AMD Austin TX | Test date: Sep-2001 | Hardware Avail: Oct-2001 | Software Avail: Sep-2001

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	1	227	8.17	1	227	8.17
171.swim	1	390	9.23	1	390	9.23
172.mgrid	1	370	5.65	1	370	5.65
173.applu	1	468	5.20	1	461	5.28
177.mesa	1	196	8.31	1	192	8.46
178.galgel	1	771	4.37	1	375	8.96
179.art	1	709	4.25	1	674	4.47
183.equake	1	306	4.92	1	251	6.02
187.facerec	1	346	6.37	1	344	6.41
188.amp	1	664	3.84	1	609	4.19
189.lucas	1	396	5.85	1	395	5.87
191.fma3d	1	391	6.23	1	391	6.23
200.sixtrack	1	327	3.91	1	317	4.03
301.apsi	1	798	3.78	1	730	4.13

### Hardware

CPU: AMD Athlon (TM) MP 1500+  
 CPU MHz: 1333  
 FPU: Integrated  
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip  
 CPU(s) orderable: 1,2  
 Parallel: No  
 Primary Cache: 64KBI + 64KBD on chip  
 Secondary Cache: 256KB(I+D) on chip  
 L3 Cache: N/A  
 Other Cache: N/A  
 Memory: 1x256MB PC2100 DDR SDRAM CL2 Registered  
 Disk Subsystem: IBM DPTA-372020  
 Other Hardware: None

### Software

Operating System: Windows 2000 SP2  
 Compiler: Compaq Visual Fortran 6.6  
 Intel C/C++ 5.0.1 build 010525Z  
 Intel Fortran 5.0.1 build 010525Z  
 Microsoft Visual Studio 6.0 SP5 (libraries)  
 MicroQuill Smartheap Library 5.0  
 File System: FAT32  
 System State: Default

## Notes/Tuning Information

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use  
 icl and ifl are the Intel C/C++ and Fortran compilers  
 f90 is the Compaq Fortran compiler  
 shlw32M.lib is the SmartHeap library V5.0 from MicroQuill www.microquill.com  
 Portability:  
 178.galgel: -FI -Fe\$@ -link -stack:32000000  
 Baseline: C icl -QxK -Qipo +FDO shlw32M.lib  
 Baseline: Fortran ifl -O3 -QxK -Qipo +FDO  
 Peak tuning:  
 168.wupwise: ifl -O3 -QxK -Qwp\_ipo +FDO  
 171.swim: ifl -O3 -QxK -Qwp\_ipo +FDO  
 172.mgrid: ifl -O3 -QxK -Qwp\_ipo +FDO  
 173.applu: ifl -O3 -QxK -Qwp\_ipo -Qscalar\_rep- -Qauto +FDO  
 177.mesa: icl -O3 -QxK -Qwp\_ipo -Oa -Qunroll10 +FDO shlw32M.lib  
 178.galgel: f90 -Optimize:5 -fast  
 179.art: icl -O3 -QxK -QaxW -Qwp\_ipo -Oa +FDO shlw32M.lib  
 183.equake: icl -O3 -QxK -Qwp\_ipo -Qrcd -Oa +FDO shlw32M.lib



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## Notes/Tuning Information (Continued)

```
187.facerec: f90 -Optimize:5 -fast
188.ampp:    icl -O3 -QxK -Qwp_ipo -Oa +FDO
189.lucas:   ifl -O3 -QxK -Qwp_ipo +FDO shlw32M.lib
191.fma3d:   ifl -O3 -QxK -Qwp_ipo +FDO
200.sixtrack: ifl -QxK -QaxW -Qwp_ipo -Qprefetch +FDO
301.apsi:    f90 -Optimize:5 -fast
```

Library ordering for 189.lucas (to include SmartHeap correctly with default libs):

```
LIBS=libIEPCF90.lib libintrins.lib libF90.lib
libqwind.lib libm.lib shlw32M.lib LIBC.lib libirc.lib OLDNAMES.lib
```

ONESTEP is used for all base and peak runs

The tested system can be assembled using a case such as the Chenming 601 AE-F-O,  
a 460W power supply such as the Delta Electronics DPS465 AB A

The System bus runs at 266MHz