



# CFP2000 Result

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**Advanced Micro Devices**  
ASUS A8N-SLI Deluxe, AMD Athlon (TM) 64 FX-57

SPECfp2000 = **1894**  
SPECfp\_base2000 = **1814**

SPEC license #: 49 | Tested by: AMD, Austin, TX | Test date: Jun-2005 | Hardware Avail: Jun-2005 | Software Avail: Jan-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
168.wupwise	1600	54.4	2942	54.4	2942	[Bar chart showing ratio 2942]			
171.swim	3100	136	2280	129	2396	[Bar chart showing ratio 2396]			
172.mgrid	1800	117	1539	117	1539	[Bar chart showing ratio 1539]			
173.applu	2100	138	1523	123	1704	[Bar chart showing ratio 1704]			
177.mesa	1400	67.4	2078	63.8	2194	[Bar chart showing ratio 2194]			
178.galgel	2900	98.3	2952	91.1	3184	[Bar chart showing ratio 3184]			
179.art	2600	134	1939	131	1988	[Bar chart showing ratio 1988]			
183.quake	1300	72.1	1802	72.1	1802	[Bar chart showing ratio 1802]			
187.facerec	1900	90.7	2094	88.8	2140	[Bar chart showing ratio 2140]			
188.amp	2200	155	1418	146	1512	[Bar chart showing ratio 1512]			
189.lucas	2000	103	1933	91.7	2181	[Bar chart showing ratio 2181]			
191.fma3d	2100	136	1547	125	1682	[Bar chart showing ratio 1682]			
200.sixtrack	1100	127	863	127	863	[Bar chart showing ratio 863]			
301.apsi	2600	161	1612	161	1612	[Bar chart showing ratio 1612]			

### Hardware

CPU: AMD Athlon (TM) 64 FX-57  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip  
 CPU(s) orderable: 1  
 Parallel: No  
 Primary Cache: 64KBI + 64KBD on chip  
 Secondary Cache: 1024KB(I+D) on chip  
 L3 Cache: N/A  
 Other Cache: N/A  
 Memory: 2x512 MB, PC3200 DDR SDRAM CL2.0  
 Disk Subsystem: SATA, Western Digital WD740  
 Other Hardware: None

### Software

Operating System: Microsoft Windows XP Pro SP2  
 Compiler: Intel C++ 8.0 build 20040714Z, Intel Fortran 8.1 build 20041019Z, PGI Fortran compiler 5.2-4 for Windows XP, AMD Core Math library Version 2.1 (ACML), Microsoft Visual Studio .NET 7.0.9466 (libraries), MicroQuill Smartheap Library 7.0  
 File System: NTFS  
 System State: Default

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
+ACML is linking with AMD Core Math Library V2.1
ONESTEP is set for all peak runs.
ifort is the Intel Fortran compiler, icl is the Intel C++ compiler and
pgf90 is the PGI Fortran compiler.
The Intel C++ 8.0 and the Intel Fortran 8.1 compilers are setup in the following order:
  "c:\program files\intel\fortran\compiler80\ia32\bin\ifortvars.bat"
  "c:\program files\intel\cpp\compiler80\ia32\bin\iclvars.bat"
To make sure that the correct libraries are selected, the following link option is
added for the peak runs where Intel Fortran 8.1 compiler is used:
  LDOPT = -Fe$@ -link -LIBPATH:"c:\program files\intel\fortran\compiler80\ia32\lib"
(denoted by +LIBPATH:INTEL8.1 in the optimization flags listed below)
Portability:
  178.galgel: -Mfixed
Baseline: C      : icl  -fast -arch:SSE2 -QaxW +FDO
Baseline: Fortran: pgf90 -fastsse -Mipa=fast,inline
Peak tuning:
```



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## Notes/Tuning Information (Continued)

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168.wupwise:   pgf90 basepeak=yes
171.swim:     ifort -Qipo -O3 -QaxN -QxW +FDO -Qunroll0      +LIBPATH:INTEL8.1
172.mgrid:   pgf90 basepeak=yes
173.applu:   ifort -Qipo -O3 -QaxN -QxW +FDO -auto          +LIBPATH:INTEL8.1
177.mesa:    icl -Qipo -arch:SSE2 +FDO -Qunroll1 -Qansi_alias
              -Qoption,f,-ip_ninl_max_stats=1500,-ip_ninl_max_total_stats=4500
179.art:     icl -Qipo -Zp4 +FDO
183.quake:   icl basepeak=yes
178.galgel:  pgf90 -fastsse -Mipa=fast,safe RM_SOURCES=lapak.f90 -Munix +ACML
187.facerec: ifort -Qipo -QxW +FDO -Qunroll3 +LIBPATH:INTEL8.1
              -Qoption,f,-ip_ninl_max_stats=2500,-ip_ninl_max_total_stats=7000
188.ammp:    icl -Oa -arch:SSE2 -Zp4 -Qansi_alias
189.lucas:   ifort -Qipo -QxW -Qunroll1 +LIBPATH:INTEL8.1
191.fma3d:   ifort -Qipo -QaxN -QxW +FDO -Qansi-alias- +LIBPATH:INTEL8.1
200.sixtrack: pgf90 basepeak=yes
301.apsi:    pgf90 basepeak=yes

```

The tested system can be assembled using a standard ATX form factor, an Antec True 550 EPS12V power supply, and a PCI or PCIe graphics card.

Memory timings manually set in BIOS: CAS=2, TRCD=2, TRAS=5, TRP=2

Memory used is Corsair CMX512-3200XLPRO configured in dual channel mode.

BIOS version V6.00PG