



CINT2000 Result

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HCL INFOSYSTEMS LTD

Infiniti Global Line/NM 2700AO (3.0 GHz Intel Xeon Processor 5160)

SPECint_rate2000 = 58.2

SPECint_rate_base2000 = 58.2

SPEC license #: 2828 | Tested by: HCL INFOSYSTEMS LTD | Test date: Aug-2006 | Hardware Avail: Aug-2006 | Software Avail: Aug-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	80.5	40.4	2	80.5	40.4
175.vpr	2	77.6	41.9	2	77.6	41.9
176.gcc	2	41.6	61.4	2	41.6	61.4
181.mcf	2	93.2	44.8	2	93.2	44.8
186.crafty	2	29.0	80.0	2	29.0	80.0
197.parser	2	99.1	42.2	2	99.1	42.2
252.eon	2	32.3	93.3	2	32.3	93.3
253.perlbnk	2	58.8	71.0	2	58.8	71.0
254.gap	2	50.5	50.6	2	50.5	50.6
255.vortex	2	50.2	87.8	2	50.2	87.8
256.bzip2	2	78.0	44.6	2	78.0	44.6
300.twolf	2	94.9	73.3	2	94.9	73.3

Hardware

CPU: Intel Xeon 5160 processor (3.0 GHz, 4MB L2, 1333 MHz system bus)
CPU MHz: 2992
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32KBI + 32KBD on chip, per core
Secondary Cache: 4096KB(I+D) on chip, per chip
L3 Cache: None
Other Cache: None
Memory: 8x1024GB ECC FB-DIMM DDR2-667 CL5
Disk Subsystem: Seagate ST3802110AS (SATA, 7.2krpm, 80GB)
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux 4 AS U3, 2.6.9-34.ELsmp EM64T
Compiler: Intel(R) C++ Compiler for EM64T, V9.1 (Build 20060707)
File System: ext3
System State: Multi-user, runlevel 3

Notes/Tuning Information

Enhanced SpeedStep Disabled in BIOS
Hardware Prefetcher Enabled in BIOS (Default)
Adjacent Cache Line Prefetch Enabled in BIOS (Default)
2.6.9-34.ELsmp
+FDO: PASS1=-prof_gen PASS2=-prof_use

SPEC CINT2000 Baseline optimization flags:
C programs: -fast -auto_ilp32 +FDO
C++ programs: -fast -ansi_alias +FDO

SPEC CINT2000 Peak optimization flags
basepeak=yes set globally

Portability Flags:
186.crafty: -DLINUX_i386
252.eon: -DHAS_ERRLIST
253.perlbnk: -DSPEC_CPU2000_NEED_BOOL -DSPEC_CPU2000_LINUX_I386
-DSPEC_CPU2000_LP64
254.gap: -DSPEC_CPU2000_LP64 -DSYS_IS_USG
255.vortex: -DSPEC_CPU2000_LP64