



CINT2000 Result

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Hewlett-Packard Company
ProLiant ML310 G4 (2.66 GHz, Intel Xeon processor 3070)

SPECint_rate2000 = **59.2**
SPECint_rate_base2000 = **58.9**

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Sep-2006 | Hardware Avail: Sep-2006 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	86.0	37.8	2	85.9	37.8
175.vpr	2	78.6	41.3	2	76.0	42.7
176.gcc	2	37.8	67.6	2	37.8	67.6
181.mcf	2	69.5	60.1	2	69.5	60.1
186.crafty	2	41.0	56.6	2	41.0	56.5
197.parser	2	88.5	47.2	2	88.1	47.4
252.eon	2	37.3	80.9	2	37.4	80.7
253.perlbnk	2	57.4	72.7	2	56.9	73.4
254.gap	2	43.1	59.2	2	42.8	59.6
255.vortex	2	44.9	98.1	2	45.0	98.1
256.bzip2	2	81.3	42.8	2	81.3	42.8
300.twolf	2	98.3	70.8	2	98.3	70.8

Hardware

CPU: Intel Xeon processor 3070 (2.66GHz, 4MB L2 shared, 1066 MHz bus)
CPU MHz: 2660
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: N/A
Other Cache: N/A
Memory: 4 GB (2x2 GB PC2-5300E)
Disk Subsystem: 1x80 GB SATA150
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Intel Fortran Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -fast +FDO ONESTEP=yes
175.vpr: -fast +FDO ONESTEP=yes
176.gcc: basepeak=1
181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib
186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
197.parser: -fast +FDO ONESTEP=yes
252.eon: -fast +FDO
253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
254.gap: -fast +FDO ONESTEP=yes
255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib
256.bzip2: basepeak=1
```



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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
Other Configuration Notes
Adjacent Sector Prefetch Disabled