



# SPEC® CINT2006 Result

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## Tyan

(Test Sponsor: Advanced Micro Devices)

**SPECint®2006 =** --

Tyan Thunder K8E Tomcat (S2865) (AMD Opteron 146)

**SPECint\_base2006 =** 9.02

**CPU2006 license:** 49

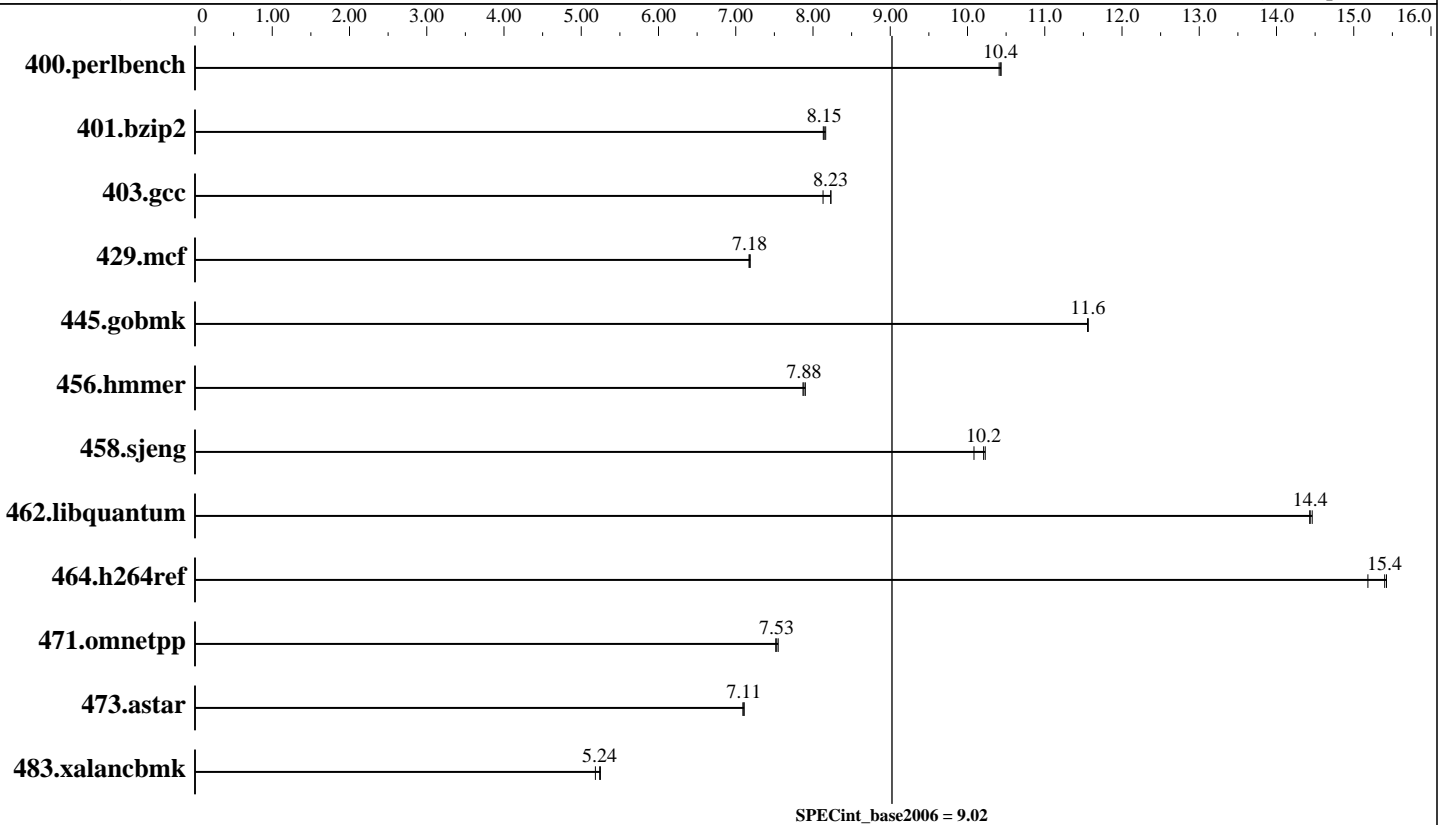
**Test sponsor:** Advanced Micro Devices

**Tested by:** Advanced Micro Devices

**Test date:** Apr-2006

**Hardware Availability:** Aug-2005

**Software Availability:** Apr-2006



### Hardware

CPU Name: AMD Opteron 146  
 CPU Characteristics:  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 64 KB I + 64 KB D on chip per chip  
 Secondary Cache: 1 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 2 GB (2 x 1 GB DDR400 CL2)  
 Disk Subsystem: 250GB SATA  
 Other Hardware: None

### Software

Operating System: SUSE Linux 10.1 (for AMD64)  
 Compiler: gcc , g++ 4.1.0 (for AMD64)  
 Auto Parallel: No  
 File System: ext3  
 System State: runlevel 3  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other Software: None



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	936	10.4	<u>937</u>	<u>10.4</u>	939	10.4						
401.bzip2	<u>1185</u>	<u>8.15</u>	1182	8.16	1186	8.13						
403.gcc	990	8.13	<u>978</u>	<u>8.23</u>	978	8.23						
429.mcf	1272	7.17	1269	7.19	<u>1270</u>	<u>7.18</u>						
445.gobmk	<u>907</u>	<u>11.6</u>	907	11.6	908	11.6						
456.hmmer	<u>1184</u>	<u>7.88</u>	1181	7.90	1185	7.87						
458.sjeng	<u>1185</u>	<u>10.2</u>	1200	10.1	1183	10.2						
462.libquantum	<u>1435</u>	<u>14.4</u>	1433	14.5	1436	14.4						
464.h264ref	1435	15.4	<u>1437</u>	<u>15.4</u>	1458	15.2						
471.omnetpp	828	7.55	<u>830</u>	<u>7.53</u>	831	7.52						
473.astar	<u>988</u>	<u>7.11</u>	988	7.11	990	7.09						
483.xalancbmk	<u>1317</u>	<u>5.24</u>	1315	5.25	1331	5.18						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Tested by Advanced Micro Devices

The system can be assembled using an ATX power supply such as a Sparkle FSP350-60PN and a case such as a Chenbro RM125

## Base Compiler Invocation

C benchmarks:

gcc

C++ benchmarks:

g++

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-O3 -fno-inline-functions -funroll-loops

C++ benchmarks:

-O3 -funroll-loops

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.04.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.04.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.xml)

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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