



# SPEC<sup>®</sup> CINT2006 Result

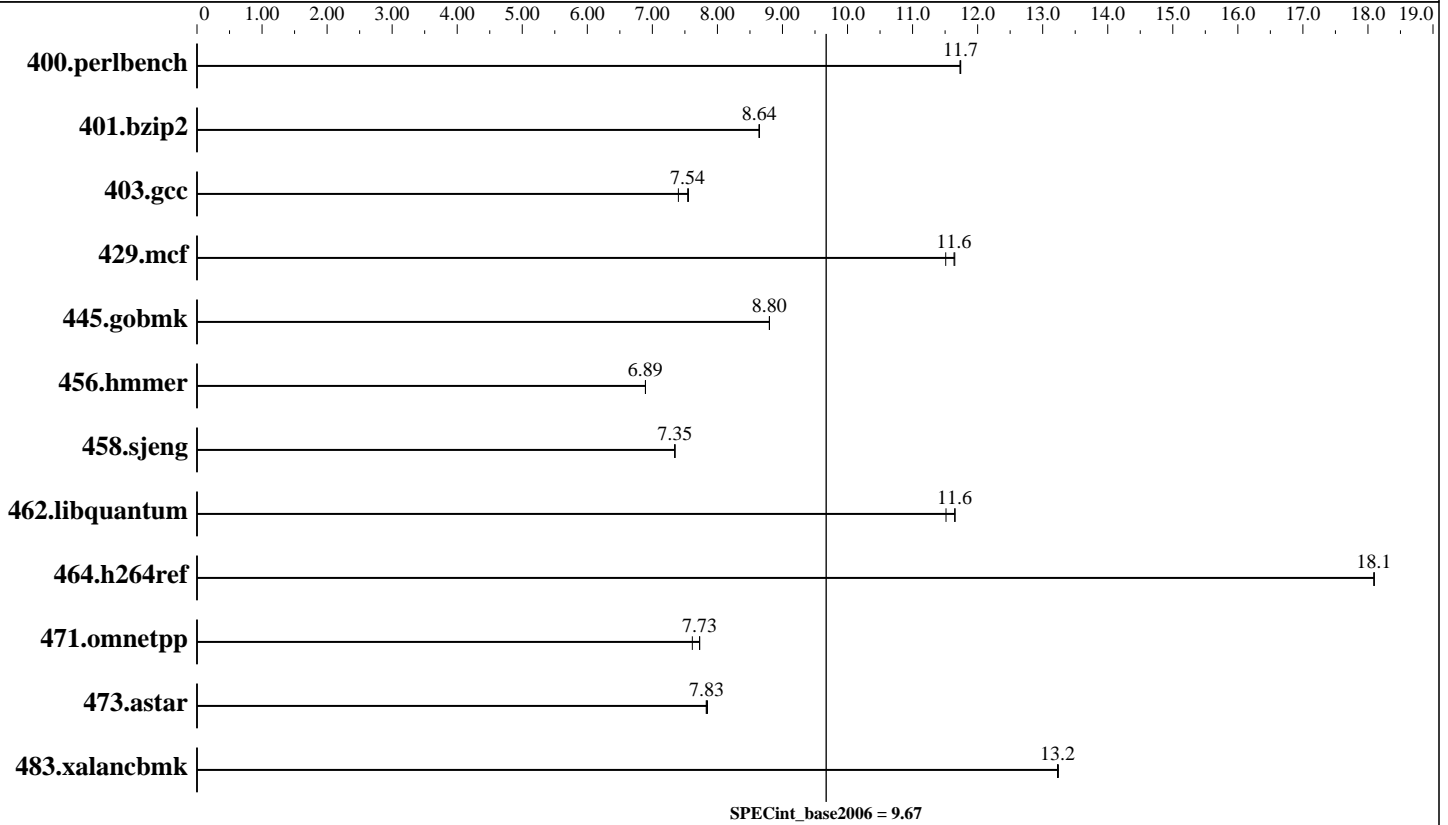
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IBM Corporation  
IBM System x3950

SPECint<sup>®</sup>2006 = **Not Run**  
SPECint\_base2006 = **9.67**

CPU2006 license: 11  
Test sponsor: IBM Corporation  
Tested by: IBM Corporation

Test date: Dec-2006  
Hardware Availability: Oct-2006  
Software Availability: Aug-2006



### Hardware

CPU Name: Intel Xeon 7130N  
 CPU Characteristics: 667 MHz bus  
 CPU MHz: 3166  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,4 chips  
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (16 x 2048 MB ECC PC2-3200)  
 Disk Subsystem: 73 GB SAS, 10k RPM  
 Other Hardware: None

### Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Build no 20060816  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Smart Heap Library, Version 8



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	832	11.7	<b>832</b>	<b>11.7</b>	833	11.7						
401.bzip2	1116	8.65	<b>1116</b>	<b>8.64</b>	1117	8.64						
403.gcc	1088	7.40	<b>1067</b>	<b>7.54</b>	1066	7.55						
429.mcf	<b>784</b>	<b>11.6</b>	792	11.5	783	11.6						
445.gobmk	1192	8.80	1192	8.80	<b>1192</b>	<b>8.80</b>						
456.hmmr	1353	6.89	1354	6.89	<b>1353</b>	<b>6.89</b>						
458.sjeng	<b>1647</b>	<b>7.35</b>	1648	7.34	1647	7.35						
462.libquantum	1778	11.7	1800	11.5	<b>1779</b>	<b>11.6</b>						
464.h264ref	1223	18.1	<b>1223</b>	<b>18.1</b>	1223	18.1						
471.omnetpp	821	7.61	809	7.73	<b>809</b>	<b>7.73</b>						
473.astar	895	7.85	<b>896</b>	<b>7.83</b>	897	7.83						
483.xalancbmk	<b>522</b>	<b>13.2</b>	521	13.2	522	13.2						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Bios Settings  
Hardware Prefetch enabled  
Memory Array set to High-Performance Memory Array

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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