



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = Not Run

IBM System x3200 (Intel Xeon X3220)

SPECint_base2006 = 15.0

CPU2006 license: 11

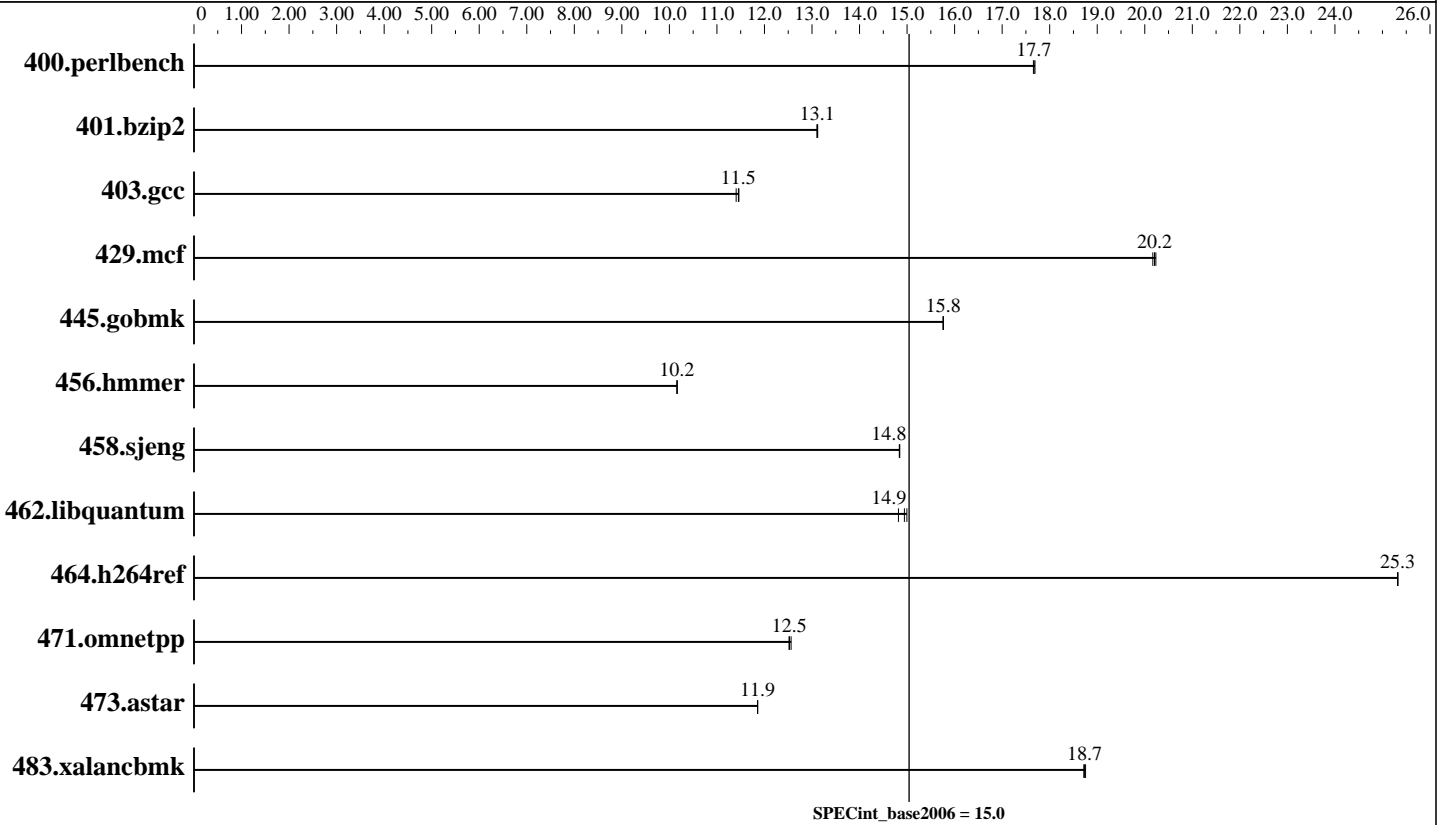
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Feb-2007

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon X3220
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2GB PC2-5300 ECC)
 Disk Subsystem: 1 x 80 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	553	17.7	<u>553</u>	<u>17.7</u>	552	17.7						
401.bzip2	736	13.1	736	13.1	<u>736</u>	<u>13.1</u>						
403.gcc	706	11.4	702	11.5	<u>703</u>	<u>11.5</u>						
429.mcf	452	20.2	<u>451</u>	<u>20.2</u>	451	20.2						
445.gobmk	666	15.8	<u>665</u>	<u>15.8</u>	665	15.8						
456.hammer	918	10.2	918	10.2	<u>918</u>	<u>10.2</u>						
458.sjeng	816	14.8	815	14.8	<u>815</u>	<u>14.8</u>						
462.libquantum	1398	14.8	<u>1387</u>	<u>14.9</u>	1382	15.0						
464.h264ref	874	25.3	874	25.3	<u>874</u>	<u>25.3</u>						
471.omnetpp	499	12.5	498	12.6	<u>499</u>	<u>12.5</u>						
473.astar	592	11.9	592	11.9	<u>592</u>	<u>11.9</u>						
483.xalancbmk	369	18.7	<u>368</u>	<u>18.7</u>	368	18.8						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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