



# SPEC® CINT2006 Result

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Itaotec

SPECint®\_rate2006 = 56.1

Servidor Itaotec LX201 (Intel Xeon E5320)

SPECint\_rate\_base2006 = 56.1

CPU2006 license: 9001

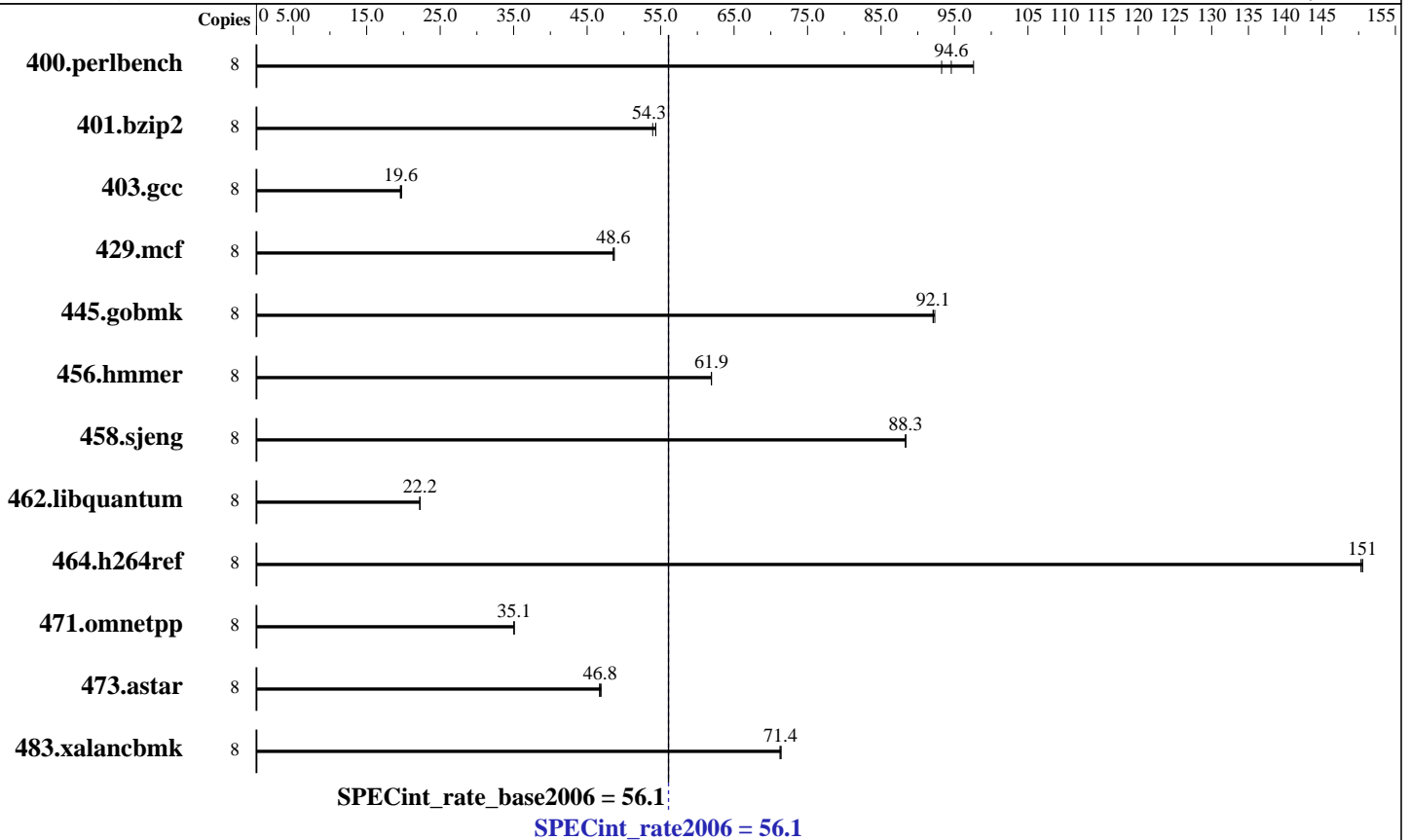
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Feb-2007

Hardware Availability: Feb-2007

Software Availability: May-2006



## Hardware

CPU Name: Intel Xeon E5320  
 CPU Characteristics: 1066MHz system bus  
 CPU MHz: 1860  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1-2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)  
 Disk Subsystem: 120 GB SATA, 7200RPM  
 Other Hardware: None

## Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Package ID W\_CC\_C\_9.1.025 Build no 20060519Z  
 Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: Microquill SmartHeap Library v.8.0 for SMP



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	801	97.6	<u>827</u>	<u>94.6</u>	838	93.2	8	801	97.6	<u>827</u>	<u>94.6</u>	838	93.2
401.bzip2	8	1432	53.9	<u>1421</u>	<u>54.3</u>	1420	54.3	8	1432	53.9	<u>1421</u>	<u>54.3</u>	1420	54.3
403.gcc	8	<u>3280</u>	<u>19.6</u>	3259	19.8	3289	19.6	8	<u>3280</u>	<u>19.6</u>	3259	19.8	3289	19.6
429.mcf	8	<u>1502</u>	<u>48.6</u>	1502	48.6	1498	48.7	8	<u>1502</u>	<u>48.6</u>	1502	48.6	1498	48.7
445.gobmk	8	911	92.1	909	92.3	<u>911</u>	<u>92.1</u>	8	911	92.1	909	92.3	<u>911</u>	<u>92.1</u>
456.hammer	8	1205	61.9	1206	61.9	<u>1206</u>	<u>61.9</u>	8	1205	61.9	1206	61.9	<u>1206</u>	<u>61.9</u>
458.sjeng	8	<u>1096</u>	<u>88.3</u>	1096	88.4	1096	88.3	8	<u>1096</u>	<u>88.3</u>	1096	88.4	1096	88.3
462.libquantum	8	7455	22.2	7449	22.3	<u>7452</u>	<u>22.2</u>	8	7455	22.2	7449	22.3	<u>7452</u>	<u>22.2</u>
464.h264ref	8	1176	151	1178	150	<u>1176</u>	<u>151</u>	8	1176	151	1178	150	<u>1176</u>	<u>151</u>
471.omnetpp	8	1426	35.1	<u>1426</u>	<u>35.1</u>	1426	35.1	8	1426	35.1	<u>1426</u>	<u>35.1</u>	1426	35.1
473.astar	8	1197	46.9	<u>1201</u>	<u>46.8</u>	1203	46.7	8	1197	46.9	<u>1201</u>	<u>46.8</u>	1203	46.7
483.xalancbmk	8	775	71.2	<u>774</u>	<u>71.4</u>	773	71.4	8	775	71.2	<u>774</u>	<u>71.4</u>	773	71.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlSMPMt.lib -link /FORCE:MULTIPLE  
C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlSMPMt.lib  
-link /FORCE:MULTIPLE



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Optimization Flags

C benchmarks:

400.perlbench: basepeak = yes

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml>



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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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