



SPEC® CFP2006 Result

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Itautec

SPECfp®_rate2006 = 40.5

Servidor Itautec MX221 (Intel Xeon E5320)

SPECfp_rate_base2006 = 40.5

CPU2006 license: 9001

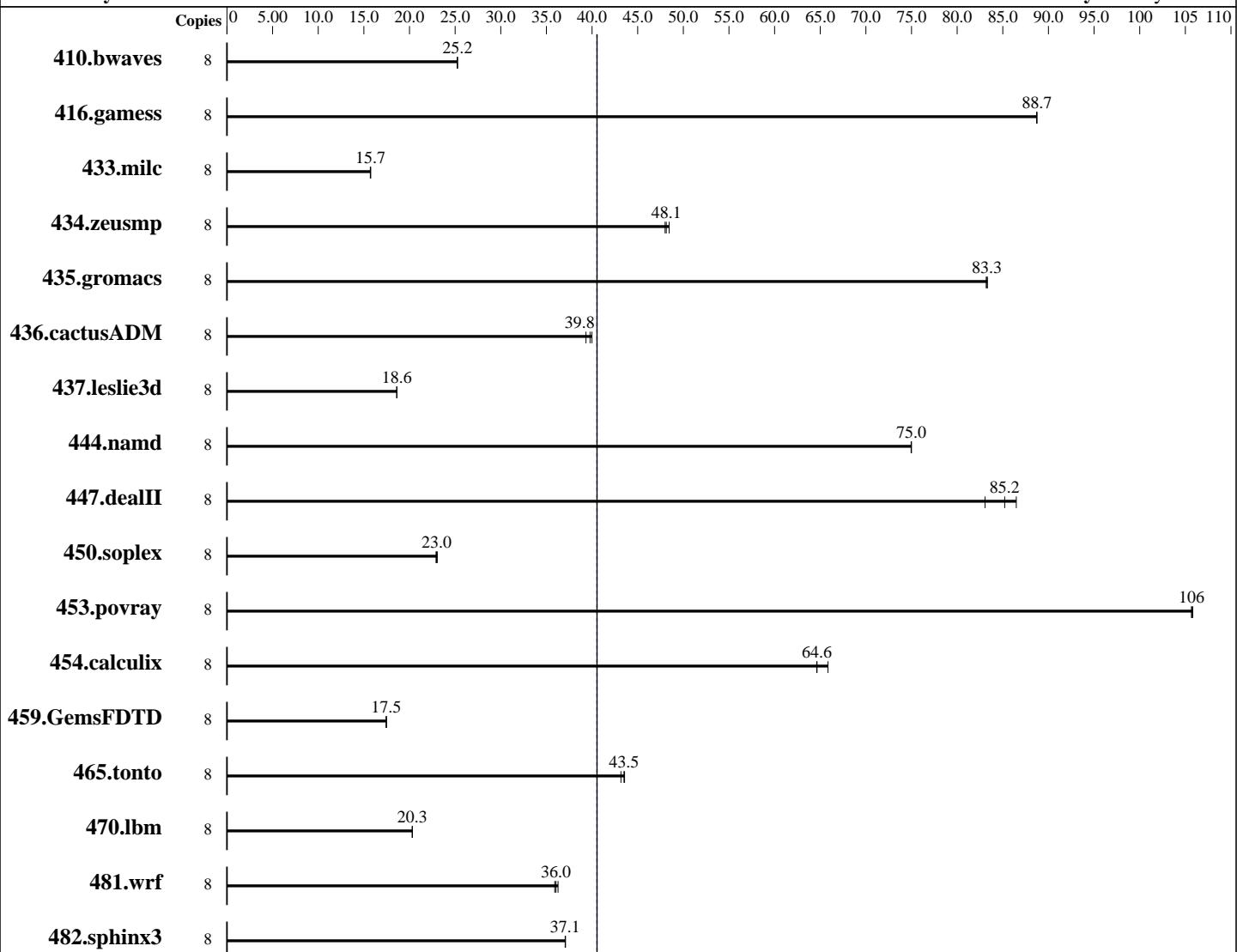
Test date: Feb-2007

Test sponsor: Itautec

Hardware Availability: Feb-2007

Tested by: Itautec

Software Availability: May-2006



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Hardware

CPU Name: Intel Xeon E5320
CPU Characteristics: 1066MHz system bus
CPU MHz: 1860
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1-2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler: Intel C++ Compiler for IA32 version 9.1
Package ID W_CC_C_9.1.025 Build no 20060519Z
Intel Fortran Compiler for IA32 version 9.1
Package ID W_FC_C_9.1.025 Build no 20060519Z
Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel: No
File System: NTFS

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L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
 Disk Subsystem: 73 GB SCSI, 10000RPM
 Other Hardware: None

System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	4310	25.2	4303	25.3	4306	25.2	8	4310	25.2	4303	25.3	4306	25.2
416.gamess	8	1766	88.7	<u>1765</u>	88.7	1765	88.7	8	1766	88.7	<u>1765</u>	88.7	1765	88.7
433.milc	8	4665	15.7	4676	15.7	4667	<u>15.7</u>	8	4665	15.7	4676	15.7	4667	<u>15.7</u>
434.zeusmp	8	1517	48.0	<u>1512</u>	48.1	1503	48.4	8	1517	48.0	<u>1512</u>	48.1	1503	48.4
435.gromacs	8	<u>686</u>	<u>83.3</u>	687	83.2	686	83.3	8	<u>686</u>	<u>83.3</u>	687	83.2	686	83.3
436.cactusADM	8	<u>2403</u>	39.8	2391	40.0	2430	39.3	8	<u>2403</u>	39.8	2391	40.0	2430	39.3
437.leslie3d	8	4038	18.6	<u>4040</u>	18.6	4042	18.6	8	4038	18.6	<u>4040</u>	18.6	4042	18.6
444.namd	8	856	75.0	856	75.0	<u>856</u>	75.0	8	856	75.0	856	75.0	<u>856</u>	75.0
447.dealII	8	1058	86.5	1102	83.1	<u>1074</u>	<u>85.2</u>	8	1058	86.5	1102	83.1	<u>1074</u>	85.2
450.soplex	8	2913	22.9	<u>2905</u>	<u>23.0</u>	2896	23.0	8	2913	22.9	<u>2905</u>	<u>23.0</u>	2896	23.0
453.povray	8	403	106	<u>403</u>	<u>106</u>	402	106	8	403	106	<u>403</u>	<u>106</u>	402	106
454.calculix	8	1021	64.6	<u>1021</u>	64.6	1002	65.8	8	1021	64.6	<u>1021</u>	64.6	1002	65.8
459.GemsFDTD	8	4865	17.4	<u>4861</u>	<u>17.5</u>	4858	17.5	8	4865	17.4	<u>4861</u>	<u>17.5</u>	4858	17.5
465.tonto	8	<u>1810</u>	43.5	1807	43.6	1824	43.2	8	<u>1810</u>	<u>43.5</u>	1807	43.6	1824	43.2
470.lbm	8	5410	20.3	5414	20.3	<u>5414</u>	<u>20.3</u>	8	5410	20.3	5414	20.3	<u>5414</u>	<u>20.3</u>
481.wrf	8	2488	35.9	<u>2479</u>	<u>36.0</u>	2464	36.3	8	2488	35.9	<u>2479</u>	<u>36.0</u>	2464	36.3
482.sphinx3	8	4203	37.1	<u>4207</u>	<u>37.1</u>	4207	37.1	8	<u>4203</u>	<u>37.1</u>	<u>4207</u>	<u>37.1</u>	4207	37.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The Servidor Itautec MX221 and the Servidor Itautec MX201 are electronically equivalent.

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

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Itaute

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CPU2006 license: 9001

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Test sponsor: Itaute

Hardware Availability: Feb-2007

Tested by: Itaute

Software Availability: May-2006

Base Compiler Invocation (Continued)

Fortran benchmarks:
fort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 sh1SMPMt.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 sh1SMPMt.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F950000000 -link /FORCE:MULTIPLE

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

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SPEC CFP2006 Result

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Itautech

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Servidor Itautech MX221 (Intel Xeon E5320)

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CPU2006 license: 9001

Test date: Feb-2007

Test sponsor: Itautech

Hardware Availability: Feb-2007

Tested by: Itautech

Software Availability: May-2006

Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: basepeak = yes

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautech-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautech-ic91-flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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