



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor X5355,  
2.66 GHz

**SPECint®\_rate2006 = 52.8**

**SPECint\_rate\_base2006 = 50.5**

CPU2006 license: 22

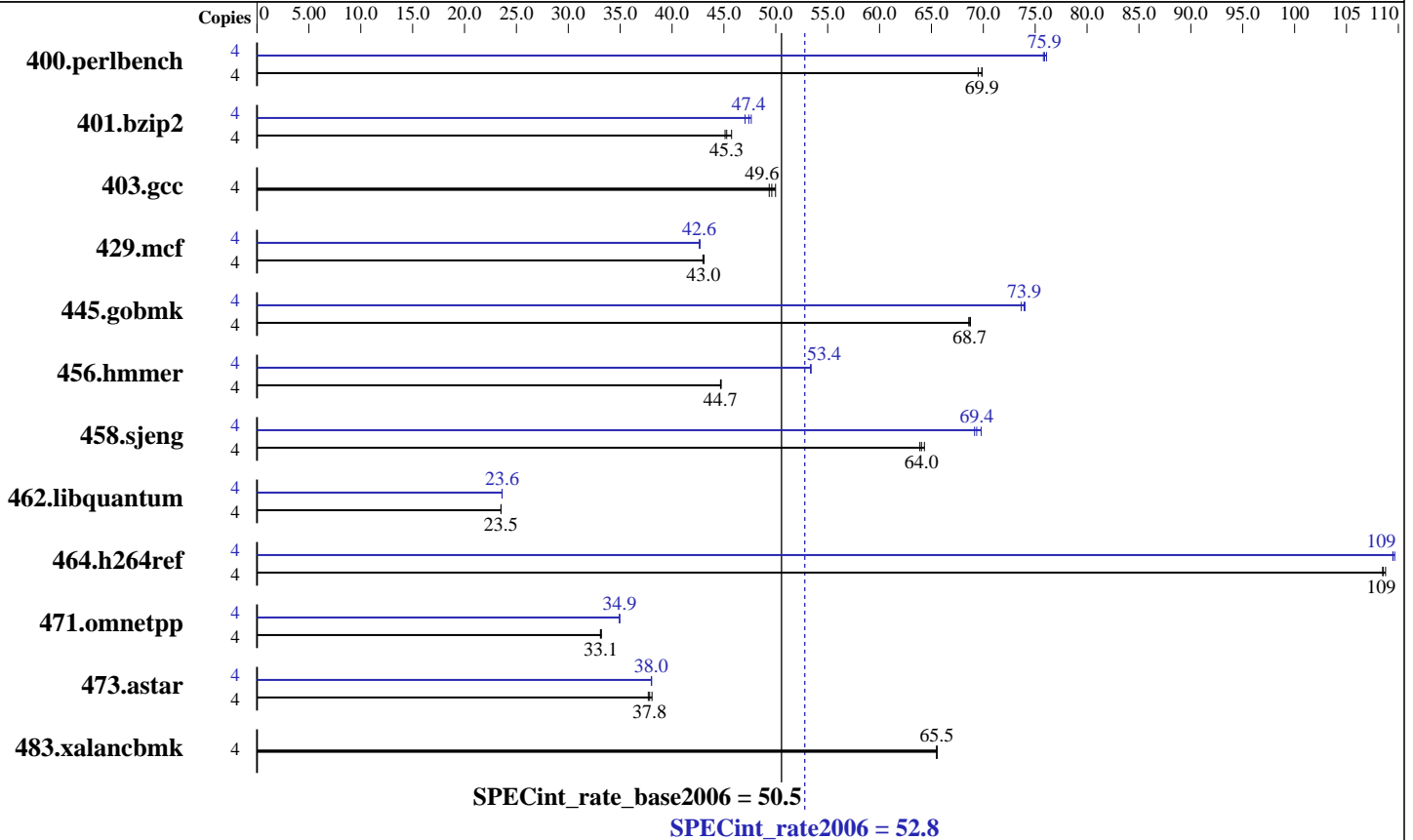
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Jan-2007

Software Availability: Mar-2007



### Hardware

CPU Name: Intel Xeon X5355  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: SAS (73GB 15400 rpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	562	69.5	<u>559</u>	<u>69.9</u>	559	69.9	4	516	75.8	<u>515</u>	<u>75.9</u>	514	76.1
401.bzip2	4	844	45.7	<u>853</u>	<u>45.3</u>	856	45.1	4	821	47.0	<u>814</u>	<u>47.4</u>	810	47.6
403.gcc	4	644	50.0	652	49.4	<u>649</u>	<u>49.6</u>	4	644	50.0	652	49.4	<u>649</u>	<u>49.6</u>
429.mcf	4	849	43.0	847	43.1	<u>848</u>	<u>43.0</u>	4	856	42.6	<u>855</u>	<u>42.6</u>	854	42.7
445.gobmk	4	612	68.6	610	68.8	<u>611</u>	<u>68.7</u>	4	<u>568</u>	<u>73.9</u>	570	73.6	567	74.0
456.hmmmer	4	835	44.7	<u>835</u>	<u>44.7</u>	834	44.7	4	<u>699</u>	<u>53.4</u>	699	53.4	699	53.4
458.sjeng	4	758	63.9	<u>756</u>	<u>64.0</u>	752	64.3	4	694	69.8	<u>698</u>	<u>69.4</u>	700	69.2
462.libquantum	4	3525	23.5	<u>3524</u>	<u>23.5</u>	3523	23.5	4	3510	23.6	3508	23.6	<u>3509</u>	<u>23.6</u>
464.h264ref	4	816	108	<u>816</u>	<u>109</u>	814	109	4	<u>809</u>	<u>109</u>	809	109	807	110
471.omnetpp	4	755	33.1	<u>755</u>	<u>33.1</u>	753	33.2	4	715	35.0	716	34.9	<u>716</u>	<u>34.9</u>
473.astar	4	<u>742</u>	<u>37.8</u>	738	38.1	745	37.7	4	738	38.0	<u>739</u>	<u>38.0</u>	739	38.0
483.xalancbmk	4	421	65.5	421	65.5	<u>421</u>	<u>65.5</u>	4	421	65.5	421	65.5	<u>421</u>	<u>65.5</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Adjacent Sector Prefetch = Disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and  
the PRIMERGY TX300 S3 are electronically equivalent.

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## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast

C++ benchmarks:  
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmarheap

445.gobmk: Same as 429.mcf

456.hmmcr: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmarheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmarheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)



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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 11:21:34 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 26 June 2007.