



# SPEC® CINT2006 Result

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## Bull SAS

SPECint®\_rate2006 = 21.4

NovaScale R410  
(Intel Xeon processor 3040,1.86GHz)

SPECint\_rate\_base2006 = 20.6

CPU2006 license: 20

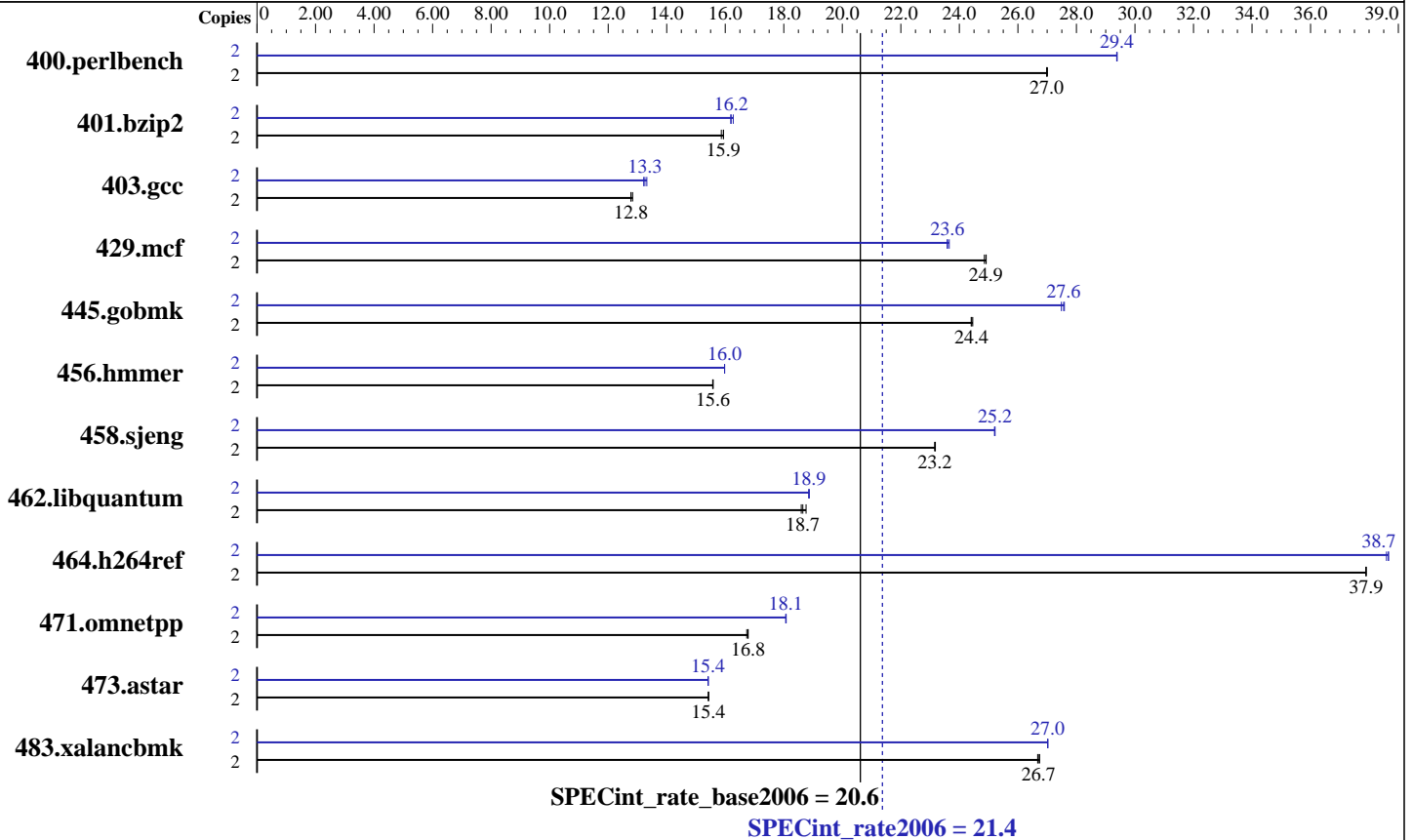
Test date: Jun-2007

Test sponsor: Bull SAS

Hardware Availability: Feb-2007

Tested by: Bull SAS

Software Availability: Dec-2006



### Hardware

CPU Name: Intel Xeon 3040  
 CPU Characteristics: 1.86 GHz, 4 MB L2, 1066 MHz system bus  
 CPU MHz: 1860  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 2 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4x2 GB) PC2-5300F ECC CL5  
 Disk Subsystem: 1x80 GB SATA, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Windows Server 2003 Enterprise Edition (32 bits) Service Pack1  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Package ID W\_CC\_C\_9.1.033 Build no 20061103Z  
 Microsoft Visual Studio .NET 2003 (lib & linker)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: MicroQuill SmartHeap Library 8.0 (shIW32M.lib)



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	723	27.0	<u>724</u>	<u>27.0</u>	724	27.0	2	665	29.4	<u>665</u>	<u>29.4</u>	665	29.4
401.bzip2	2	<u>1211</u>	<u>15.9</u>	1216	15.9	1211	15.9	2	1192	16.2	<u>1191</u>	<u>16.2</u>	1186	16.3
403.gcc	2	1260	12.8	1254	12.8	<u>1260</u>	<u>12.8</u>	2	1218	13.2	1209	13.3	<u>1215</u>	<u>13.3</u>
429.mcf	2	734	24.9	<u>732</u>	<u>24.9</u>	732	24.9	2	773	23.6	771	23.7	<u>773</u>	<u>23.6</u>
445.gobmk	2	858	24.5	<u>859</u>	<u>24.4</u>	860	24.4	2	763	27.5	760	27.6	<u>761</u>	<u>27.6</u>
456.hammer	2	1198	15.6	<u>1197</u>	<u>15.6</u>	1197	15.6	2	<u>1168</u>	<u>16.0</u>	1168	16.0	1168	16.0
458.sjeng	2	1045	23.2	1044	23.2	<u>1045</u>	<u>23.2</u>	2	960	25.2	960	25.2	<u>960</u>	<u>25.2</u>
462.libquantum	2	2228	18.6	2209	18.8	<u>2222</u>	<u>18.7</u>	2	<u>2197</u>	<u>18.9</u>	2196	18.9	2198	18.9
464.h264ref	2	<u>1168</u>	<u>37.9</u>	1168	37.9	1168	37.9	2	1145	38.7	1147	38.6	<u>1145</u>	<u>38.7</u>
471.omnetpp	2	747	16.7	745	16.8	<u>745</u>	<u>16.8</u>	2	<u>692</u>	<u>18.1</u>	691	18.1	692	18.1
473.astar	2	<u>910</u>	<u>15.4</u>	910	15.4	910	15.4	2	911	15.4	<u>911</u>	<u>15.4</u>	911	15.4
483.xalancbmk	2	<u>517</u>	<u>26.7</u>	516	26.7	517	26.7	2	511	27.0	511	27.0	<u>511</u>	<u>27.0</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

## Peak Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Peak Optimization Flags

C benchmarks:

-Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

C++ benchmarks:

-Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx\_features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags.20090714.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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