SPEC® CINT2006 Result

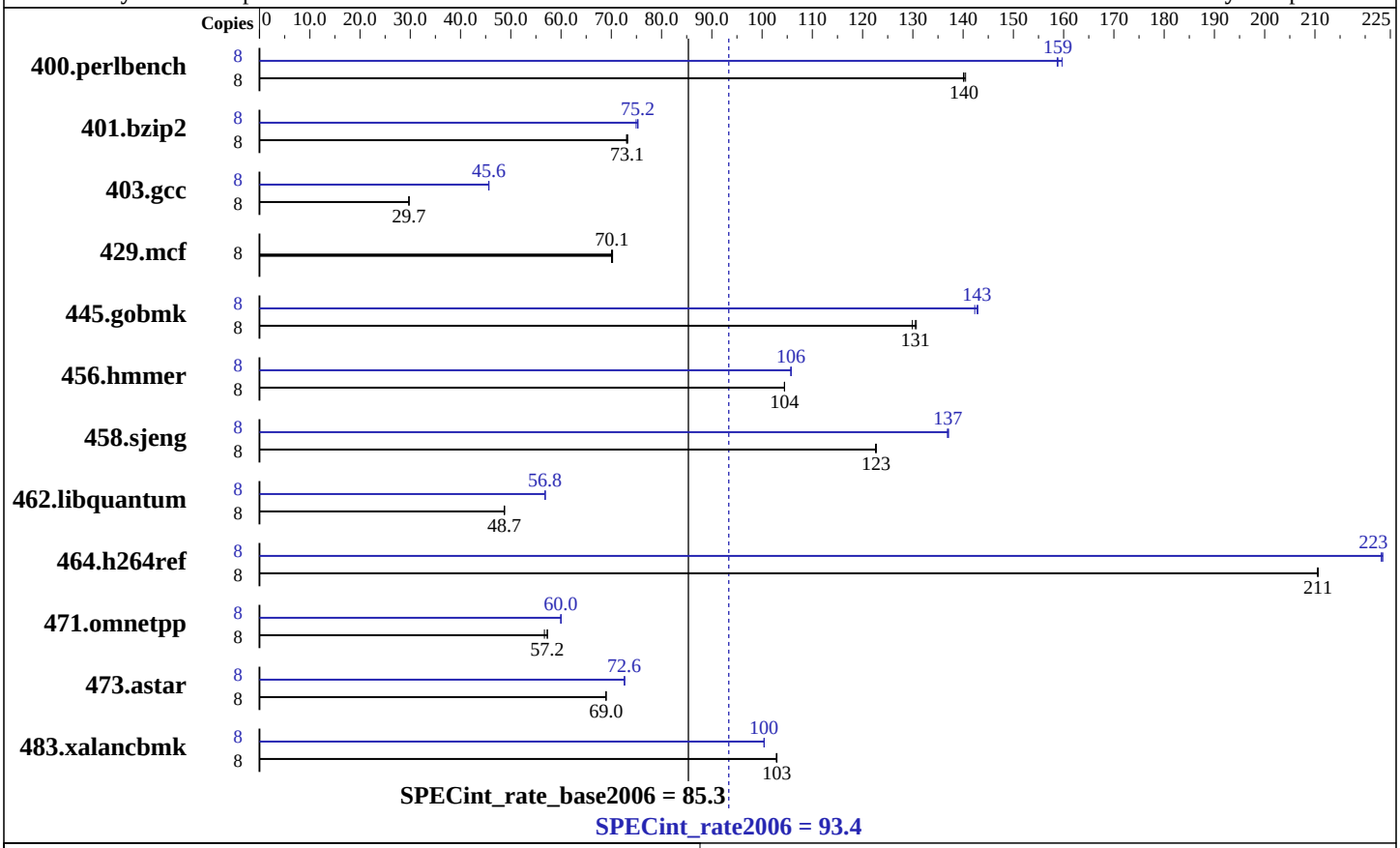
Supermicro Motherboard X7DBT-INF

SPECint®_rate2006 = 93.4

SPECint_rate_base2006 = 85.3

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2007
Hardware Availability: May-2007
Software Availability: Apr-2007



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66GHz 1333MHz System Bus
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 X 2GB ECC PC2-5300, CL5, FBDIMM)
Disk Subsystem:	250GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition W/ SP1
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

SPEC CINT2006 Result

Supermicro Motherboard X7DBT-INF

SPECint_rate2006 = 93.4

SPECint_rate_base2006 = 85.3

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2007
Hardware Availability: May-2007
Software Availability: Apr-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**558**	**140**	558	140	556	140	8	**492**	**159**	492	159	489	160
401.bzip2	8	**1056**	**73.1**	1057	73.0	1054	73.3	8	1030	74.9	1025	75.3	**1026**	**75.2**
403.gcc	8	**2165**	**29.7**	2163	29.8	2167	29.7	8	**1411**	**45.6**	1413	45.6	1410	45.7
429.mcf	8	1042	70.0	1038	70.3	**1040**	**70.1**	8	1042	70.0	1038	70.3	**1040**	**70.1**
445.gobmk	8	**643**	**131**	646	130	642	131	8	**588**	**143**	587	143	590	142
456.hmmer	8	**715**	**104**	715	104	714	104	8	705	106	706	106	**706**	**106**
458.sjeng	8	789	123	**789**	**123**	790	123	8	707	137	**707**	**137**	706	137
462.libquantum	8	3401	48.7	**3401**	**48.7**	3402	48.7	8	2917	56.8	**2917**	**56.8**	2916	56.8
464.h264ref	8	**841**	**211**	841	210	841	211	8	793	223	**793**	**223**	792	224
471.omnetpp	8	873	57.3	882	56.7	**874**	**57.2**	8	833	60.0	**834**	**60.0**	834	60.0
473.astar	8	815	68.9	**814**	**69.0**	814	69.0	8	774	72.6	**774**	**72.6**	773	72.7
483.xalancbmk	8	**536**	**103**	537	103	536	103	8	550	100	550	100	**550**	**100**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with CSE-808TQ-980 case,
Product description located as of http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DBT-INF.cfm
The system bus runs at 1333 MHz
"start /b /wait /affinity" used to bind processes to CPUs.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib                -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
-link /FORCE:MULTIPLE

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec_div- -Qansi-alias /F512000000
-link /FORCE:MULTIPLE

Continued on next page

Peak Optimization Flags (Continued)

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
-Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 13:26:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 July 2007.