



SPEC® CFP2006 Result

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Acer Incorporated

SPECfp®_rate2006 = 60.5

Acer Altos R720 (Intel Xeon X5355)

SPECfp_rate_base2006 = 58.8

CPU2006 license: 97

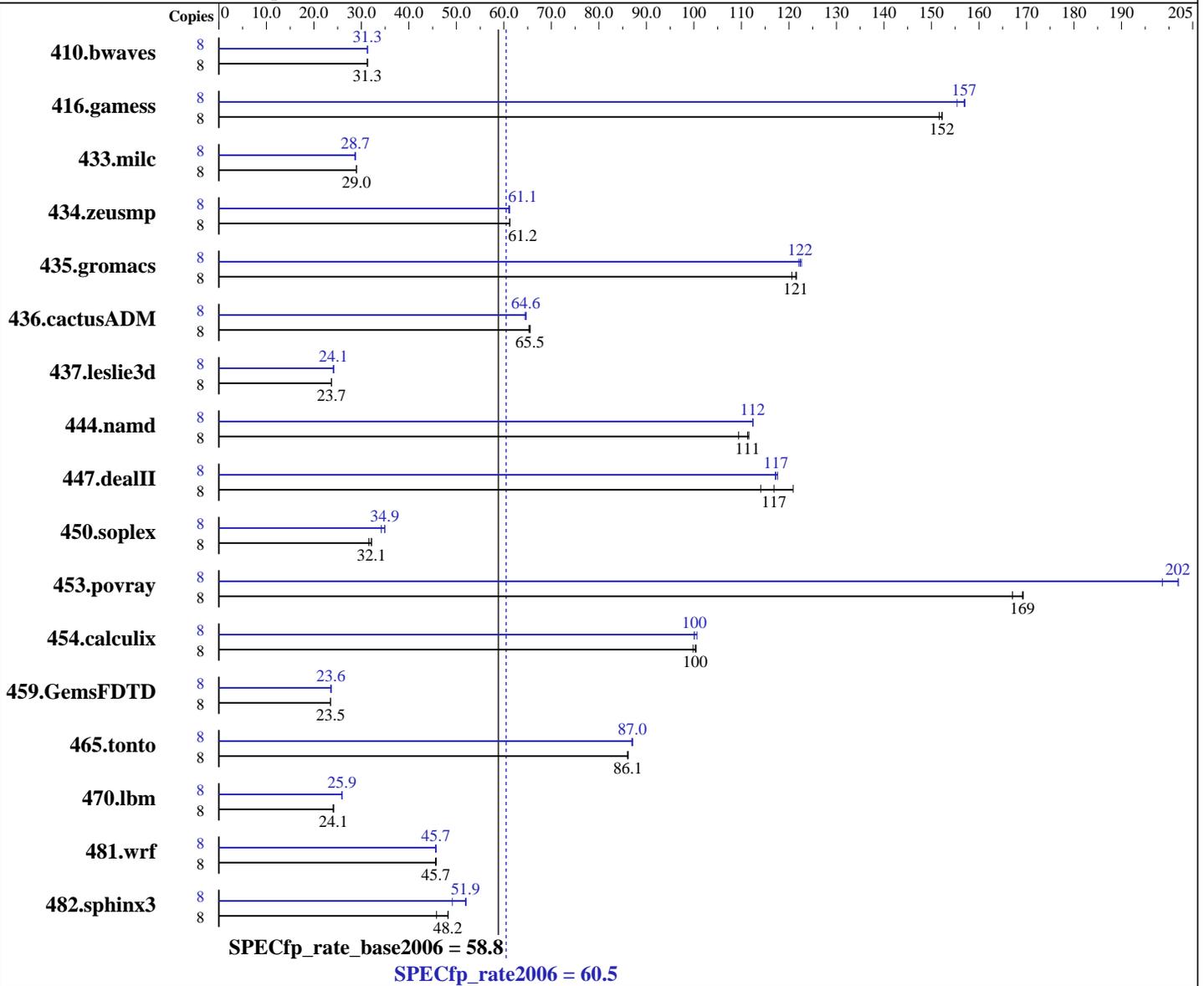
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jun-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2660
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 EM64T (kernel 2.6.16.21-0.8-smp)
 Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023 Intel Fortran Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_fc_p_10.0.023
 Auto Parallel: No
 File System: ReiserFS
 System State: Default

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3478	31.3	3478	31.3	3477	31.3	8	3477	31.3	3477	31.3	3478	31.3
416.gamess	8	1029	152	1029	152	1033	152	8	998	157	1008	155	997	157
433.milc	8	2537	28.9	2535	29.0	2535	29.0	8	2551	28.8	2563	28.7	2557	28.7
434.zeusmp	8	1189	61.2	1189	61.2	1190	61.2	8	1193	61.0	1189	61.2	1192	61.1
435.gromacs	8	474	121	470	121	470	122	8	466	123	467	122	468	122
436.cactusADM	8	1464	65.3	1460	65.5	1460	65.5	8	1479	64.6	1483	64.4	1478	64.7
437.leslie3d	8	3176	23.7	3172	23.7	3172	23.7	8	3115	24.1	3111	24.2	3118	24.1
444.namd	8	576	111	586	109	575	112	8	571	112	571	112	571	112
447.dealII	8	802	114	783	117	757	121	8	780	117	781	117	778	118
450.soplex	8	2112	31.6	2075	32.1	2077	32.1	8	1951	34.2	1909	35.0	1911	34.9
453.povray	8	252	169	251	169	255	167	8	214	199	211	202	211	202
454.calculix	8	657	100	658	100	661	99.8	8	660	100	656	101	660	100
459.GemsFDTD	8	3616	23.5	3608	23.5	3610	23.5	8	3597	23.6	3596	23.6	3593	23.6
465.tonto	8	915	86.0	914	86.2	914	86.1	8	905	87.0	904	87.1	906	86.9
470.lbm	8	4560	24.1	4559	24.1	4559	24.1	8	4241	25.9	4239	25.9	4242	25.9
481.wrf	8	1954	45.7	1960	45.6	1956	45.7	8	1959	45.6	1954	45.7	1956	45.7
482.sphinx3	8	3403	45.8	3230	48.3	3235	48.2	8	3173	49.1	2996	52.0	3004	51.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All programs compiled with the 64-bit compiler except 437.leslie3d, 450.soplex, 470.lbm and 482.sphinx3, at peak, compiled with the 32-bit compiler
This result was measured on the Acer Altos R520
The Altos R720 and Altos R520 are electronically equivalent.

taskset was used to bind copies to cores

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast -auto-ilp32

C++ benchmarks:
-fast -auto-ilp32

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:

435.gromacs: -fast -auto-ilp32 -nofor_main(*)

436.cactusADM: Same as 435.gromacs

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Base Optimization Flags (Continued)

454.calculix: Same as 435.gromacs

481.wrf: -fast -auto-ilp32

(*) Indicates an optimization flag that was found in a portability variable.

Peak Compiler Invocation

C benchmarks (except as noted below):

/opt/intel/cc/10.0.023/bin/icc -L/opt/intel/cc/10.0.023/lib
-I/opt/intel/cc/10.0.023/include

433.milc: icc

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/cc/10.0.023/bin/icpc -L/opt/intel/cc/10.0.023/lib
-I/opt/intel/cc/10.0.023/include

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: /opt/intel/fc/10.0.023/bin/ifort -L/opt/intel/fc/10.0.023/lib
-I/opt/intel/fc/10.0.023/include

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep- -prefetch

482.sphinx3: -fast -unroll2

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xT -ipo -O3
-no-prec-div

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: Same as 434.zeusmp

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -auto-ilp32
-nofor_main(*)

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-auto-ilp32 -nofor_main(*)

454.calculix: -fast -auto-ilp32 -nofor_main(*)

481.wrf: -fast -auto-ilp32

(*) Indicates an optimization flag that was found in a portability variable.

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.16.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.16.xml>



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For other inquiries, please contact webmaster@spec.org.

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