SPEC® CINT2006 Result
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IBM Corporation

IBM System x3650 (Intel Xeon 5140)

SPECint®2006 =   15.4
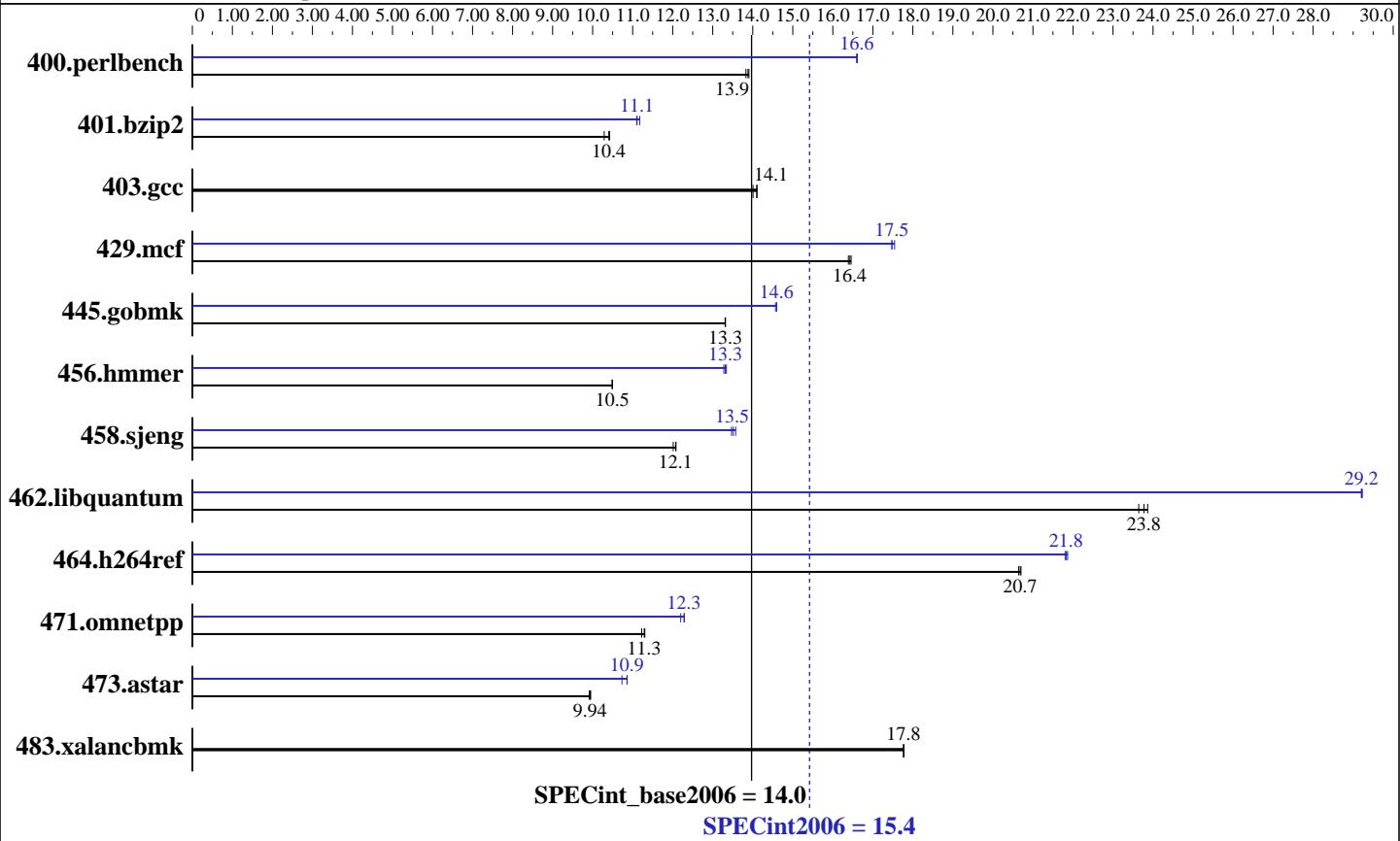
SPECint_base2006 =   14.0

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jul-2007
Hardware Availability: Jul-2006
Software Availability: Jul-2007

	400.perlbench	16.6 / 13.9
	401.bzip2	11.1 / 10.4
	403.gcc	14.1
	429.mcf	17.5 / 16.4
	445.gobmk	14.6 / 13.3
	456.hmmer	13.3 / 10.5
	458.sjeng	13.5 / 12.1
	462.libquantum	29.2 / 23.8
	464.h264ref	21.8 / 20.7
	471.omnetpp	12.3 / 11.3
	473.astar	10.9 / 9.94
	483.xalancbmk	17.8

SPECint_base2006 = 14.0

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Hardware

CPU Name:	Intel Xeon 5140
CPU Characteristics:	1333MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem:	1 x 36 GB SAS, 15000 RPM
Other Hardware:	None

Software

Operating System:	SLES 10 (x86_64), 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Linux version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap 8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**704**	**13.9**	706	13.8	703	13.9	588	16.6	589	16.6	**588**	**16.6**
401.bzip2	938	10.3	**928**	**10.4**	926	10.4	**868**	**11.1**	869	11.1	863	11.2
403.gcc	571	14.1	575	14.0	**571**	**14.1**	571	14.1	575	14.0	**571**	**14.1**
429.mcf	554	16.5	556	16.4	**555**	**16.4**	522	17.5	520	17.5	**522**	**17.5**
445.gobmk	**788**	**13.3**	788	13.3	788	13.3	720	14.6	**719**	**14.6**	719	14.6
456.hmmer	889	10.5	**889**	**10.5**	890	10.5	703	13.3	**701**	**13.3**	699	13.3
458.sjeng	1001	12.1	1007	12.0	**1002**	**12.1**	**895**	**13.5**	898	13.5	891	13.6
462.libquantum	868	23.9	876	23.6	**872**	**23.8**	709	29.2	**709**	**29.2**	709	29.2
464.h264ref	1069	20.7	1072	20.6	**1070**	**20.7**	**1014**	**21.8**	1012	21.9	1015	21.8
471.omnetpp	553	11.3	557	11.2	**554**	**11.3**	509	12.3	512	12.2	**509**	**12.3**
473.astar	708	9.91	**706**	**9.94**	705	9.95	**647**	**10.9**	654	10.7	646	10.9
483.xalancbmk	**388**	**17.8**	388	17.8	388	17.8	**388**	**17.8**	388	17.8	388	17.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
   462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-fast
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap
```

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Base Other Flags

C benchmarks:

       403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
     `icc`

      401.bzip2: `/opt/intel/cce/10.0.023/bin/icc`
              `-L/opt/intel/cce/10.0.023/lib`
              `-I/opt/intel/cce/10.0.023/include`

     456.hmmer: `/opt/intel/cce/10.0.023/bin/icc`
              `-L/opt/intel/cce/10.0.023/lib`
              `-I/opt/intel/cce/10.0.023/include`

C++ benchmarks:
     `icpc`

Peak Portability Flags

    400.perlbench: `-DSPEC_CPU_LINUX_IA32`
         401.bzip2: `-DSPEC_CPU_LP64`
        456.hmmer: `-DSPEC_CPU_LP64`
   462.libquantum: `-DSPEC_CPU_LINUX`
   483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias`
              `-prefetch`

       401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast`

        403.gcc: `basepeak = yes`

        429.mcf: `-fast -prefetch`

      445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo`
              `-no-prec_div -ansi-alias`

IBM Corporation	SPECint2006 =	15.4
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CPU2006 license: 11　　　　　　　　　　　　　　　　　　　　　　　　　**Test date:** Jul-2007
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Peak Optimization Flags (Continued)

456.hmmer: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2`
`-ansi-alias`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

462.libquantum: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0`
`-prefetch -opt-streaming-stores always`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo`
`-no-prec_div -ansi-alias -Wl,-z,muldefs`
`-L/spec/cpu2006.1.0/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.44.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.44.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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