



# SPEC® CINT2006 Result

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Acer Incorporated

**SPECint\_rate2006 = 47.0**

Acer Altos G540 (Intel Xeon E5335)

**SPECint\_rate\_base2006 = 43.4**

CPU2006 license: 97

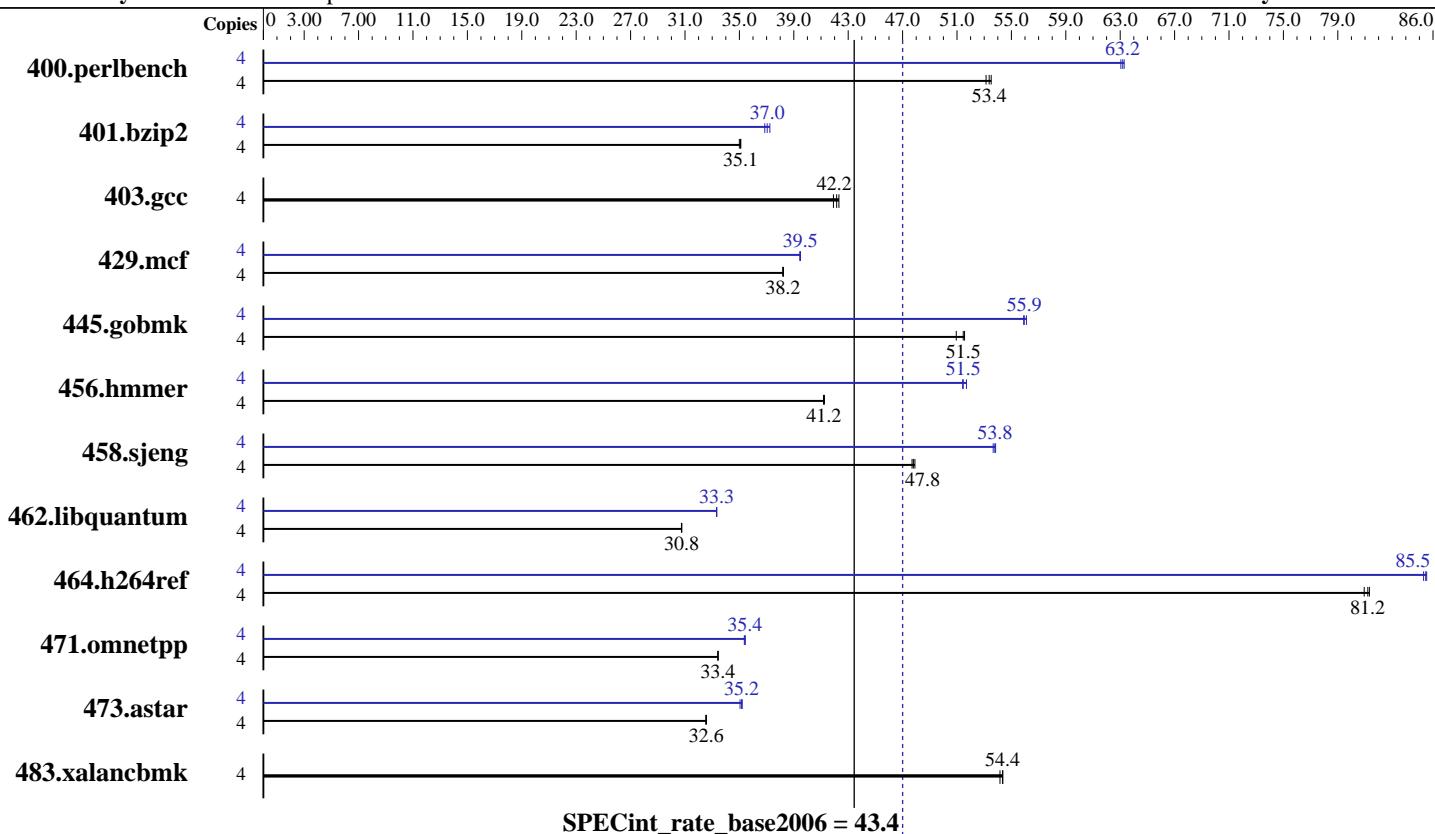
Test date: Jul-2007

Test sponsor: Acer Incorporated

Hardware Availability: Nov-2006

Tested by: Acer Incorporated

Software Availability: Jun-2007



## Hardware

CPU Name: Intel Xeon E5335  
CPU Characteristics: 1333MHz system bus  
CPU MHz: 2000  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
CPU(s) orderable: 1, 2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
L3 Cache: None  
Other Cache: None  
Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)  
Disk Subsystem: 1 x 73 GB 10000RPM SAS HDD  
Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 EM64T (kernel 2.6.16.21-0.8-smp)  
Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l\_cc\_p\_10.0.023  
Auto Parallel: No  
File System: ReiserFS  
System State: Multi-user, run level 3  
Base Pointers: 32-bit  
Peak Pointers: 32/64-bit  
Other Software: MicroQuill SmartHeap Library 8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>732</b>	<b>53.4</b>	735	53.1	730	53.5	4	620	63.0	617	63.3	<b>619</b>	<b>63.2</b>
401.bzip2	4	1103	35.0	<b>1100</b>	<b>35.1</b>	1100	35.1	4	1037	37.2	1047	36.9	<b>1042</b>	<b>37.0</b>
403.gcc	4	<b>764</b>	<b>42.2</b>	761	42.3	768	41.9	4	<b>764</b>	<b>42.2</b>	761	42.3	768	41.9
429.mcf	4	<b>955</b>	<b>38.2</b>	954	38.2	955	38.2	4	925	39.4	924	39.5	<b>924</b>	<b>39.5</b>
445.gobmk	4	824	50.9	814	51.6	<b>815</b>	<b>51.5</b>	4	751	55.9	<b>750</b>	<b>55.9</b>	748	56.1
456.hammer	4	906	41.2	905	41.3	<b>905</b>	<b>41.2</b>	4	<b>725</b>	<b>51.5</b>	722	51.7	726	51.4
458.sjeng	4	<b>1013</b>	<b>47.8</b>	1015	47.7	1010	47.9	4	899	53.8	902	53.7	<b>900</b>	<b>53.8</b>
462.libquantum	4	2696	30.7	2695	30.8	<b>2695</b>	<b>30.8</b>	4	<b>2487</b>	<b>33.3</b>	2487	33.3	2486	33.3
464.h264ref	4	1088	81.3	1094	80.9	<b>1090</b>	<b>81.2</b>	4	1035	85.5	<b>1036</b>	<b>85.5</b>	1038	85.3
471.omnetpp	4	748	33.4	<b>748</b>	<b>33.4</b>	748	33.4	4	706	35.4	<b>706</b>	<b>35.4</b>	707	35.4
473.astar	4	<b>862</b>	<b>32.6</b>	863	32.5	862	32.6	4	798	35.2	<b>799</b>	<b>35.2</b>	801	35.0
483.xalancbmk	4	<b>508</b>	<b>54.4</b>	508	54.4	510	54.2	4	<b>508</b>	<b>54.4</b>	508	54.4	510	54.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

taskset was used to bind copies to cores

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/cce/10.0.023/bin/icc  
           -L/opt/intel/cce/10.0.023/lib  
           -I/opt/intel/cce/10.0.023/include
```

```
456.hmmr: /opt/intel/cce/10.0.023/bin/icc  
           -L/opt/intel/cce/10.0.023/lib  
           -I/opt/intel/cce/10.0.023/include
```

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmr: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
               -prefetch
```

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## Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec\_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Obo  
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec\_div -ansi-alias -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.16.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.16.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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