



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint®_rate2006 = 47.1

IBM BladeCenter HS21 (Intel Xeon 5120)

SPECint_rate_base2006 = 41.3

CPU2006 license: 11

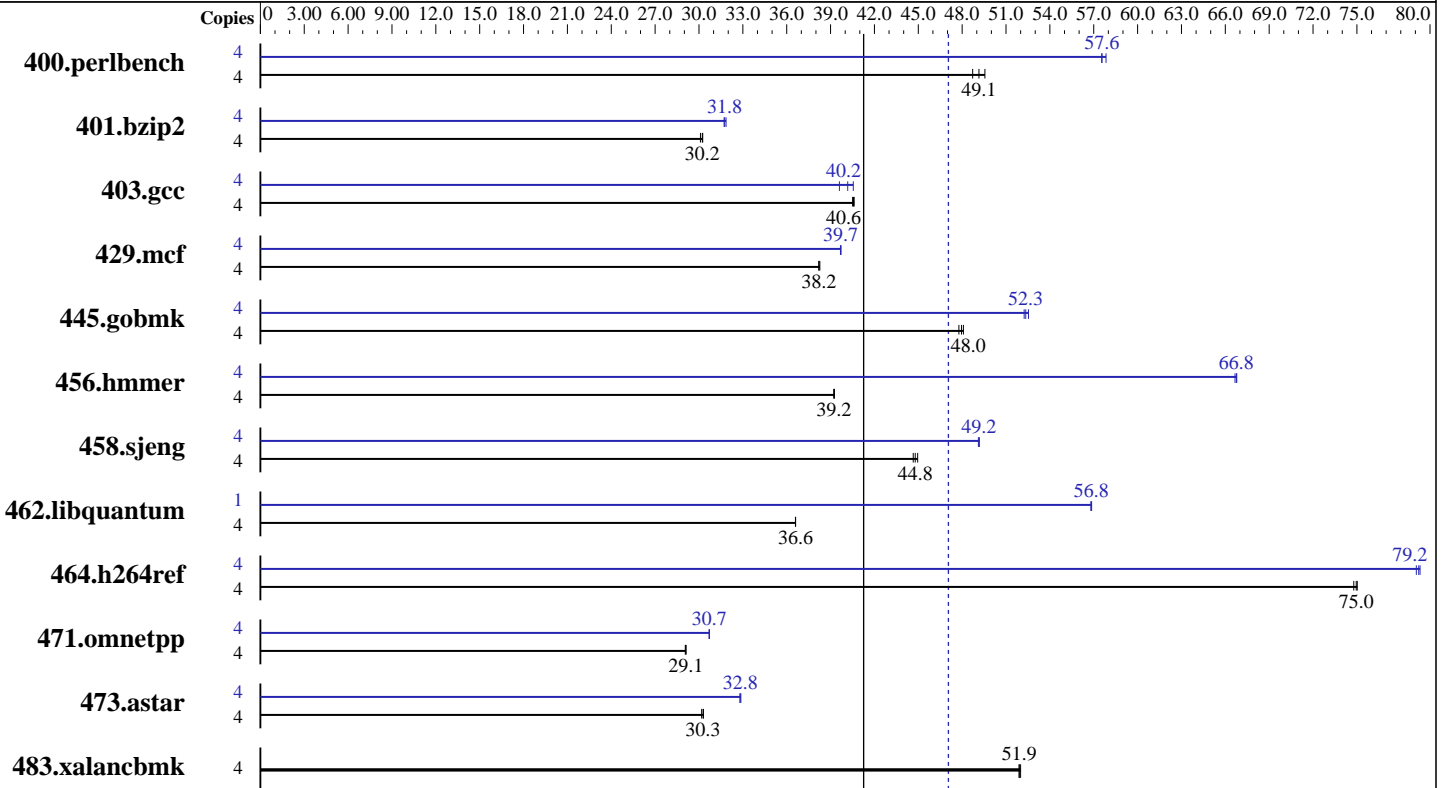
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Aug-2007

Hardware Availability: Jul-2006

Software Availability: Nov-2007



SPECint_rate_base2006 = 41.3

SPECint_rate2006 = 47.1

Hardware

CPU Name: Intel Xeon 5120
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 36 GB SAS, 10000 RPM
 Other Hardware: Memory and I/O Expansion Unit (P/N 42C1600)

Software

Operating System: SLES 10 (x86_64), 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 47.1

IBM BladeCenter HS21 (Intel Xeon 5120)

SPECint_rate_base2006 = 41.3

CPU2006 license: 11

Test date: Aug-2007

Test sponsor: IBM Corporation

Hardware Availability: Jul-2006

Tested by: IBM Corporation

Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	789	49.6	802	48.7	795	49.1	4	679	57.5	676	57.8	679	57.6
401.bzip2	4	1276	30.3	1277	30.2	1282	30.1	4	1212	31.9	1215	31.8	1217	31.7
403.gcc	4	793	40.6	794	40.6	795	40.5	4	802	40.2	794	40.6	813	39.6
429.mcf	4	953	38.3	955	38.2	955	38.2	4	919	39.7	919	39.7	919	39.7
445.gobmk	4	878	47.8	875	48.0	873	48.1	4	799	52.5	802	52.3	803	52.2
456.hammer	4	951	39.3	951	39.2	951	39.2	4	559	66.8	560	66.7	559	66.8
458.sjeng	4	1081	44.8	1077	45.0	1084	44.7	4	986	49.1	984	49.2	985	49.2
462.libquantum	4	2264	36.6	2264	36.6	2265	36.6	1	364	56.9	365	56.8	365	56.8
464.h264ref	4	1181	75.0	1180	75.0	1184	74.8	4	1116	79.3	1120	79.1	1117	79.2
471.omnetpp	4	859	29.1	859	29.1	860	29.1	4	814	30.7	814	30.7	814	30.7
473.astar	4	927	30.3	927	30.3	930	30.2	4	854	32.9	856	32.8	856	32.8
483.xalancbmk	4	531	52.0	532	51.9	532	51.9	4	531	52.0	532	51.9	532	51.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs

-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 47.1

IBM BladeCenter HS21 (Intel Xeon 5120)

SPECint_rate_base2006 = 41.3

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Aug-2007

Hardware Availability: Jul-2006

Software Availability: Nov-2007

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbenc: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbenc: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 47.1

IBM BladeCenter HS21 (Intel Xeon 5120)

SPECint_rate_base2006 = 41.3

CPU2006 license: 11

Test date: Aug-2007

Test sponsor: IBM Corporation

Hardware Availability: Jul-2006

Tested by: IBM Corporation

Software Availability: Nov-2007

Peak Optimization Flags (Continued)

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.46.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.46.xml>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 47.1

IBM BladeCenter HS21 (Intel Xeon 5120)

SPECint_rate_base2006 = 41.3

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Aug-2007

Hardware Availability: Jul-2006

Software Availability: Nov-2007

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 13:28:10 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 September 2007.