



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint®_rate2006 = 27.7

ProLiant DL380 G5
(2.0 GHz, Intel Xeon processor 5130)

SPECint_rate_base2006 = 24.4

CPU2006 license: 3

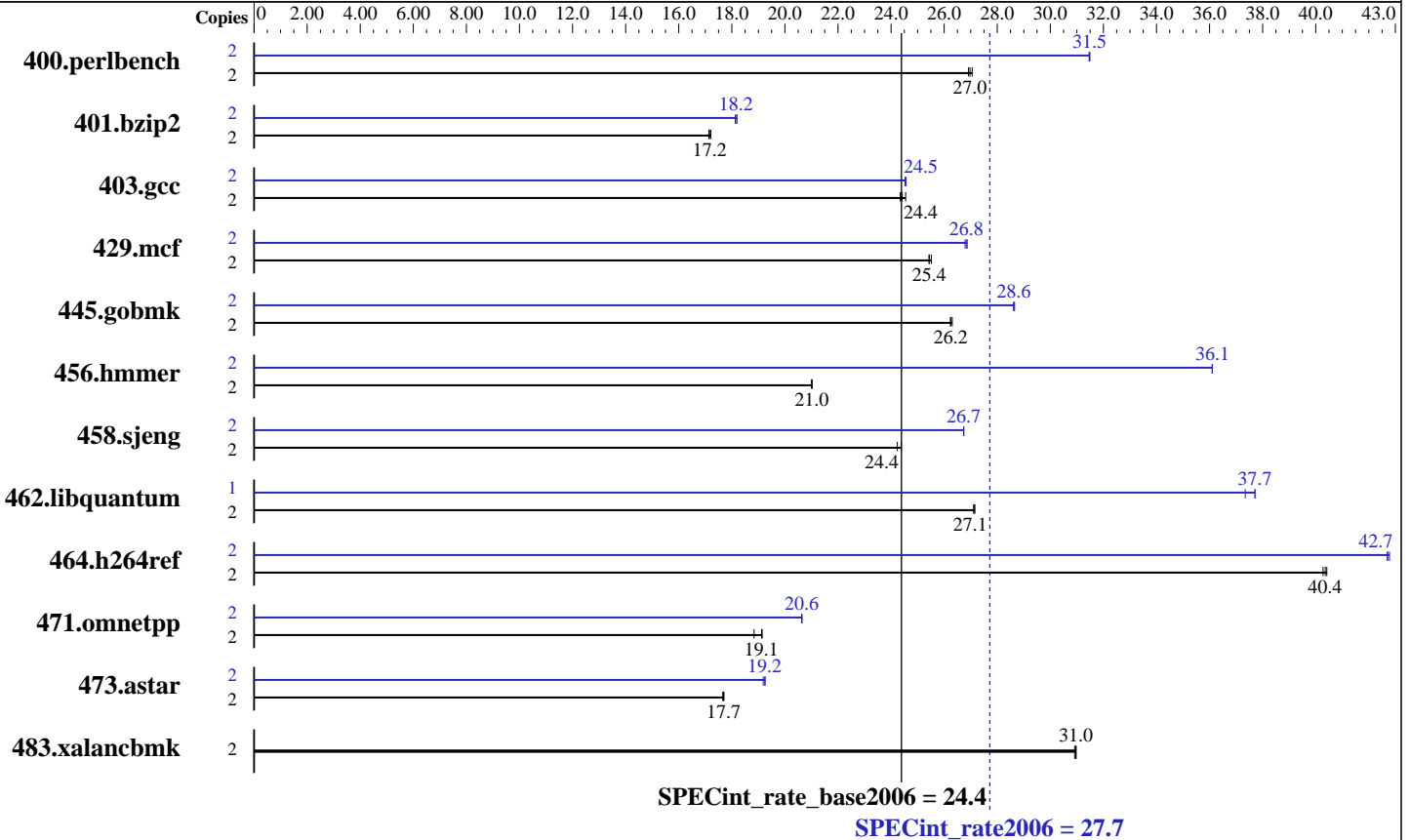
Test date: Sep-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon 5130
 CPU Characteristics: 2.0 GHz, 4 MB L2 shared, 1333 MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 or 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 10 K SAS
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1
 kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64
 version 10.1
 Build 20070725
 Auto Parallel: Yes
 File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1
 binutils-2.17.50



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 27.7

ProLiant DL380 G5
(2.0 GHz, Intel Xeon processor 5130)

SPECint_rate_base2006 = 24.4

CPU2006 license: 3

Test date: Sep-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	722	27.1	726	26.9	724	27.0	2	621	31.5	621	31.5	620	31.5
401.bzip2	2	1125	17.2	1121	17.2	1126	17.1	2	1064	18.1	1063	18.2	1061	18.2
403.gcc	2	656	24.6	661	24.3	659	24.4	2	655	24.6	656	24.5	656	24.5
429.mcf	2	717	25.4	717	25.4	715	25.5	2	681	26.8	679	26.9	680	26.8
445.gobmk	2	799	26.2	798	26.3	800	26.2	2	733	28.6	732	28.7	733	28.6
456.hammer	2	888	21.0	888	21.0	888	21.0	2	517	36.1	517	36.1	517	36.1
458.sjeng	2	999	24.2	992	24.4	993	24.4	2	905	26.7	905	26.7	905	26.7
462.libquantum	2	1528	27.1	1526	27.2	1528	27.1	1	555	37.3	549	37.7	549	37.7
464.h264ref	2	1097	40.4	1095	40.4	1099	40.3	2	1037	42.7	1036	42.7	1035	42.8
471.omnetpp	2	653	19.1	663	18.8	654	19.1	2	605	20.6	606	20.6	606	20.6
473.astar	2	795	17.7	794	17.7	793	17.7	2	732	19.2	729	19.3	730	19.2
483.xalancbmk	2	446	31.0	446	31.0	446	30.9	2	446	31.0	446	31.0	446	30.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 27.7

ProLiant DL380 G5
(2.0 GHz, Intel Xeon processor 5130)

SPECint_rate_base2006 = 24.4

CPU2006 license: 3

Test date: Sep-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007

Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 27.7

ProLiant DL380 G5
(2.0 GHz, Intel Xeon processor 5130)

SPECint_rate_base2006 = 24.4

CPU2006 license: 3

Test date: Sep-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-alloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 27.7

ProLiant DL380 G5
(2.0 GHz, Intel Xeon processor 5130)

SPECint_rate_base2006 = 24.4

CPU2006 license: 3

Test date: Sep-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:53:53 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 16 October 2007.