



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®2006 = 17.2

ProLiant DL380 G5
(2.0 GHz, Intel Xeon processor 5130)

SPECint_base2006 = 14.6

CPU2006 license: 3

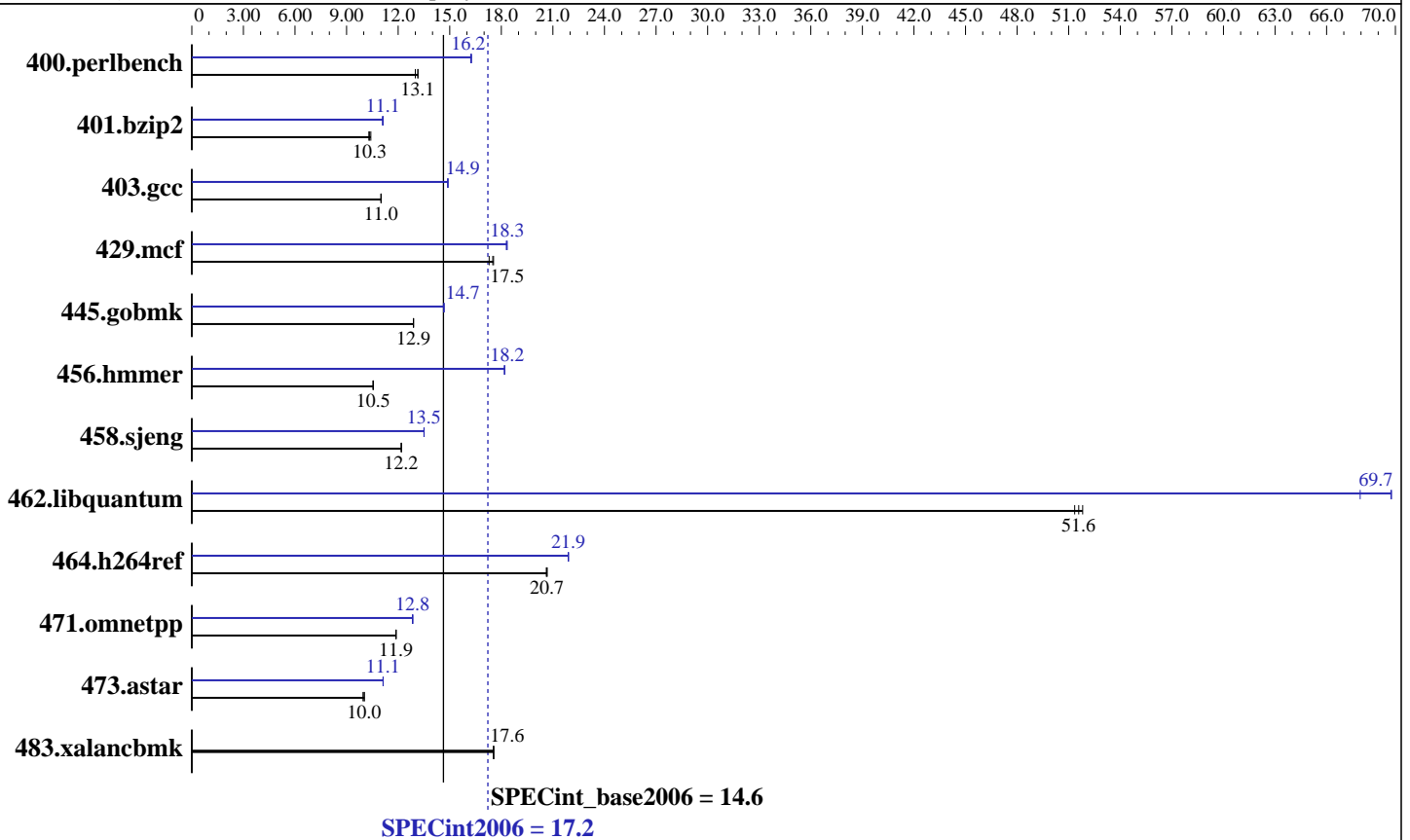
Test date: Sep-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon 5130
 CPU Characteristics: 2.0 GHz, 4 MB L2 shared, 1333 MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1 or 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 10 K SAS
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1
 kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64
 version 10.1
 Build 20070824
 Auto Parallel: Yes
 File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1
 binutils-2.17.50



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	743	13.2	743	13.1	752	13.0	602	16.2	601	16.3	602	16.2
401.bzip2	937	10.3	927	10.4	933	10.3	868	11.1	868	11.1	870	11.1
403.gcc	732	11.0	732	11.0	731	11.0	541	14.9	540	14.9	541	14.9
429.mcf	520	17.5	527	17.3	521	17.5	498	18.3	497	18.3	498	18.3
445.gobmk	814	12.9	813	12.9	814	12.9	715	14.7	715	14.7	716	14.7
456.hammer	884	10.6	885	10.5	885	10.5	513	18.2	513	18.2	513	18.2
458.sjeng	994	12.2	994	12.2	992	12.2	896	13.5	896	13.5	896	13.5
462.libquantum	404	51.3	400	51.8	402	51.6	297	69.7	297	69.8	305	68.0
464.h264ref	1071	20.7	1071	20.7	1073	20.6	1011	21.9	1010	21.9	1010	21.9
471.omnetpp	526	11.9	527	11.9	526	11.9	487	12.8	487	12.8	486	12.9
473.astar	701	10.0	705	9.95	699	10.0	631	11.1	631	11.1	631	11.1
483.xalancbmk	393	17.6	393	17.5	393	17.6	393	17.6	393	17.5	393	17.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -opt-malloc-options=3 -parallel
-par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmcr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

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