



# SPEC<sup>®</sup> CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY RX300 S4, Intel Xeon processor X5460, 3.16 GHz

### SPECint<sup>®</sup>\_rate2006 = Not Run

### SPECint\_rate\_base2006 = 113

CPU2006 license: 22

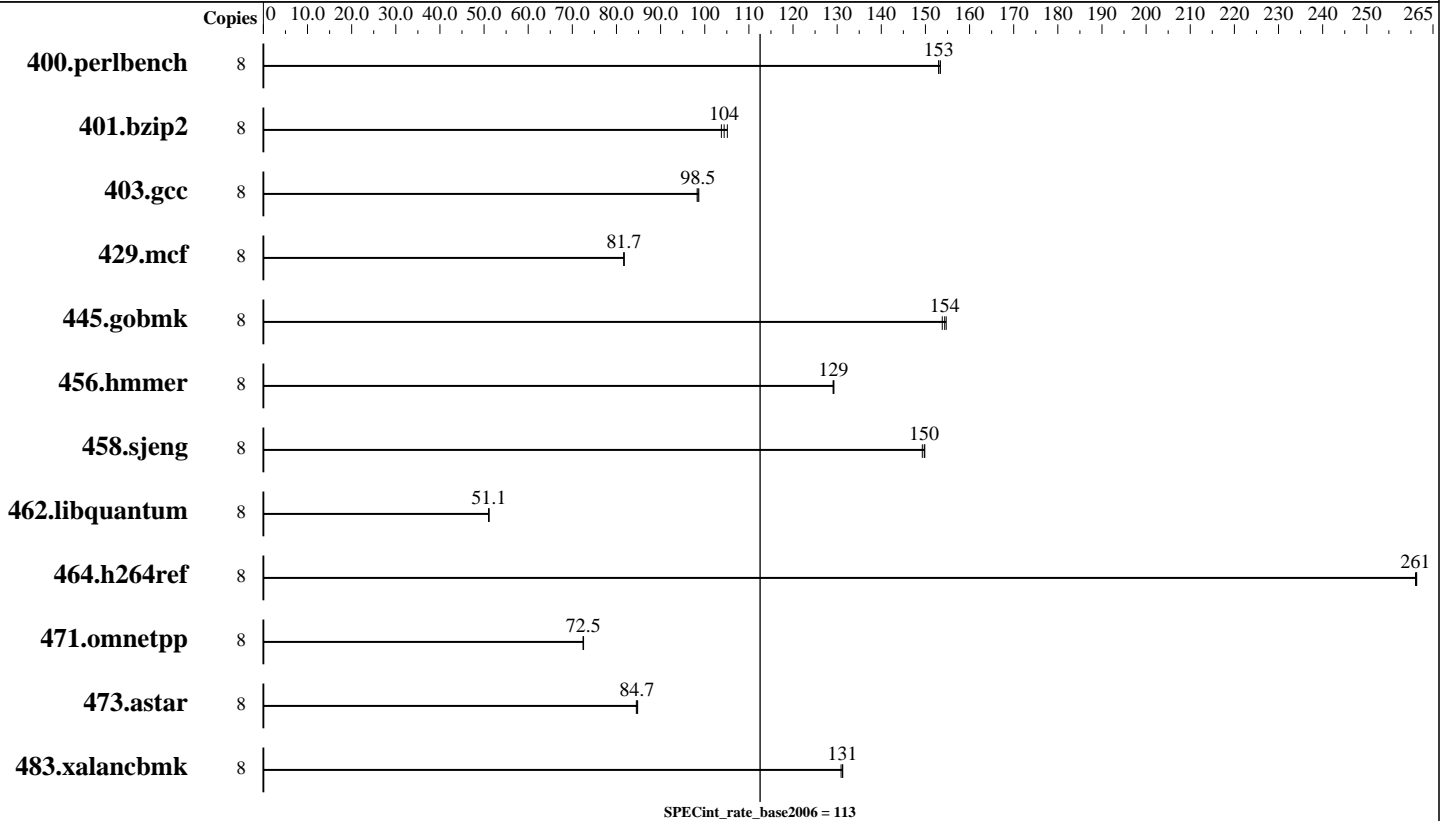
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Oct-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5460  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 3167  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Smart Heap Library, Version 8.1  
 binutils-2.17.tar.gz, Version 2.17



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b>511</b>	<b>153</b>	511	153	510	153							
401.bzip2	8	744	104	<b>739</b>	<b>104</b>	734	105							
403.gcc	8	653	98.7	<b>654</b>	<b>98.5</b>	655	98.3							
429.mcf	8	893	81.7	893	81.7	<b>893</b>	<b>81.7</b>							
445.gobmk	8	<b>544</b>	<b>154</b>	542	155	546	154							
456.hammer	8	577	129	578	129	<b>578</b>	<b>129</b>							
458.sjeng	8	<b>646</b>	<b>150</b>	648	149	646	150							
462.libquantum	8	3249	51.0	<b>3244</b>	<b>51.1</b>	3243	51.1							
464.h264ref	8	678	261	<b>678</b>	<b>261</b>	678	261							
471.omnetpp	8	690	72.5	<b>690</b>	<b>72.5</b>	689	72.5							
473.astar	8	665	84.5	662	84.8	<b>663</b>	<b>84.7</b>							
483.xalancbmk	8	422	131	<b>421</b>	<b>131</b>	421	131							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:

<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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