



# SPEC<sup>®</sup> CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY BX620 S4, Intel Xeon processor E5450,  
3.0 GHz

SPECint<sup>®</sup>\_rate2006 = Not Run

SPECint\_rate\_base2006 = 108

CPU2006 license: 22

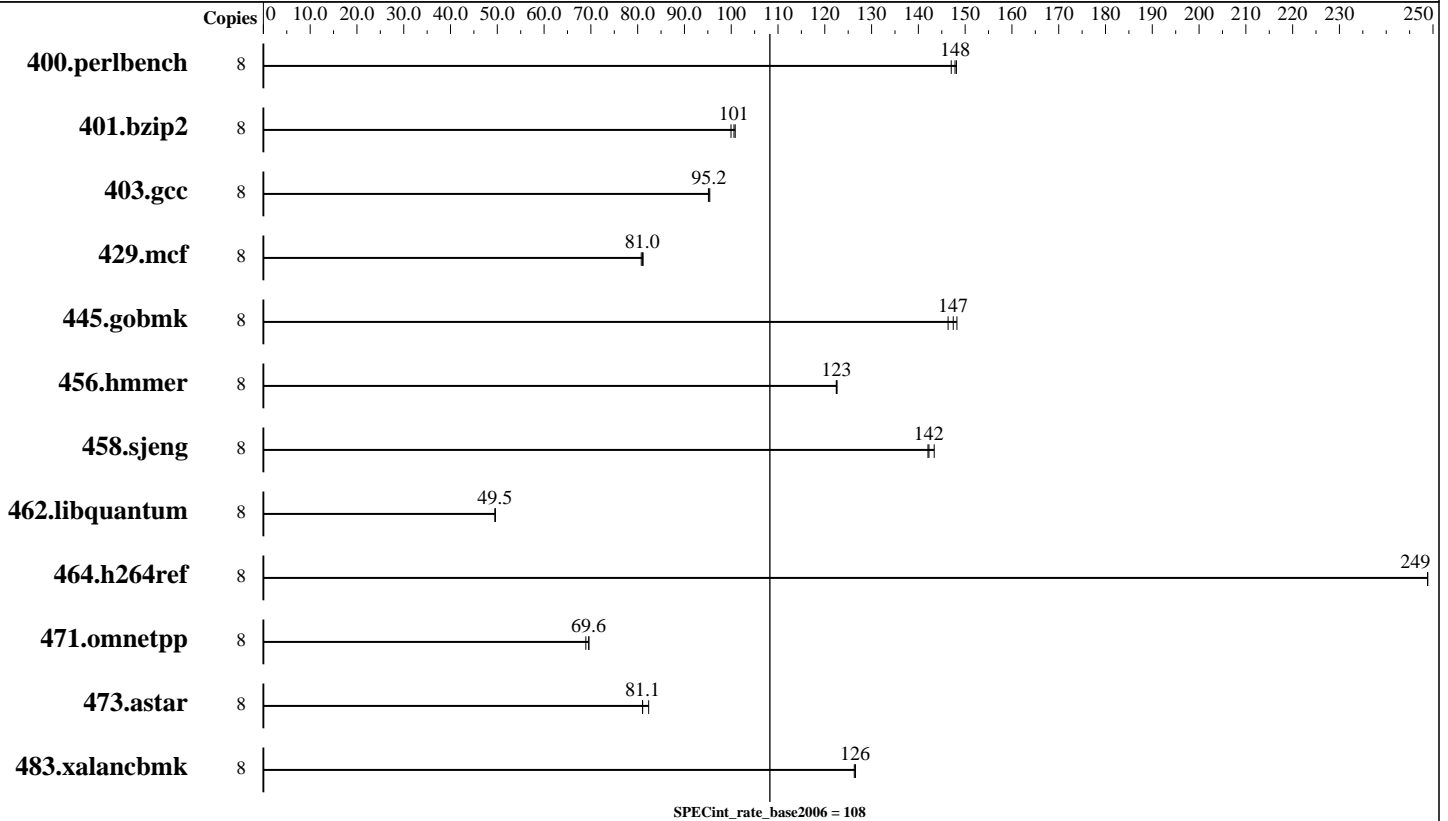
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5450  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Smart Heap Library, Version 8.1  
 binutils-2.17.tar.gz, Version 2.17



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<u>529</u>	<u>148</u>	528	148	532	147							
401.bzip2	8	<u>767</u>	<u>101</u>	765	101	772	100							
403.gcc	8	675	95.4	677	95.1	<b>676</b>	<b>95.2</b>							
429.mcf	8	899	81.2	<b>900</b>	<b>81.0</b>	903	80.8							
445.gobmk	8	<b>569</b>	<b>147</b>	566	148	573	146							
456.hammer	8	610	122	609	123	<b>609</b>	<b>123</b>							
458.sjeng	8	<b>680</b>	<b>142</b>	681	142	675	143							
462.libquantum	8	3344	49.6	<b>3346</b>	<b>49.5</b>	3346	49.5							
464.h264ref	8	711	249	711	249	<b>711</b>	<b>249</b>							
471.omnetpp	8	725	68.9	<b>719</b>	<b>69.6</b>	719	69.6							
473.astar	8	693	81.0	<b>693</b>	<b>81.1</b>	682	82.4							
483.xalancbmk	8	436	127	<b>436</b>	<b>126</b>	437	126							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:

<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-fast -inline-alloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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