



# SPEC® CINT2006 Result

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## Acer Incorporated

SPECint®\_rate2006 = 27.2

## Acer Altos R720 (Intel Xeon 5130)

SPECint\_rate\_base2006 = 24.0

CPU2006 license: 97

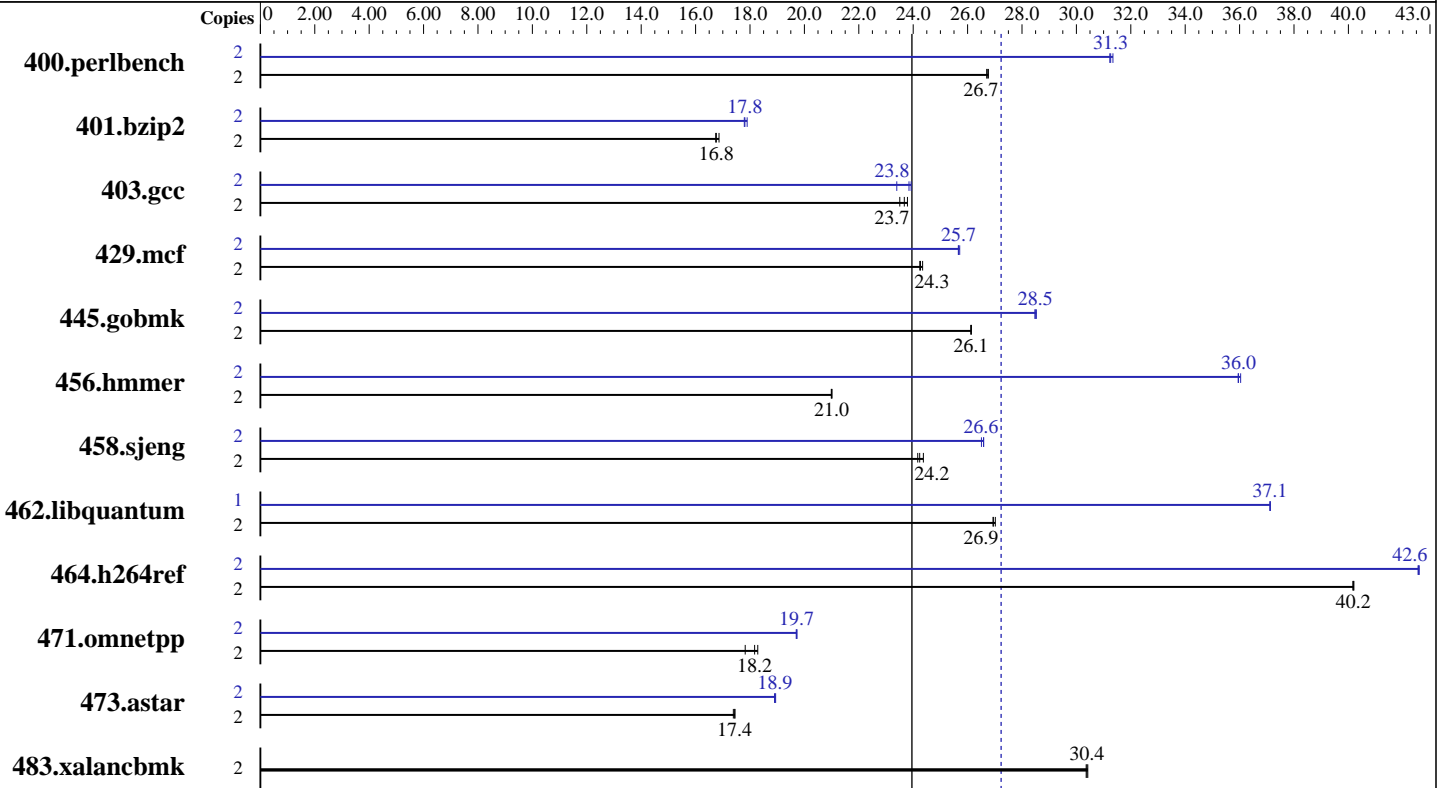
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Nov-2007

Hardware Availability: Nov-2006

Software Availability: Nov-2007



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### Hardware

CPU Name: Intel Xeon 5130  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)  
 Disk Subsystem: 1 x 73GB 10000PRM SAS HDD  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 SP1 EM64T (kernel 2.6.16.46-0.12-smp)  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	730	26.8	732	26.7	<b><u>731</u></b>	<b><u>26.7</u></b>	2	623	31.3	626	31.2	<b><u>625</u></b>	<b><u>31.3</u></b>
401.bzip2	2	1145	16.9	1153	16.7	<b><u>1151</u></b>	<b><u>16.8</u></b>	2	1078	17.9	1085	17.8	<b><u>1084</u></b>	<b><u>17.8</u></b>
403.gcc	2	<b><u>680</u></b>	<b><u>23.7</u></b>	677	23.8	685	23.5	2	688	23.4	<b><u>675</u></b>	<b><u>23.8</u></b>	673	23.9
429.mcf	2	752	24.2	<b><u>752</u></b>	<b><u>24.3</u></b>	749	24.3	2	709	25.7	711	25.7	<b><u>711</u></b>	<b><u>25.7</u></b>
445.gobmk	2	803	26.1	<b><u>803</u></b>	<b><u>26.1</u></b>	803	26.1	2	737	28.5	735	28.5	<b><u>736</u></b>	<b><u>28.5</u></b>
456.hammer	2	888	21.0	<b><u>888</u></b>	<b><u>21.0</u></b>	889	21.0	2	<b><u>519</u></b>	<b><u>36.0</u></b>	518	36.0	519	35.9
458.sjeng	2	<b><u>998</u></b>	<b><u>24.2</u></b>	993	24.4	1001	24.2	2	910	26.6	913	26.5	<b><u>910</u></b>	<b><u>26.6</u></b>
462.libquantum	2	1538	26.9	1534	27.0	<b><u>1538</u></b>	<b><u>26.9</u></b>	1	558	37.1	<b><u>558</u></b>	<b><u>37.1</u></b>	558	37.1
464.h264ref	2	<b><u>1101</u></b>	<b><u>40.2</u></b>	1102	40.2	1101	40.2	2	<b><u>1039</u></b>	<b><u>42.6</u></b>	1039	42.6	1040	42.6
471.omnetpp	2	684	18.3	701	17.8	<b><u>688</u></b>	<b><u>18.2</u></b>	2	635	19.7	634	19.7	<b><u>634</u></b>	<b><u>19.7</u></b>
473.astar	2	807	17.4	<b><u>805</u></b>	<b><u>17.4</u></b>	805	17.4	2	741	18.9	<b><u>742</u></b>	<b><u>18.9</u></b>	743	18.9
483.xalancbmk	2	454	30.4	455	30.4	<b><u>454</u></b>	<b><u>30.4</u></b>	2	454	30.4	455	30.4	<b><u>454</u></b>	<b><u>30.4</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

This result was measured on the Acer Altos R520.  
The Altos R720 and Altos R520 are electronically equivalent.

### BIOS Settings

Hardware Prefetcher set to ON

Adjacent Cache Line Prefetch set to OFF

Optimization for high-frequency FSB applications set to ON

All results used taskset to bind processes to cores.

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmr: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmr: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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