



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®2006 = 27.4

ProLiant BL460c
(3.33 GHz, Intel Xeon processor X5260)

SPECint_base2006 = 22.6

CPU2006 license: 3

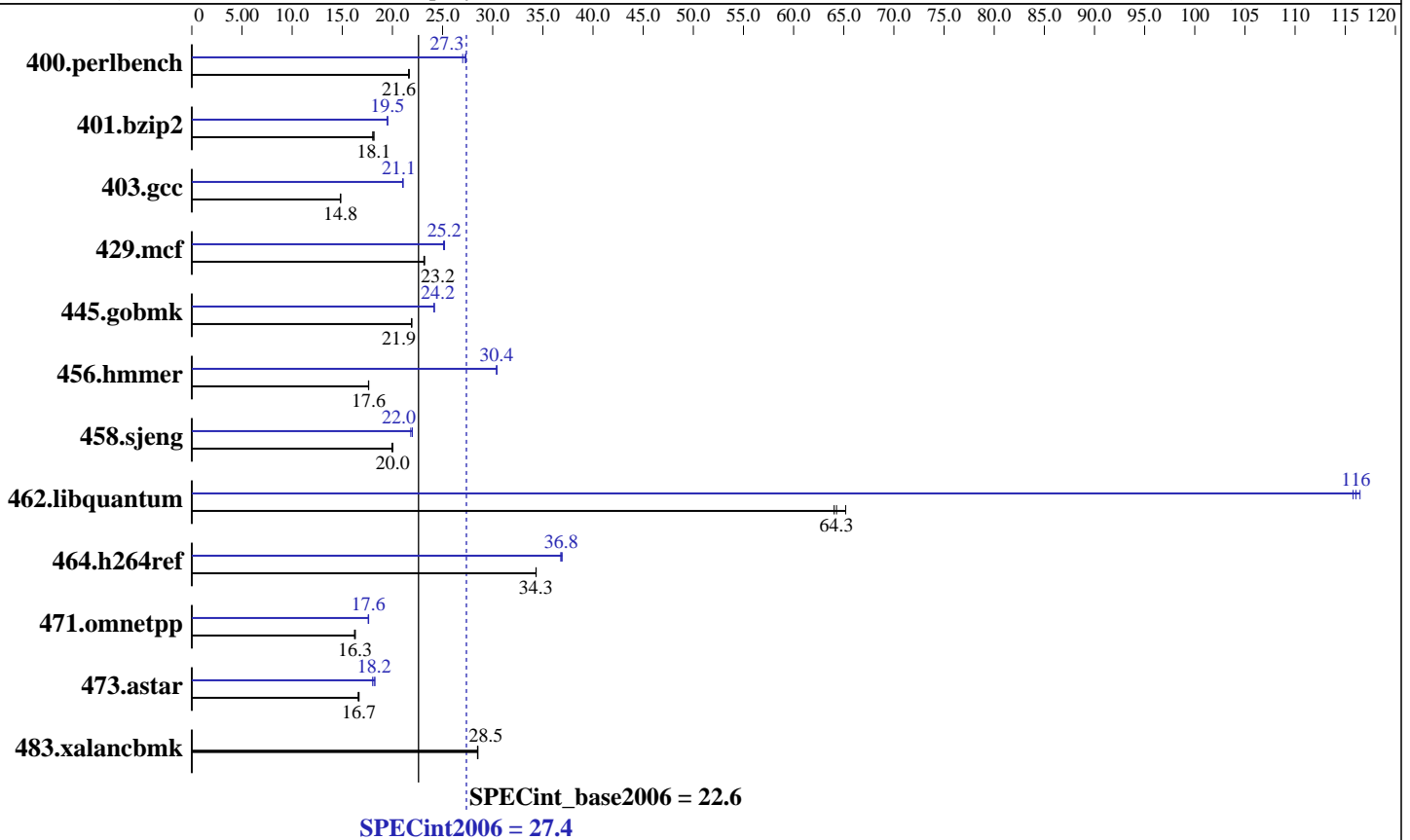
Test date: Dec-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5260
 CPU Characteristics: 3.33 GHz, 6 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 15 K SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1
 Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for applications running on IA-32 and Intel 64, Version 10.1
 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1
 binutils-2.17.50



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|------------|-------------|------------|-------------|------------|-------------|------------|-------------|------------|-------------|------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | <u>452</u> | <u>21.6</u> | 452 | 21.6 | 451 | 21.7 | <u>358</u> | <u>27.3</u> | 362 | 27.0 | 358 | 27.3 |
| 401.bzip2 | 535 | 18.0 | <u>532</u> | <u>18.1</u> | 532 | 18.2 | <u>495</u> | <u>19.5</u> | 495 | 19.5 | 494 | 19.5 |
| 403.gcc | 543 | 14.8 | <u>543</u> | <u>14.8</u> | 543 | 14.8 | 382 | 21.1 | 383 | 21.0 | <u>382</u> | <u>21.1</u> |
| 429.mcf | 393 | 23.2 | 394 | 23.1 | <u>394</u> | <u>23.2</u> | 363 | 25.1 | 362 | 25.2 | <u>362</u> | <u>25.2</u> |
| 445.gobmk | 479 | 21.9 | 479 | 21.9 | <u>479</u> | <u>21.9</u> | 434 | 24.1 | 434 | 24.2 | <u>434</u> | <u>24.2</u> |
| 456.hammer | 530 | 17.6 | 529 | 17.6 | <u>529</u> | <u>17.6</u> | 307 | 30.4 | <u>307</u> | <u>30.4</u> | 307 | 30.4 |
| 458.sjeng | 604 | 20.0 | <u>604</u> | <u>20.0</u> | 607 | 20.0 | 550 | 22.0 | <u>551</u> | <u>22.0</u> | 554 | 21.8 |
| 462.libquantum | <u>322</u> | <u>64.3</u> | 324 | 64.0 | 318 | 65.2 | <u>179</u> | <u>116</u> | 178 | 116 | 179 | 116 |
| 464.h264ref | <u>645</u> | <u>34.3</u> | 645 | 34.3 | 645 | 34.3 | 599 | 36.9 | <u>601</u> | <u>36.8</u> | 602 | 36.8 |
| 471.omnetpp | <u>384</u> | <u>16.3</u> | 385 | 16.2 | 384 | 16.3 | <u>355</u> | <u>17.6</u> | 355 | 17.6 | 354 | 17.6 |
| 473.astar | <u>422</u> | <u>16.7</u> | 421 | 16.7 | 423 | 16.6 | <u>386</u> | <u>18.2</u> | 389 | 18.0 | 384 | 18.3 |
| 483.xalancbmk | <u>242</u> | <u>28.5</u> | 242 | 28.5 | 242 | 28.5 | <u>242</u> | <u>28.5</u> | 242 | 28.5 | 242 | 28.5 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



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Base Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -opt-malloc-options=3 -parallel
-par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbenc: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:10:48 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 January 2008.