



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 99.5

PowerEdge M600 (Intel Xeon E5410, 2.33 GHz)

SPECint_rate_base2006 = 85.3

CPU2006 license: 55

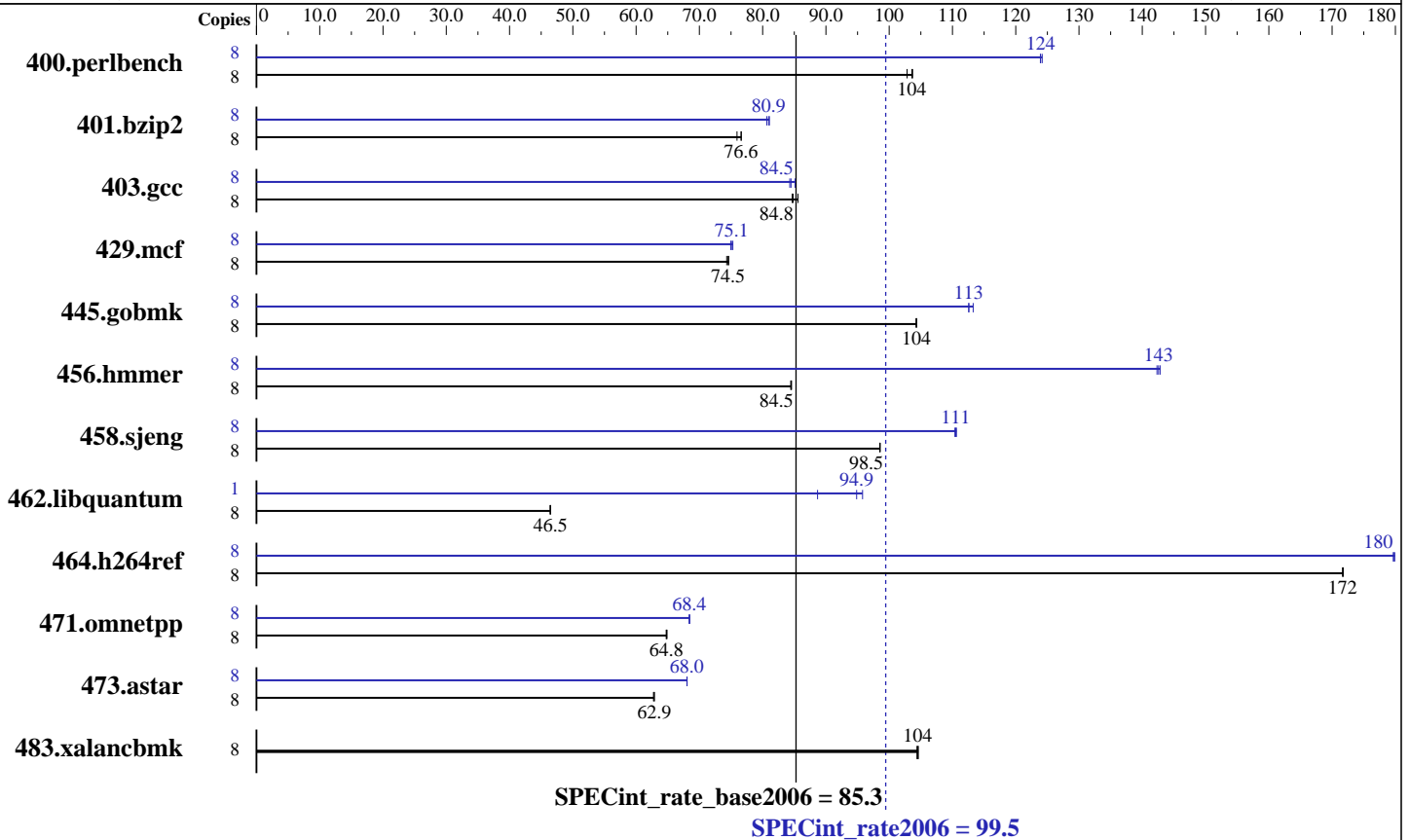
Test date: Feb-2008

Test sponsor: Dell Inc.

Hardware Availability: Jan-2008

Tested by: Dell Inc.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5410
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4x4 GB 667 MHz ECC CL5 FB-DIMM)
 Disk Subsystem: 1 x 80 GB 7.2k SATA
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap library V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	754	104	760	103	<u>755</u>	<u>104</u>	8	631	124	<u>631</u>	<u>124</u>	629	124
401.bzip2	8	1007	76.6	<u>1008</u>	<u>76.6</u>	1016	75.9	8	952	81.1	957	80.6	<u>954</u>	<u>80.9</u>
403.gcc	8	761	84.7	752	85.6	<u>759</u>	<u>84.8</u>	8	764	84.3	757	85.1	<u>762</u>	<u>84.5</u>
429.mcf	8	978	74.6	<u>979</u>	<u>74.5</u>	982	74.3	8	973	75.0	<u>972</u>	<u>75.1</u>	969	75.3
445.gobmk	8	<u>805</u>	<u>104</u>	805	104	804	104	8	741	113	745	113	<u>745</u>	<u>113</u>
456.hammer	8	883	84.6	884	84.4	<u>883</u>	<u>84.5</u>	8	<u>524</u>	<u>143</u>	523	143	524	142
458.sjeng	8	982	98.6	983	98.5	<u>982</u>	<u>98.5</u>	8	875	111	<u>876</u>	<u>111</u>	877	110
462.libquantum	8	3571	46.4	<u>3568</u>	<u>46.5</u>	3567	46.5	1	<u>218</u>	<u>94.9</u>	234	88.7	216	95.8
464.h264ref	8	<u>1031</u>	<u>172</u>	1031	172	1031	172	8	<u>985</u>	<u>180</u>	985	180	984	180
471.omnetpp	8	772	64.8	771	64.9	<u>772</u>	<u>64.8</u>	8	731	68.4	<u>731</u>	<u>68.4</u>	730	68.5
473.astar	8	895	62.7	893	62.9	<u>893</u>	<u>62.9</u>	8	825	68.1	825	68.0	<u>825</u>	<u>68.0</u>
483.xalancbmk	8	528	105	<u>528</u>	<u>104</u>	529	104	8	528	105	<u>528</u>	<u>104</u>	529	104

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default Enabled)
Hardware Prefetcher = Disabled (default Enabled)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.03.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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