



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 69.1

ProLiant BL260c G5
(3.0 GHz, Intel Xeon E5450)

SPECint_rate_base2006 = 60.3

CPU2006 license: 3

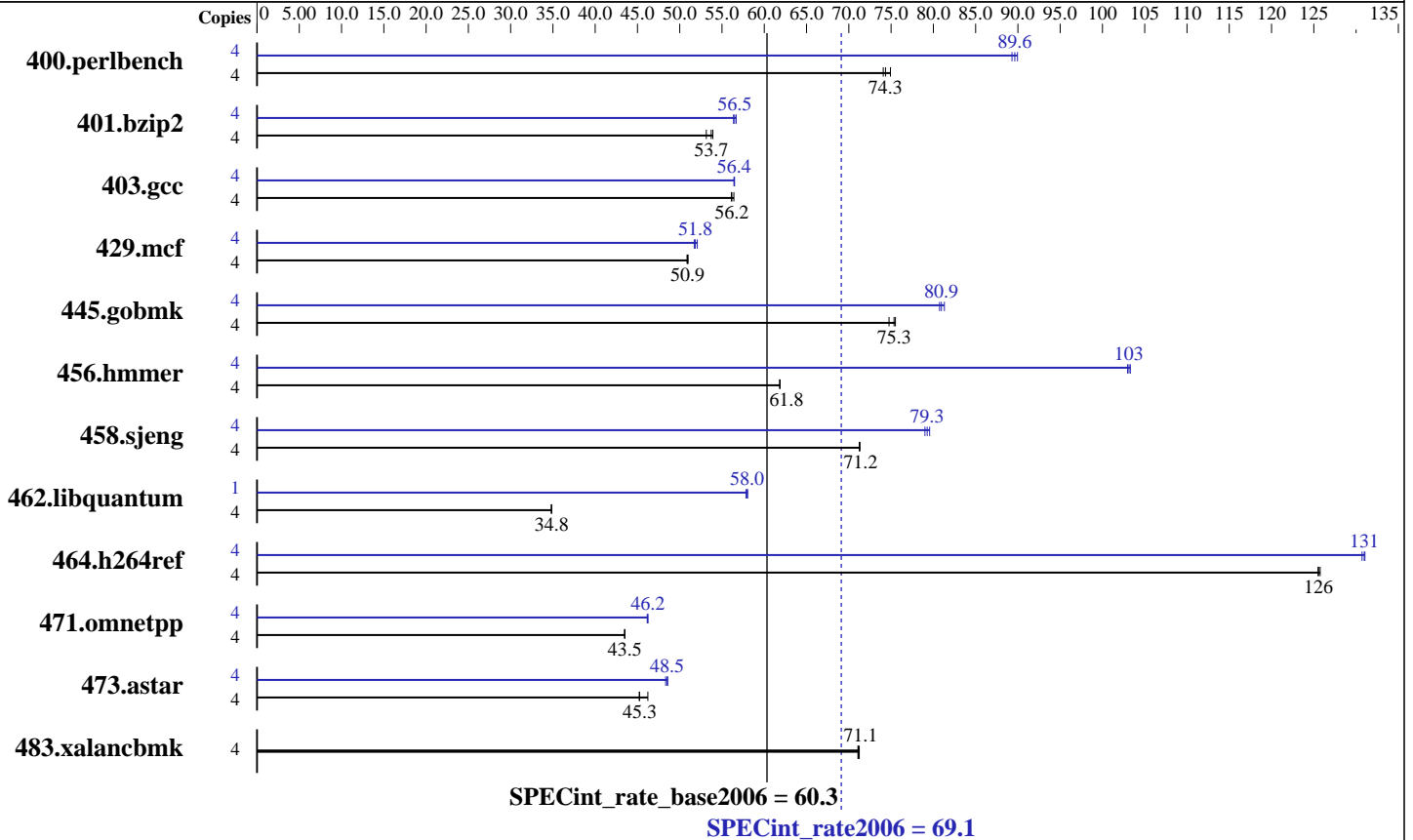
Test date: Mar-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5450
 CPU Characteristics: 3.0 GHz, 2x6 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4x4 GB PC2-5300P CL5)
 Disk Subsystem: 1x72 GB 15 K SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	528	74.1	<u>526</u>	<u>74.3</u>	522	74.9	4	438	89.3	<u>436</u>	<u>89.6</u>	434	90.0
401.bzip2	4	<u>718</u>	<u>53.7</u>	726	53.1	716	53.9	4	<u>683</u>	<u>56.5</u>	685	56.4	681	56.7
403.gcc	4	<u>573</u>	<u>56.2</u>	571	56.4	574	56.1	4	<u>571</u>	<u>56.4</u>	570	56.5	571	56.4
429.mcf	4	717	50.9	715	51.0	<u>716</u>	<u>50.9</u>	4	701	52.1	705	51.7	<u>704</u>	<u>51.8</u>
445.gobmk	4	556	75.5	<u>557</u>	<u>75.3</u>	561	74.8	4	516	81.3	<u>518</u>	<u>80.9</u>	520	80.7
456.hammer	4	603	61.9	604	61.8	<u>603</u>	<u>61.8</u>	4	361	103	362	103	<u>362</u>	<u>103</u>
458.sjeng	4	679	71.2	679	71.3	<u>679</u>	<u>71.2</u>	4	608	79.6	613	79.0	<u>611</u>	<u>79.3</u>
462.libquantum	4	2380	34.8	<u>2379</u>	<u>34.8</u>	2379	34.8	1	357	58.0	<u>357</u>	<u>58.0</u>	358	57.8
464.h264ref	4	704	126	<u>705</u>	<u>126</u>	705	126	4	<u>676</u>	<u>131</u>	675	131	677	131
471.omnetpp	4	575	43.5	<u>575</u>	<u>43.5</u>	575	43.5	4	541	46.2	<u>541</u>	<u>46.2</u>	542	46.2
473.astar	4	607	46.2	<u>620</u>	<u>45.3</u>	621	45.2	4	578	48.6	581	48.3	<u>579</u>	<u>48.5</u>
483.xalancbmk	4	<u>388</u>	<u>71.1</u>	388	71.1	387	71.2	4	<u>388</u>	<u>71.1</u>	388	71.1	387	71.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
```

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml>

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