



SPEC® CINT2006 Result

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Itautec

SPECint®_rate2006 = 124

Servidor Itautec MX221 (Intel Xeon X5450)

SPECint_rate_base2006 = 110

CPU2006 license: 9001

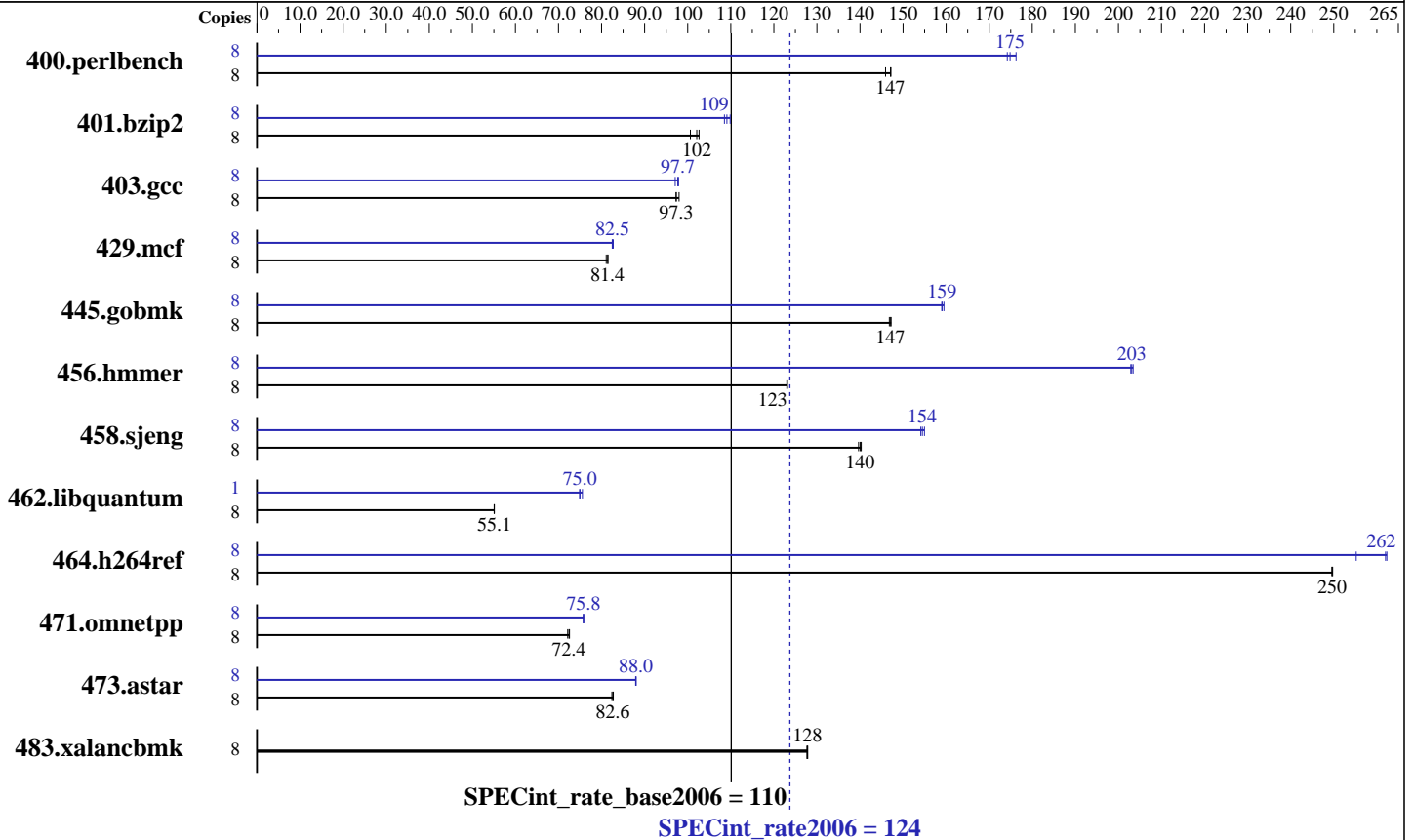
Test date: May-2008

Test sponsor: Itautec

Hardware Availability: Dec-2007

Tested by: Itautec

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon X5450
 CPU Characteristics:
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run Level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.10.50
 MicroQuill SmartHeap V8.1



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	536	146	531	147	<u>531</u>	<u>147</u>	8	443	176	449	174	<u>447</u>	<u>175</u>
401.bzip2	8	767	101	752	103	<u>756</u>	<u>102</u>	8	703	110	<u>708</u>	<u>109</u>	711	109
403.gcc	8	662	97.3	662	97.3	657	98.0	8	663	97.1	658	97.9	659	97.7
429.mcf	8	895	81.5	900	81.1	896	81.4	8	884	82.5	882	82.7	885	82.5
445.gobmk	8	570	147	570	147	572	147	8	528	159	528	159	526	159
456.hammer	8	607	123	606	123	606	123	8	367	203	368	203	368	203
458.sjeng	8	691	140	693	140	690	140	8	625	155	628	154	627	154
462.libquantum	8	3011	55.1	3008	55.1	3008	55.1	1	274	75.6	277	74.9	276	75.0
464.h264ref	8	709	250	709	250	709	250	8	676	262	675	262	694	255
471.omnetpp	8	689	72.6	693	72.1	690	72.4	8	660	75.8	659	75.8	660	75.8
473.astar	8	680	82.6	679	82.7	682	82.4	8	639	87.9	638	88.0	638	88.0
483.xalancbmk	8	432	128	432	128	432	128	8	432	128	432	128	432	128

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.

Platform Notes

BIOS configuration:
Hardware Prefetch Disabled

General Notes

This result was measured on the Servidor Itaotec MX201.
The Servidor Itaotec MX221 and the Servidor Itaotec MX201 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include
456.hmmer: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include
C++ benchmarks:
icpc



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
403.gcc: -fast -inline-calloc -opt-malloc-options=3
429.mcf: -fast -prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

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