



# SPEC<sup>®</sup> CFP2006 Result

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## Sun Microsystems

## SPECfp<sup>®</sup>\_rate2006 = 2010

## Sun SPARC Enterprise M9000

## SPECfp\_rate\_base2006 = 1860

CPU2006 license: 6

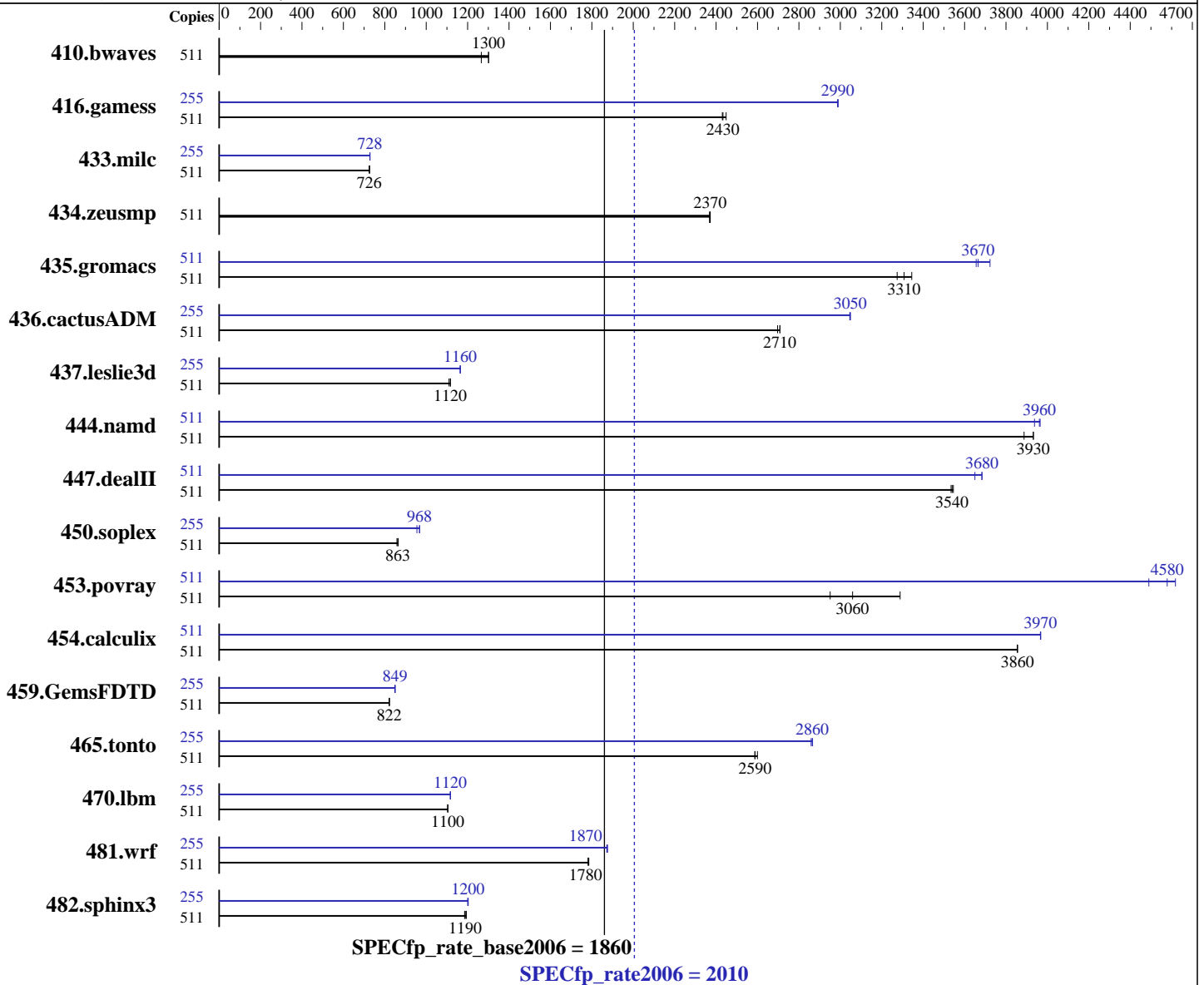
Test date: Jul-2008

Test sponsor: Sun Microsystems

Hardware Availability: Jul-2008

Tested by: Sun Microsystems

Software Availability: Jul-2008



### Hardware

CPU Name: SPARC64 VII  
 CPU Characteristics:  
 CPU MHz: 2520  
 FPU: Integrated  
 CPU(s) enabled: 256 cores, 64 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 to 16 CMUs; each CMU contains 2 or 4 chips  
 Primary Cache: 64 KB I + 64 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip

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### Software

Operating System: Solaris 10 5/08 with Patch 137111-03  
 Compiler: Sun Studio 12 with patches 124867-06, 124861-07, 124863-05, 127000-05 (see patch information below)  
 Auto Parallel: No  
 File System: ufs  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit

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L3 Cache: None  
Other Cache: None  
Memory: 1 TB (512 x 2 GB)  
Disk Subsystem: 12 TB RAID 0 Solaris Volume  
24 x 500 GB 15000 RPM disk  
Stripe interlace size 128Kbytes  
Other Hardware: None

Other Software: None

### Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	511	5485	1270	5337	1300	<u>5338</u>	<u>1300</u>	511	5485	1270	5337	1300	<u>5338</u>	<u>1300</u>		
416.gamess	511	4117	2430	<u>4112</u>	<u>2430</u>	4087	2450	255	<u>1671</u>	<u>2990</u>	1671	2990	1670	2990		
433.milc	511	6463	726	<u>6464</u>	<u>726</u>	6464	726	255	3215	728	<u>3215</u>	<u>728</u>	3215	728		
434.zeusmp	511	1961	2370	<u>1961</u>	<u>2370</u>	1964	2370	511	1961	2370	<u>1961</u>	<u>2370</u>	1964	2370		
435.gromacs	511	1091	3340	1114	3270	<u>1103</u>	<u>3310</u>	511	998	3660	980	3720	<u>995</u>	<u>3670</u>		
436.cactusADM	511	2264	2700	2255	2710	<u>2255</u>	<u>2710</u>	255	1000	3050	<u>1000</u>	<u>3050</u>	1001	3050		
437.leslie3d	511	<u>4303</u>	<u>1120</u>	4300	1120	4327	1110	255	2059	1160	2057	1170	<u>2058</u>	<u>1160</u>		
444.namd	511	1054	3890	1042	3930	<u>1042</u>	<u>3930</u>	511	1034	3960	<u>1035</u>	<u>3960</u>	1041	3940		
447.dealII	511	<u>1651</u>	<u>3540</u>	1654	3530	1649	3550	511	1602	3650	1587	3680	<u>1587</u>	<u>3680</u>		
450.soplex	511	4964	859	4928	865	<u>4940</u>	<u>863</u>	255	2227	955	2197	968	<u>2198</u>	<u>968</u>		
453.povray	511	<u>889</u>	<u>3060</u>	827	3290	921	2950	511	605	4490	<u>594</u>	<u>4580</u>	589	4620		
454.calculix	511	<u>1094</u>	<u>3860</u>	1094	3850	1093	3860	511	1062	3970	<u>1063</u>	<u>3970</u>	1063	3970		
459.GemsFDTD	511	6599	822	<u>6598</u>	<u>822</u>	6595	822	255	3185	850	<u>3185</u>	<u>849</u>	3186	849		
465.tonto	511	1944	2590	<u>1943</u>	<u>2590</u>	1934	2600	255	878	2860	876	2870	<u>876</u>	<u>2860</u>		
470.lbm	511	6357	1100	<u>6358</u>	<u>1100</u>	6360	1100	255	3136	1120	3138	1120	<u>3138</u>	<u>1120</u>		
481.wrf	511	<u>3200</u>	<u>1780</u>	3197	1790	3204	1780	255	1522	1870	1519	1880	<u>1520</u>	<u>1870</u>		
482.sphinx3	511	8342	1190	8403	1190	<u>8377</u>	<u>1190</u>	255	4139	1200	<u>4137</u>	<u>1200</u>	4131	1200		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Invocation Notes

Sun Studio compiler patches are available at  
[http://developers.sun.com/sunstudio/downloads/patches/ss12\\_patches.jsp](http://developers.sun.com/sunstudio/downloads/patches/ss12_patches.jsp)

### Submit Notes

Processes were assigned to specific processors using 'pbind' commands. The config file option 'submit' was used, along with a list of processors in the 'BIND' variable, to generate the pbind commands. (For details, please see the config file.)



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## Operating System Notes

Environment Variable Settings:

```
export LD_PRELOAD=mpss.so.1:madv.so.1
export MPSSHEAP=4MB
export MPSSSTACK=4MB
    Requests system to use 4 MB pages when possible.
export MADV access_lwp
    access_lwp requests that the next light weight process to touch
    the specified address range will access it most heavily.
ulimit -s 131072 was used to limit the space consumed
    by the stack (making more space available for the heap)
```

System Tunables (/etc/system parameters):

```
autoup=200
    Causes pages older than the listed number of seconds to
    be written by fsflush.
lpg_alloc_prefer=1
    Set lgroup page allocation to strongly prefer local pages
```

Other System Settings:

The webconsole service was turned off using  
svcadm disable webconsole

The SPEC toolset was bound to processors 1-511 using processor sets:  
psrset -c 1-511  
psrset -e 1 ksh

## Platform Notes

Memory is 8-way interleaved by filling all slots with the same capacity DIMMs.

This result is measured on a Sun SPARC Enterprise M9000 Server. Note that the Sun SPARC Enterprise M9000 and Fujitsu SPARC Enterprise M9000 are electrically equivalent.

## Base Compiler Invocation

C benchmarks:

cc

C++ benchmarks:

CC

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## Base Compiler Invocation (Continued)

Fortran benchmarks:  
f90

Benchmarks using both Fortran and C:  
cc f90

## Base Optimization Flags

C benchmarks:  
-fast -xipo=2 -xpagesize=4M -xprefetch\_level=1 -fma=fused  
-xalias\_level=std -xprefetch\_auto\_type=indirect\_array\_access -l12amm

C++ benchmarks:  
-library=stlport4 -fast -xipo=2 -xpagesize=4M -xprefetch\_level=1  
-fma=fused -xalias\_level=compatible -l12amm

Fortran benchmarks:  
-fast -xipo=2 -xpagesize=4M -xprefetch\_level=1 -fma=fused -l12amm

Benchmarks using both Fortran and C:  
-fast(cc) -fast(f90) -xipo=2 -xpagesize=4M -xprefetch\_level=1  
-fma=fused -xalias\_level=std -xprefetch\_auto\_type=indirect\_array\_access  
-l12amm

## Peak Compiler Invocation

C benchmarks:  
cc

C++ benchmarks:  
CC

Fortran benchmarks:  
f90

Benchmarks using both Fortran and C:  
cc f90

## Peak Optimization Flags

C benchmarks:  
433.milc: -fast -xipo=2 -xpagesize=4M -fma=fused  
-xalias\_level=strong -xprefetch\_level=2  
-xprefetch\_auto\_type=indirect\_array\_access -xprefetch=latx:5

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## Peak Optimization Flags (Continued)

433.milc (continued):  
-l12amm

470.lbm: -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xrestrict -xipo=2  
-xprefetch\_level=2 -xarch=v8plusb -l12amm

482.sphinx3: -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xipo=2  
-xpagesize=4M -fma=fused -xinline= -xalias\_level=strong  
-xprefetch\_level=2 -lfast -l12amm

### C++ benchmarks:

444.namd: -library=stlport4 -fast -xipo=2 -xpagesize=4M -fma=fused  
-xdepend -xalias\_level=compatible -xprefetch\_level=1  
-xprefetch=latx:0.5 -l12amm

447.dealIII: -library=stlport4 -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xipo=2  
-xpagesize=4M -fma=fused -xdepend -xalias\_level=compatible  
-xrestrict -xprefetch=no -l12amm

450.soplex: -library=stlport4 -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xipo=2  
-xpagesize=4M -fma=fused -xdepend -xalias\_level=compatible  
-fsimple=0 -xrestrict -xprefetch=no -l12amm

453.povray: -library=stlport4 -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xipo=2  
-xpagesize=4M -fma=fused -xdepend -xalias\_level=compatible  
-xprefetch=latx:5.0 -l12amm

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xipo=2  
-xpagesize=4M -fma=fused -xprefetch\_level=1 -l12amm

434.zeusmp: basepeak = yes

437.leslie3d: -fast -xipo=2 -xpagesize=4M -fma=fused -xprefetch=latx:5.0  
-l12amm

459.GemsFDTD: -fast -xipo=2 -xpagesize=4M -fma=fused -fsimple=1  
-xprefetch=no -l12amm

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## Peak Optimization Flags (Continued)

465.tonto: -fast -xipo=2 -xpagesize=4M -fma=fused -xprefetch=no  
-lfast -l12amm

Benchmarks using both Fortran and C:

435.gromacs: -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast(cc) -fast(f90)  
-xipo=2 -xpagesize=4M -fma=fused -fsimple=0  
-xprefetch=latx:0.5 -xarch=generic -xchip=generic

436.cactusADM: -fast(cc) -fast(f90) -xipo=2 -xpagesize=4M -fma=fused  
-l12amm

454.calculix: -fast(cc) -fast(f90) -xipo=2 -xpagesize=4M -fma=fused  
-xprefetch=latx:3.0 -l12amm

481.wrf: -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast(cc) -fast(f90)  
-xipo=2 -xpagesize=4M -fma=fused -xprefetch=no -l12amm

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Sun-Solaris-Studio12-and-gccfss4.2.20090713.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Sun-Solaris-Studio12-and-gccfss4.2.20090713.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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