



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = 26.5

IBM System x iDataPlex dx360 (Intel Xeon E5472)

SPECint_base2006 = 23.2

CPU2006 license: 11

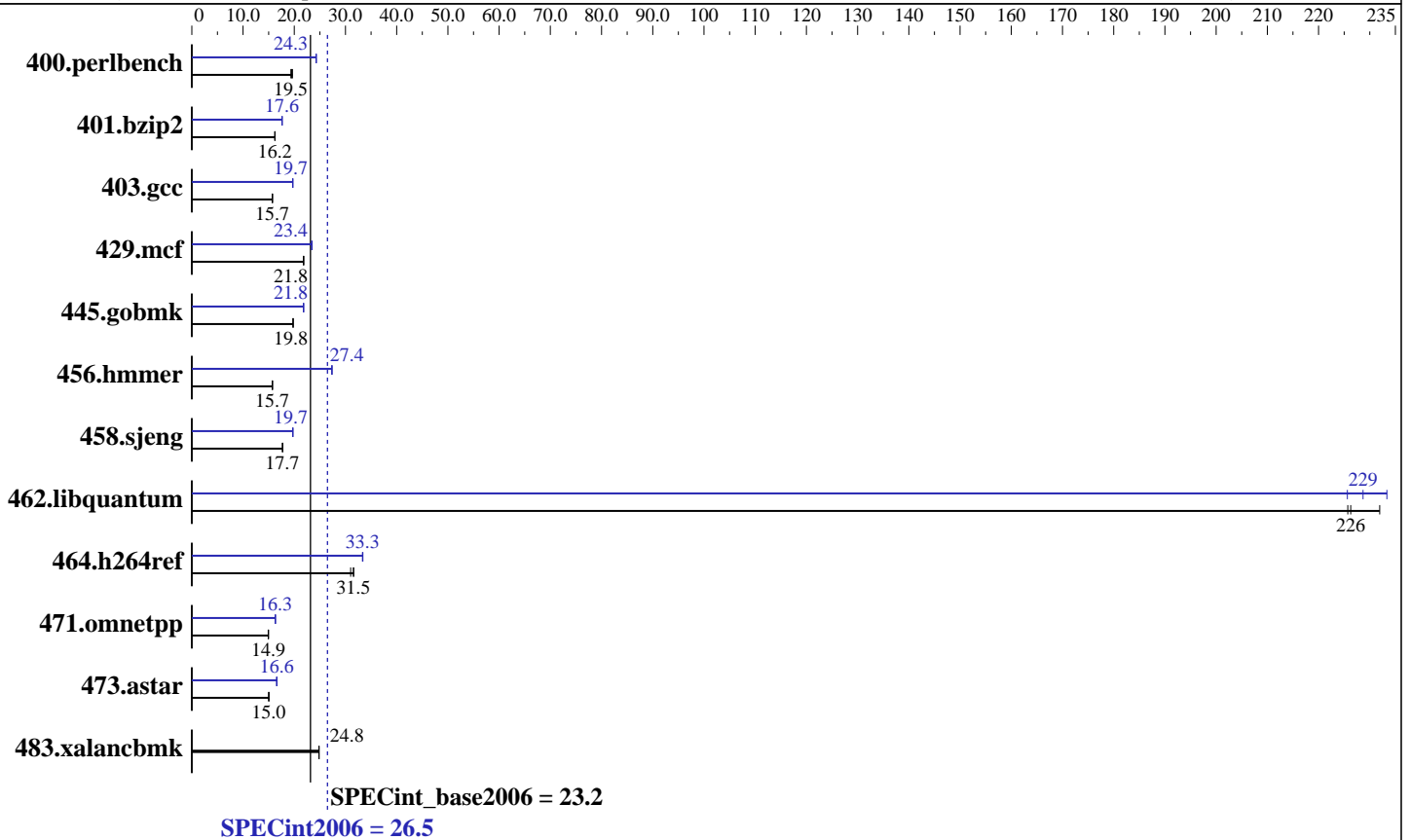
Test date: Jul-2008

Test sponsor: IBM Corporation

Hardware Availability: Sep-2008

Tested by: IBM Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5472
 CPU Characteristics: 1600 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4 x 4 GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 160 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	505	19.3	497	19.7	<u>501</u>	<u>19.5</u>	403	24.3	402	24.3	<u>402</u>	<u>24.3</u>
401.bzip2	596	16.2	<u>594</u>	<u>16.2</u>	594	16.3	548	17.6	<u>547</u>	<u>17.6</u>	547	17.6
403.gcc	511	15.8	512	15.7	<u>512</u>	<u>15.7</u>	<u>408</u>	<u>19.7</u>	408	19.7	409	19.7
429.mcf	<u>418</u>	<u>21.8</u>	418	21.8	418	21.8	388	23.5	390	23.4	<u>390</u>	<u>23.4</u>
445.gobmk	531	19.8	<u>530</u>	<u>19.8</u>	530	19.8	481	21.8	481	21.8	<u>481</u>	<u>21.8</u>
456.hmmmer	594	15.7	592	15.7	<u>593</u>	<u>15.7</u>	341	27.4	341	27.4	<u>341</u>	<u>27.4</u>
458.sjeng	<u>683</u>	<u>17.7</u>	682	17.8	684	17.7	<u>614</u>	<u>19.7</u>	614	19.7	613	19.7
462.libquantum	<u>91.6</u>	<u>226</u>	91.8	226	89.3	232	91.8	226	88.8	233	<u>90.6</u>	<u>229</u>
464.h264ref	700	31.6	714	31.0	<u>702</u>	<u>31.5</u>	663	33.4	<u>664</u>	<u>33.3</u>	664	33.3
471.omnetpp	<u>418</u>	<u>14.9</u>	419	14.9	417	15.0	384	16.3	382	16.3	<u>383</u>	<u>16.3</u>
473.astar	<u>467</u>	<u>15.0</u>	468	15.0	467	15.0	<u>423</u>	<u>16.6</u>	422	16.6	424	16.6
483.xalancbmk	278	24.8	278	24.8	<u>278</u>	<u>24.8</u>	278	24.8	278	24.8	<u>278</u>	<u>24.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode
Hardware Sector Prefetch Enabled and Adjacent Sector Prefetch Enabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to null

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 19:23:44 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 August 2008.