



SPEC® CINT2006 Result

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Bull SAS

NovaScale B260+
(Intel Xeon E5450, 3.00 GHz)

SPECint®_rate2006 = 130

SPECint_rate_base2006 = 106

CPU2006 license: 20

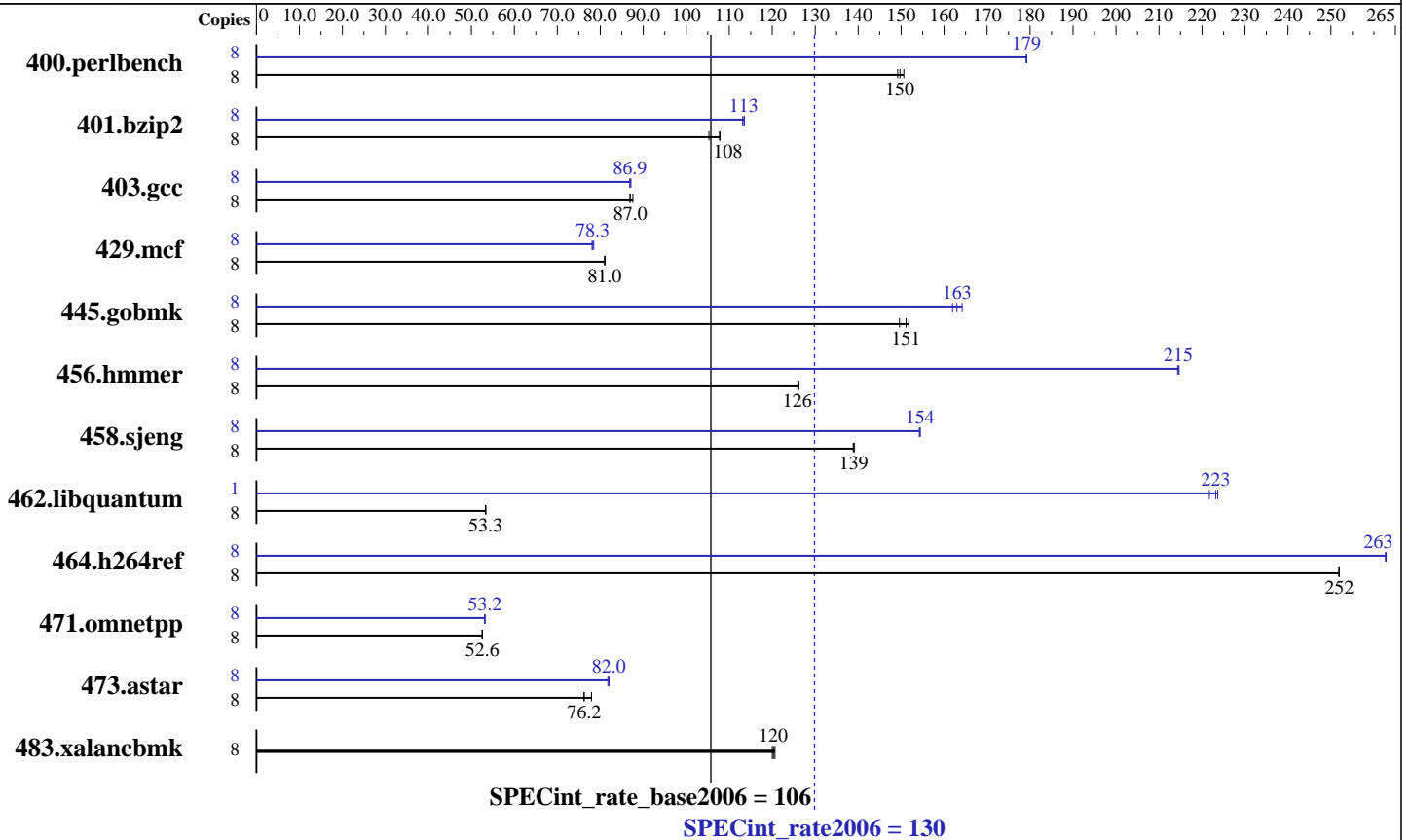
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Jul-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5450
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5
 Disk Subsystem: 1x73 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 SP1
 Kernel 2.6.16.46-0.12-smp for x86_64
 Compiler: Intel C++ Compiler 10.1 for Linux
 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.50.0.15
 SmartHeap library V8.1



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	519	151	524	149	<u>522</u>	<u>150</u>	8	<u>436</u>	<u>179</u>	436	179	436	179
401.bzip2	8	733	105	<u>717</u>	<u>108</u>	716	108	8	<u>681</u>	<u>113</u>	682	113	680	114
403.gcc	8	741	86.9	735	87.6	<u>740</u>	<u>87.0</u>	8	<u>741</u>	<u>86.9</u>	739	87.1	741	86.9
429.mcf	8	<u>900</u>	<u>81.0</u>	900	81.0	900	81.1	8	934	78.1	<u>932</u>	<u>78.3</u>	930	78.5
445.gobmk	8	561	150	<u>555</u>	<u>151</u>	553	152	8	<u>515</u>	<u>163</u>	511	164	518	162
456.hmmer	8	591	126	593	126	<u>592</u>	<u>126</u>	8	348	214	348	215	<u>348</u>	<u>215</u>
458.sjeng	8	<u>696</u>	<u>139</u>	697	139	696	139	8	627	154	<u>627</u>	<u>154</u>	628	154
462.libquantum	8	3109	53.3	3109	53.3	<u>3109</u>	<u>53.3</u>	1	92.6	224	93.5	222	<u>92.8</u>	<u>223</u>
464.h264ref	8	703	252	703	252	<u>703</u>	<u>252</u>	8	673	263	674	263	<u>674</u>	<u>263</u>
471.omnetpp	8	<u>951</u>	<u>52.6</u>	951	52.6	953	52.5	8	941	53.1	941	53.2	<u>941</u>	<u>53.2</u>
473.astar	8	<u>737</u>	<u>76.2</u>	737	76.2	720	78.0	8	687	81.8	<u>685</u>	<u>82.0</u>	685	82.0
483.xalancbmk	8	458	121	460	120	<u>459</u>	<u>120</u>	8	458	121	460	120	<u>459</u>	<u>120</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode
BIOS settings :
Hardware Prefetcher : Enabled
Adjacent Cache-Line Prefetch : Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.00.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.00.xml

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For other inquiries, please contact webmaster@spec.org.

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