



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = Not Run

PowerEdge 2900 III (Intel Xeon E5450, 3.00 GHz)

SPECint\_rate\_base2006 = 110

CPU2006 license: 55

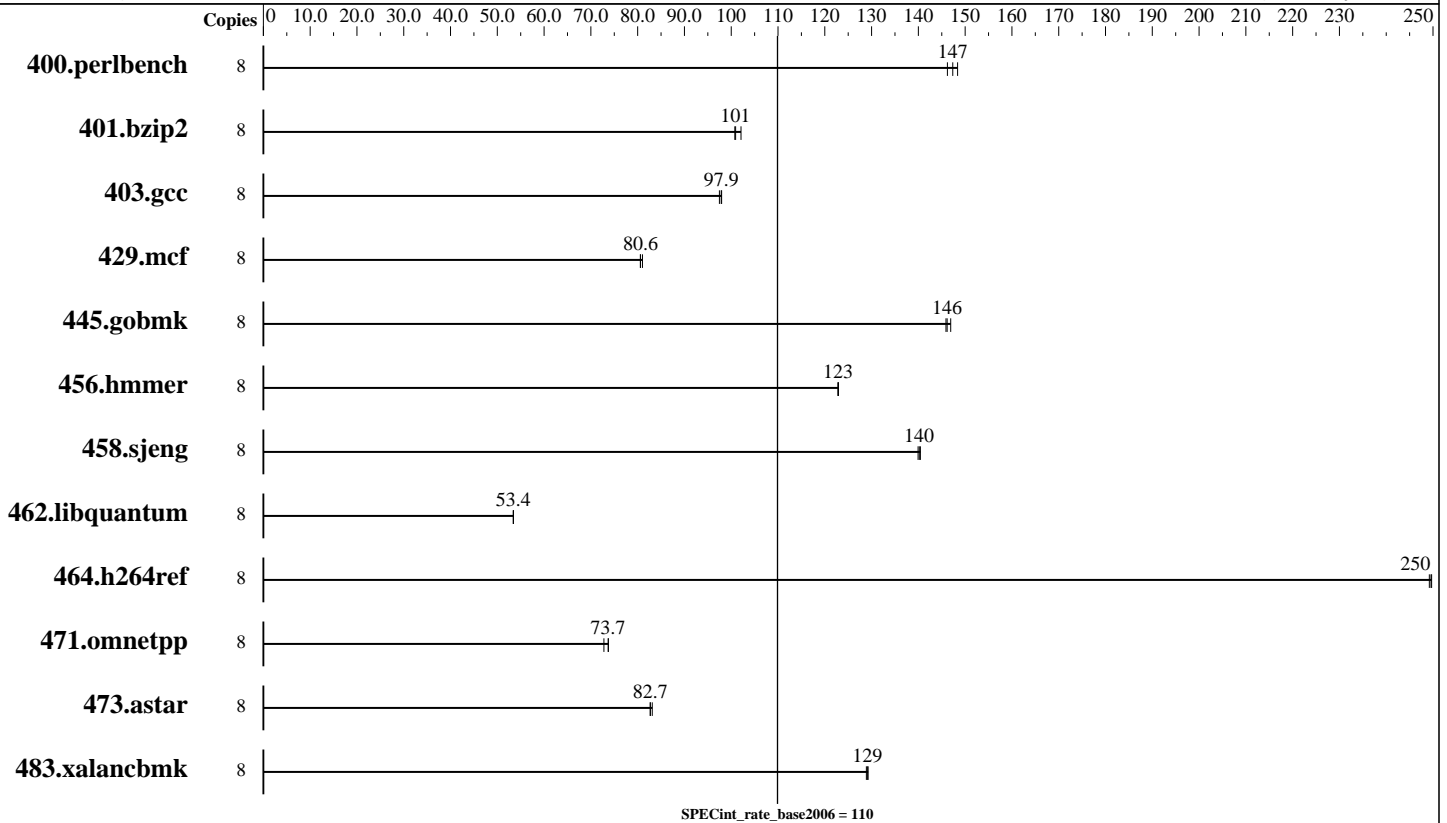
Test date: Aug-2008

Test sponsor: Dell Inc.

Hardware Availability: Nov-2007

Tested by: Dell Inc.

Software Availability: May-2008



### Hardware

CPU Name: Intel Xeon E5450  
 CPU Characteristics:  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB, DDR2-667 MHz, CL5, Reg, Dual Rank)  
 Disk Subsystem: 2 x 73 GB GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2  
 Kernel 2.6.16-60.0.21-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux  
 Build 20080312 Package ID: l\_cc\_p\_10.1.015  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	535	146	<u>530</u>	<u>147</u>	527	148							
401.bzip2	8	766	101	<u>765</u>	<u>101</u>	756	102							
403.gcc	8	657	97.9	<u>658</u>	<u>97.9</u>	661	97.5							
429.mcf	8	906	80.6	900	81.0	<u>905</u>	<u>80.6</u>							
445.gobmk	8	571	147	<u>574</u>	<u>146</u>	575	146							
456.hammer	8	608	123	<u>608</u>	<u>123</u>	607	123							
458.sjeng	8	689	140	692	140	<u>690</u>	<u>140</u>							
462.libquantum	8	<u>3102</u>	<u>53.4</u>	3102	53.4	3103	53.4							
464.h264ref	8	709	250	710	249	<u>709</u>	<u>250</u>							
471.omnetpp	8	687	72.8	678	73.7	<u>678</u>	<u>73.7</u>							
473.astar	8	679	82.7	<u>679</u>	<u>82.7</u>	676	83.1							
483.xalancbmk	8	<u>428</u>	<u>129</u>	427	129	428	129							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Platform Notes

BIOS Settings:  
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)  
Hardware Prefetcher = Disabled (Default = Enabled)

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:

`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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