



SPEC® CINT2006 Result

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ACTION S.A.

SPECint®_rate2006 = 94.8

ACTINA SOLAR 442 S3 (Intel Xeon E7430, 2.13 GHz)

SPECint_rate_base2006 = 79.9

CPU2006 license: 9008

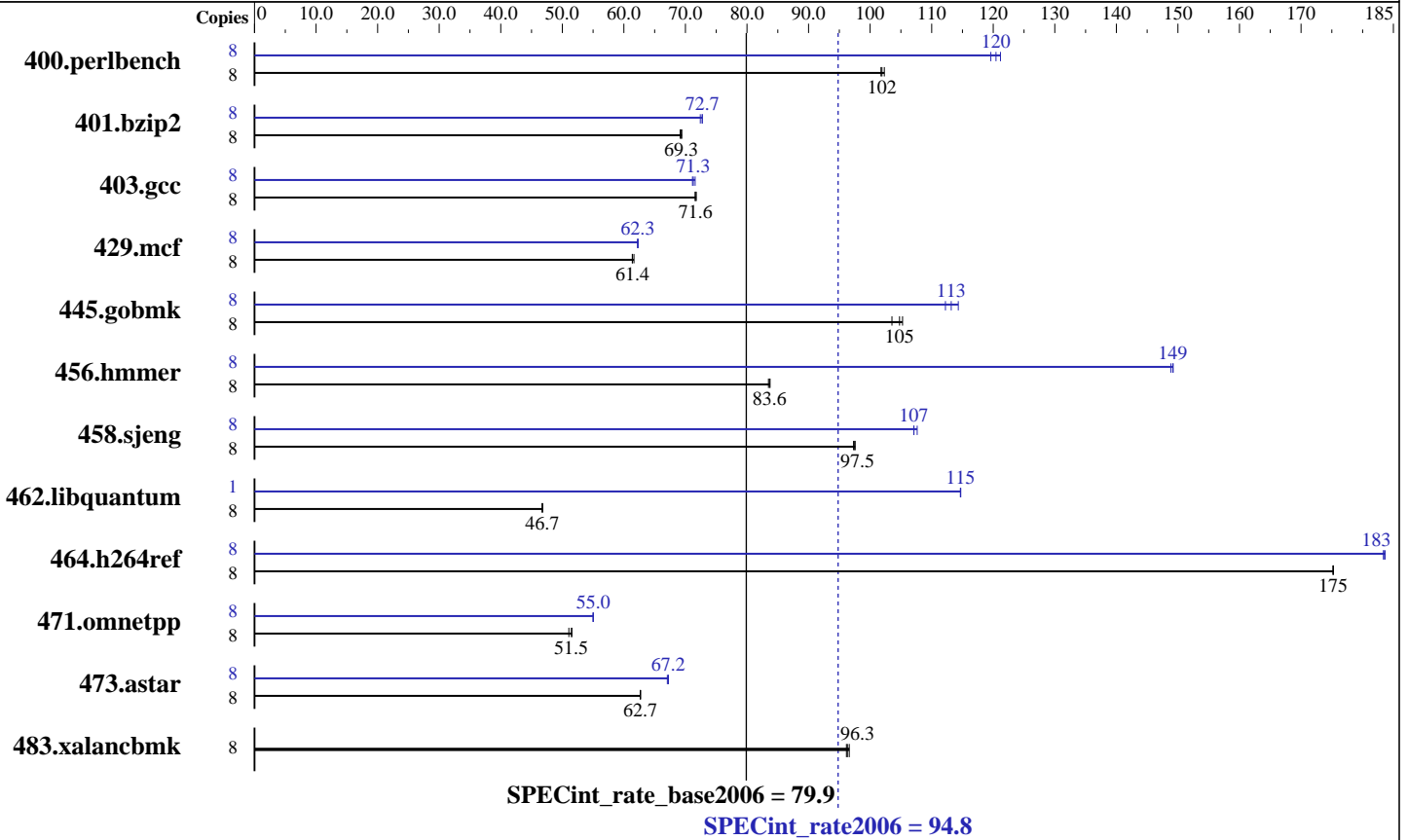
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Aug-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E7430
 CPU Characteristics: Quad-Core, 1066 MHz bus
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (8x 2 GB PC2-5300 ECC FBDIMM)
 Disk Subsystem: 1x 250 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	768	102	764	102	767	102	8	649	120	653	120	645	121
401.bzip2	8	1112	69.4	1114	69.3	1116	69.1	8	1061	72.7	1061	72.7	1066	72.4
403.gcc	8	897	71.8	899	71.6	900	71.6	8	900	71.6	903	71.3	905	71.1
429.mcf	8	1189	61.4	1188	61.4	1184	61.6	8	1172	62.3	1173	62.2	1171	62.3
445.gobmk	8	797	105	801	105	810	104	8	734	114	748	112	742	113
456.hammer	8	894	83.5	892	83.6	891	83.7	8	501	149	500	149	500	149
458.sjeng	8	992	97.6	993	97.5	995	97.3	8	904	107	899	108	904	107
462.libquantum	8	3543	46.8	3547	46.7	3546	46.7	1	181	115	181	115	181	115
464.h264ref	8	1010	175	1011	175	1010	175	8	964	184	965	183	965	183
471.omnetpp	8	970	51.6	979	51.1	971	51.5	8	908	55.0	909	55.0	909	55.0
473.astar	8	896	62.7	896	62.7	896	62.7	8	835	67.2	836	67.2	837	67.1
483.xalancbmk	8	574	96.2	573	96.3	571	96.6	8	574	96.2	573	96.3	571	96.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hammer, for peak, are compiled in 64-bit mode.
Taskset command was used to bind processes to CPUs.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:

icpc

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch
```

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Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.00.xml>



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