



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

SPECint®_rate2006 = 157

ASUS D5EB-DG server motherboard (Intel Xeon X5492)

SPECint_rate_base2006 = 145

CPU2006 license: 009016

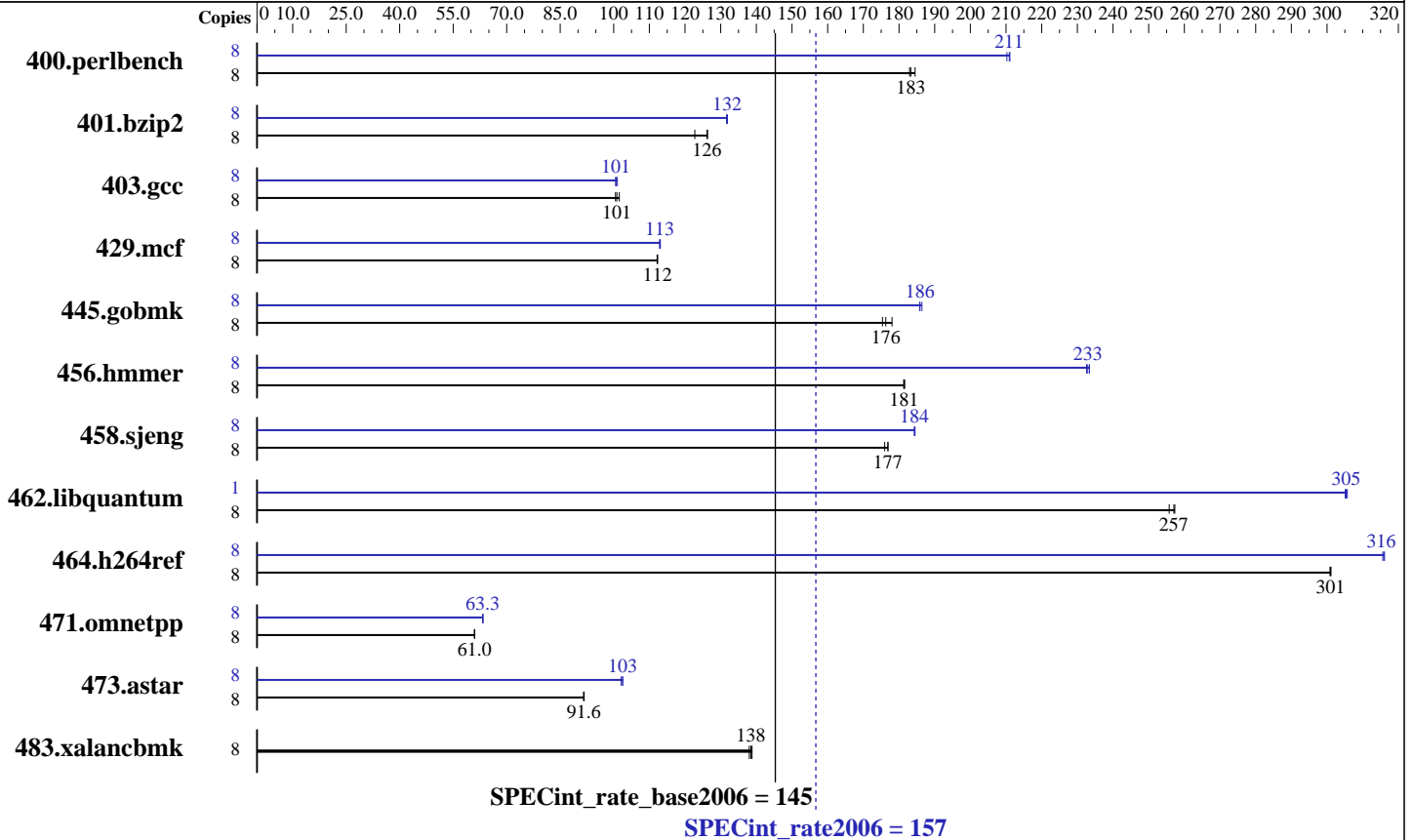
Test date: Oct-2008

Test sponsor: ASUSTeK Computer Inc.

Hardware Availability: Feb-2008

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5492
 CPU Characteristics: 1600 MHz Bus Speed
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 32 GB (8 X 4GB PC2-6400F, CL5, FBDIMM)
 Disk Subsystem: Seagate ST3500830AS 500GB SATAII, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

SPECint_rate2006 = 157

ASUS D5EB-DG server motherboard (Intel Xeon X5492)

SPECint_rate_base2006 = 145

CPU2006 license: 009016

Test date: Oct-2008

Test sponsor: ASUSTeK Computer Inc.

Hardware Availability: Feb-2008

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2008

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	424	185	<u>426</u>	<u>183</u>	427	183	8	<u>371</u>	<u>211</u>	372	210	370	211
401.bzip2	8	611	126	<u>612</u>	<u>126</u>	629	123	8	586	132	<u>586</u>	<u>132</u>	585	132
403.gcc	8	641	101	634	102	<u>638</u>	<u>101</u>	8	641	101	637	101	<u>640</u>	<u>101</u>
429.mcf	8	<u>650</u>	<u>112</u>	650	112	650	112	8	646	113	<u>646</u>	<u>113</u>	645	113
445.gobmk	8	479	175	<u>476</u>	<u>176</u>	471	178	8	<u>451</u>	<u>186</u>	450	186	452	186
456.hammer	8	411	182	<u>411</u>	<u>181</u>	412	181	8	320	233	<u>320</u>	<u>233</u>	321	233
458.sjeng	8	547	177	550	176	<u>547</u>	<u>177</u>	8	525	184	<u>525</u>	<u>184</u>	525	184
462.libquantum	8	648	256	<u>645</u>	<u>257</u>	644	257	1	67.9	305	<u>67.9</u>	<u>305</u>	67.8	306
464.h264ref	8	<u>588</u>	<u>301</u>	588	301	588	301	8	560	316	561	316	<u>560</u>	<u>316</u>
471.omnetpp	8	820	61.0	819	61.0	<u>820</u>	<u>61.0</u>	8	<u>790</u>	<u>63.3</u>	790	63.3	789	63.3
473.astar	8	612	91.7	<u>613</u>	<u>91.6</u>	613	91.6	8	548	103	550	102	<u>548</u>	<u>103</u>
483.xalancbmk	8	400	138	398	139	<u>399</u>	<u>138</u>	8	400	138	398	139	<u>399</u>	<u>138</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M

Platform Notes

Tested system case can be used with Intel EEB 3.61 spec
PC Power and Cooling 650W power supply
System was configured with XGI Z9s VGA (on board VGA)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

SPECint_rate2006 = 157

ASUS D5EB-DG server motherboard (Intel Xeon X5492)

SPECint_rate_base2006 = 145

CPU2006 license: 009016

Test date: Oct-2008

Test sponsor: ASUSTeK Computer Inc.

Hardware Availability: Feb-2008

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2008

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

SPECint_rate2006 = 157

ASUS D5EB-DG server motherboard (Intel Xeon X5492)

SPECint_rate_base2006 = 145

CPU2006 license: 009016

Test date: Oct-2008

Test sponsor: ASUSTeK Computer Inc.

Hardware Availability: Feb-2008

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2008

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

SPECint_rate2006 = 157

ASUS D5EB-DG server motherboard (Intel Xeon X5492)

SPECint_rate_base2006 = 145

CPU2006 license: 009016

Test date: Oct-2008

Test sponsor: ASUSTeK Computer Inc.

Hardware Availability: Feb-2008

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2008

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 20:50:42 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 December 2008.