



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 55.2

PowerEdge 1950 III (Intel Xeon E5205, 1.86 GHz)

SPECint_rate_base2006 = 52.7

CPU2006 license: 55

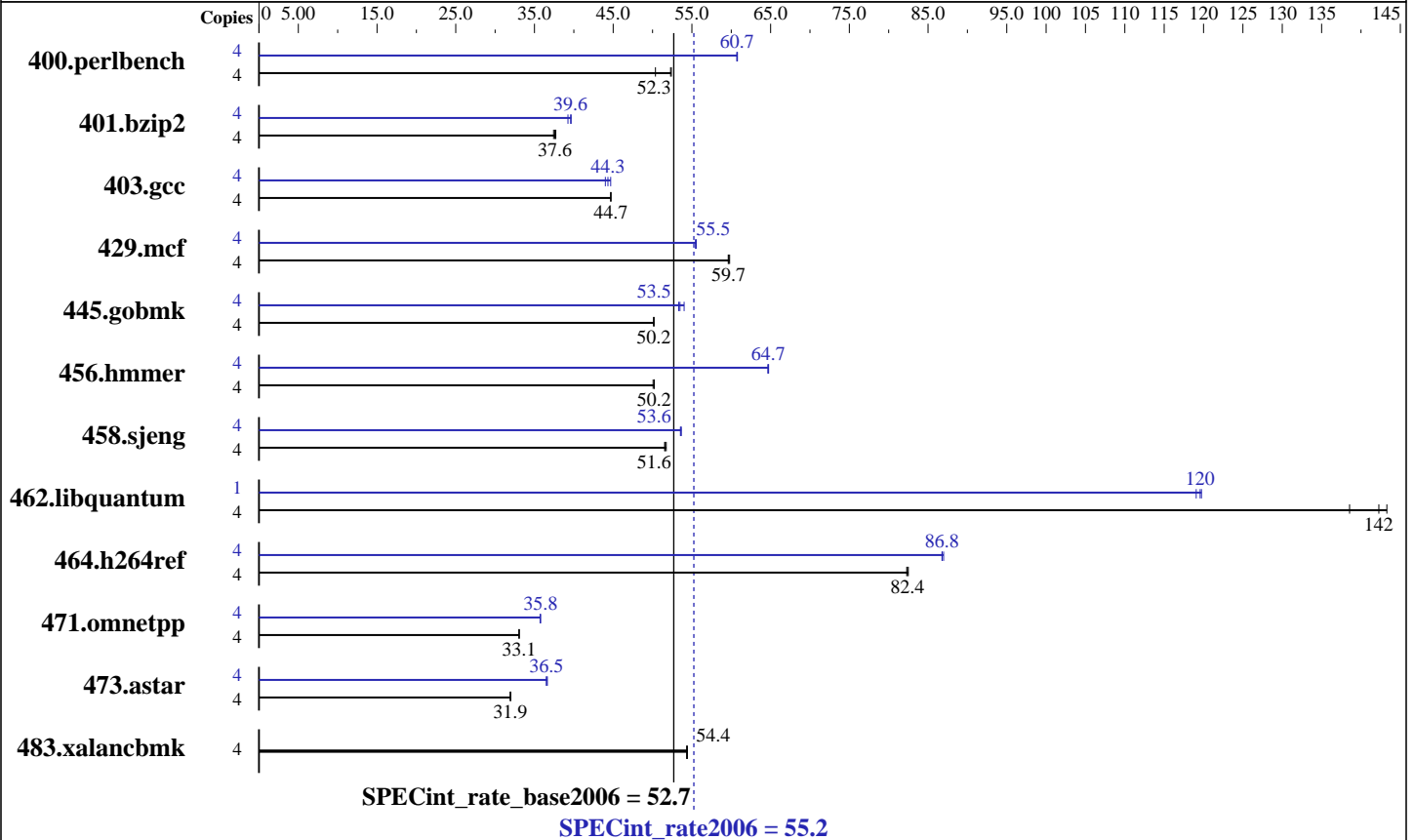
Test date: Nov-2008

Test sponsor: Dell Inc.

Hardware Availability: Sep-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5205
 CPU Characteristics:
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB 667 MHz ECC CL5 FB-DIMM)
 Disk Subsystem: 1 x 73 GB 15000 RPM SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	776	50.4	<u>747</u>	<u>52.3</u>	746	52.4	4	<u>643</u>	<u>60.7</u>	644	60.7	643	60.8
401.bzip2	4	1024	37.7	<u>1027</u>	<u>37.6</u>	1031	37.4	4	983	39.3	973	39.7	<u>975</u>	<u>39.6</u>
403.gcc	4	721	44.7	<u>720</u>	<u>44.7</u>	720	44.7	4	721	44.7	<u>726</u>	<u>44.3</u>	732	44.0
429.mcf	4	610	59.8	612	59.6	<u>611</u>	<u>59.7</u>	4	<u>658</u>	<u>55.5</u>	657	55.6	658	55.5
445.gobmk	4	837	50.1	836	50.2	<u>836</u>	<u>50.2</u>	4	777	54.0	787	53.3	<u>785</u>	<u>53.5</u>
456.hammer	4	743	50.2	<u>743</u>	<u>50.2</u>	745	50.1	4	576	64.7	577	64.7	<u>577</u>	<u>64.7</u>
458.sjeng	4	935	51.7	939	51.5	<u>938</u>	<u>51.6</u>	4	902	53.7	<u>903</u>	<u>53.6</u>	904	53.5
462.libquantum	4	598	139	<u>583</u>	<u>142</u>	578	143	1	173	120	<u>173</u>	<u>120</u>	174	119
464.h264ref	4	1073	82.5	<u>1075</u>	<u>82.4</u>	1076	82.3	4	1020	86.8	1018	87.0	<u>1020</u>	<u>86.8</u>
471.omnetpp	4	757	33.0	756	33.1	<u>756</u>	<u>33.1</u>	4	700	35.7	699	35.8	<u>699</u>	<u>35.8</u>
473.astar	4	<u>879</u>	<u>31.9</u>	878	32.0	880	31.9	4	<u>768</u>	<u>36.5</u>	766	36.6	769	36.5
483.xalancbmk	4	507	54.4	<u>508</u>	<u>54.4</u>	508	54.4	4	507	54.4	<u>508</u>	<u>54.4</u>	508	54.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

BIOS Settings:

Adjacent Cache Line Prefetch = Disabled (Default = Enabled)

General Notes

taskset was used to bind processes to cores except for 462.libquantum peak
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:
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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
 -opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
 -no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
 -opt-malloc-options=3 -parallel -par-runtime-control
 -opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -ansi-alias -opt-ra-region-strategy=block
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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