



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®2006 = 26.6

PowerEdge R200 (Intel Xeon X3360, 2.83 GHz)

SPECint_base2006 = 23.7

CPU2006 license: 55

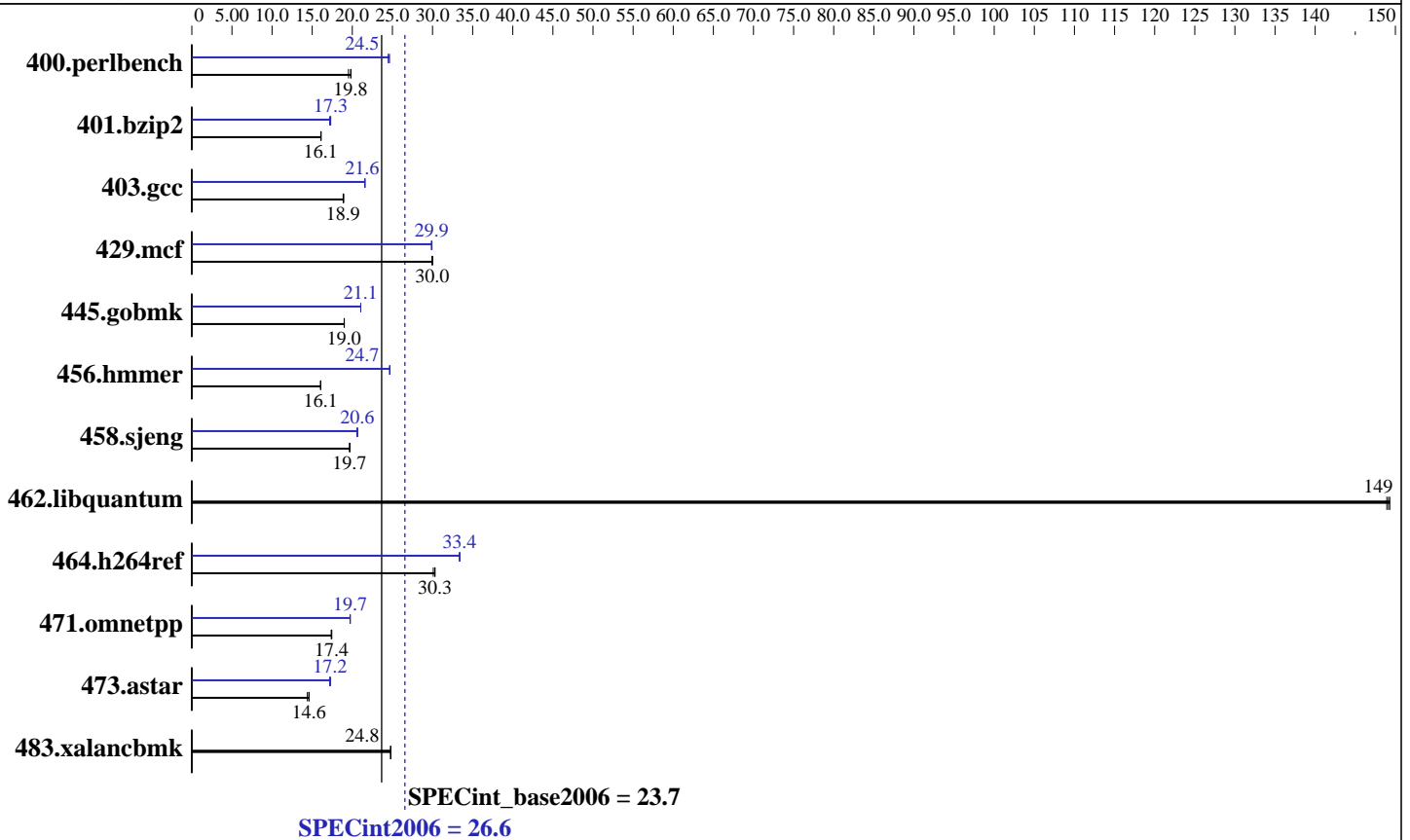
Test date: Oct-2008

Test sponsor: Dell Inc.

Hardware Availability: Mar-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X3360
 CPU Characteristics:
 CPU MHz: 2833
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2 GB DDR2-800)
 Disk Subsystem: 1 x 750 GB 7200 RPM SATA
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 26.6

PowerEdge R200 (Intel Xeon X3360, 2.83 GHz)

SPECint_base2006 = 23.7

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Oct-2008
Hardware Availability: Mar-2008
Software Availability: Nov-2008

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	500	19.5	493	19.8	<u>494</u>	<u>19.8</u>	<u>398</u>	<u>24.5</u>	399	24.5	396	24.6
401.bzip2	599	16.1	<u>600</u>	<u>16.1</u>	600	16.1	558	17.3	562	17.2	<u>558</u>	<u>17.3</u>
403.gcc	425	18.9	427	18.9	<u>427</u>	<u>18.9</u>	373	21.6	<u>373</u>	<u>21.6</u>	373	21.6
429.mcf	305	29.9	304	30.0	<u>304</u>	<u>30.0</u>	305	29.9	<u>305</u>	<u>29.9</u>	305	29.9
445.gobmk	553	19.0	<u>552</u>	<u>19.0</u>	552	19.0	498	21.1	498	21.1	<u>498</u>	<u>21.1</u>
456.hammer	581	16.1	581	16.1	<u>581</u>	<u>16.1</u>	379	24.6	<u>378</u>	<u>24.7</u>	378	24.7
458.sjeng	<u>615</u>	<u>19.7</u>	614	19.7	615	19.7	<u>586</u>	<u>20.6</u>	588	20.6	586	20.7
462.libquantum	<u>139</u>	<u>149</u>	139	149	139	149	<u>139</u>	<u>149</u>	139	149	139	149
464.h264ref	735	30.1	730	30.3	<u>730</u>	<u>30.3</u>	<u>663</u>	<u>33.4</u>	664	33.4	662	33.4
471.omnetpp	358	17.4	<u>359</u>	<u>17.4</u>	360	17.4	317	19.7	317	19.7	<u>317</u>	<u>19.7</u>
473.astar	480	14.6	<u>481</u>	<u>14.6</u>	488	14.4	406	17.3	<u>408</u>	<u>17.2</u>	408	17.2
483.xalancbmk	278	24.8	<u>278</u>	<u>24.8</u>	279	24.8	<u>278</u>	<u>24.8</u>	<u>278</u>	<u>24.8</u>	279	24.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 26.6

PowerEdge R200 (Intel Xeon X3360, 2.83 GHz)

SPECint_base2006 = 23.7

CPU2006 license: 55

Test date: Oct-2008

Test sponsor: Dell Inc.

Hardware Availability: Mar-2008

Tested by: Dell Inc.

Software Availability: Nov-2008

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 26.6

PowerEdge R200 (Intel Xeon X3360, 2.83 GHz)

SPECint_base2006 = 23.7

CPU2006 license: 55

Test date: Oct-2008

Test sponsor: Dell Inc.

Hardware Availability: Mar-2008

Tested by: Dell Inc.

Software Availability: Nov-2008

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.html>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 26.6

PowerEdge R200 (Intel Xeon X3360, 2.83 GHz)

SPECint_base2006 = 23.7

CPU2006 license: 55

Test date: Oct-2008

Test sponsor: Dell Inc.

Hardware Availability: Mar-2008

Tested by: Dell Inc.

Software Availability: Nov-2008

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 22:03:26 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 November 2008.