



SPEC® CINT2006 Result

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Intel Corporation

Supermicro SuperServer 6026T-NTR+ (Intel Xeon L5520, 2.26 GHz)

SPECint®2006 = 27.9

SPECint_base2006 = 24.7

CPU2006 license: 13

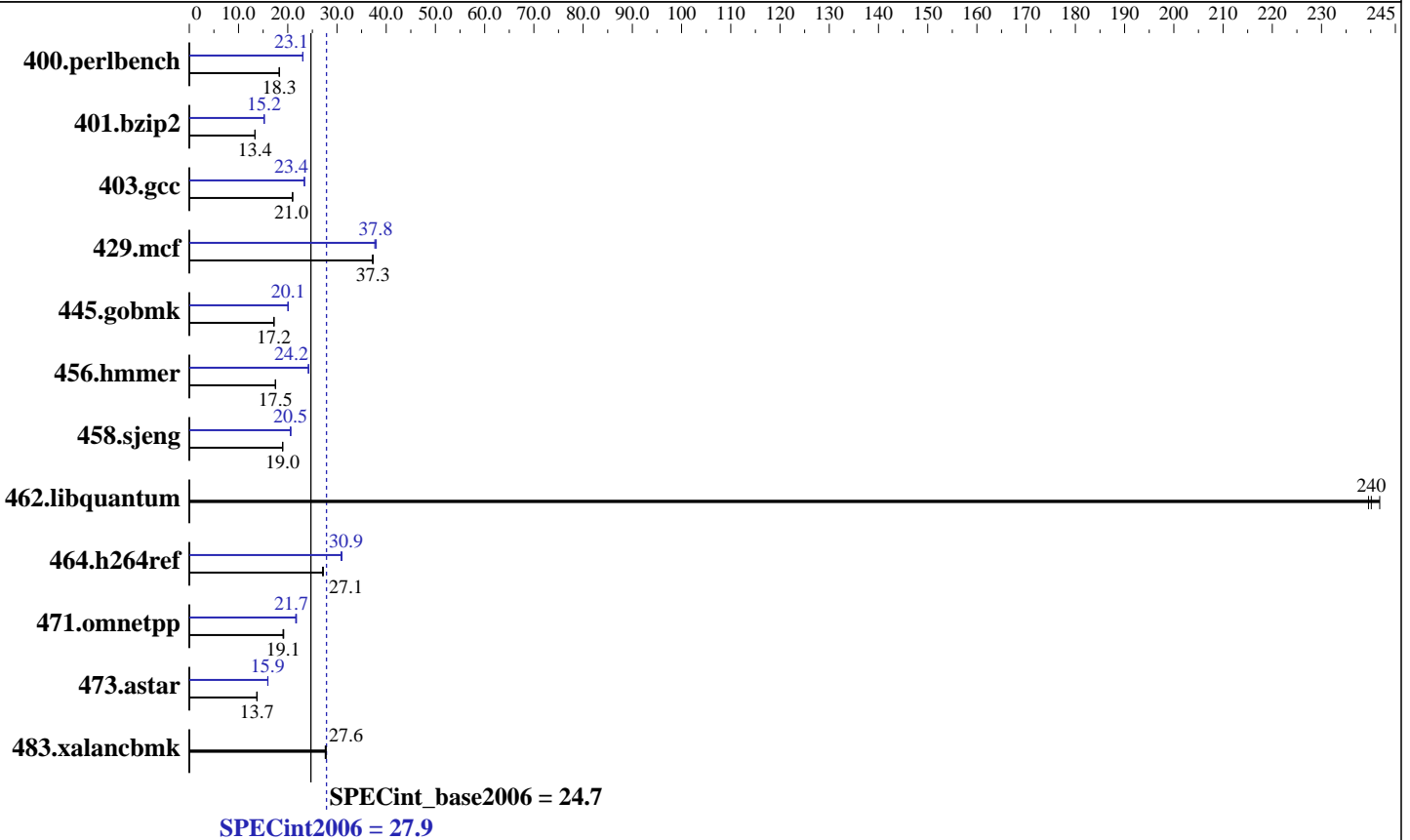
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Mar-2009

Hardware Availability: Mar-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon L5520
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (12 x 2GB DDR3-1066, CL7)
 Disk Subsystem: 1 x 150 GB SATA, 10000RPM
 Other Hardware: None

Software

Operating System: SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.34-smp for x86_64
 Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: 1_cproc_p_11.0.080
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	534	18.3	<u>534</u>	<u>18.3</u>	535	18.3	424	23.0	423	23.1	<u>423</u>	<u>23.1</u>
401.bzip2	722	13.4	724	13.3	<u>722</u>	<u>13.4</u>	<u>634</u>	<u>15.2</u>	634	15.2	635	15.2
403.gcc	<u>384</u>	<u>21.0</u>	383	21.0	384	21.0	344	23.4	<u>344</u>	<u>23.4</u>	344	23.4
429.mcf	245	37.2	<u>244</u>	<u>37.3</u>	244	37.4	240	38.0	242	37.7	<u>241</u>	<u>37.8</u>
445.gobmk	610	17.2	<u>610</u>	<u>17.2</u>	610	17.2	<u>523</u>	<u>20.1</u>	523	20.1	522	20.1
456.hammer	<u>533</u>	<u>17.5</u>	533	17.5	533	17.5	<u>386</u>	<u>24.2</u>	386	24.2	385	24.2
458.sjeng	<u>638</u>	<u>19.0</u>	639	18.9	638	19.0	586	20.6	<u>589</u>	<u>20.5</u>	589	20.5
462.libquantum	86.5	240	85.7	242	<u>86.3</u>	<u>240</u>	86.5	240	85.7	242	<u>86.3</u>	<u>240</u>
464.h264ref	813	27.2	817	27.1	<u>816</u>	<u>27.1</u>	714	31.0	<u>717</u>	<u>30.9</u>	717	30.9
471.omnetpp	326	19.2	328	19.1	<u>328</u>	<u>19.1</u>	289	21.6	287	21.8	<u>288</u>	<u>21.7</u>
473.astar	509	13.8	<u>511</u>	<u>13.7</u>	511	13.7	439	16.0	<u>440</u>	<u>15.9</u>	440	15.9
483.xalancbmk	248	27.8	<u>250</u>	<u>27.6</u>	250	27.6	248	27.8	<u>250</u>	<u>27.6</u>	250	27.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

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Peak Optimization Flags (Continued)

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 23:23:19 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 31 March 2009.