



# SPEC® CINT2006 Result

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## Fujitsu

### SPECint®\_rate2006 = 87.2

PRIMERGY TX150 S6, Intel Xeon X3380, 3.16 GHz

### SPECint\_rate\_base2006 = 81.6

CPU2006 license: 19

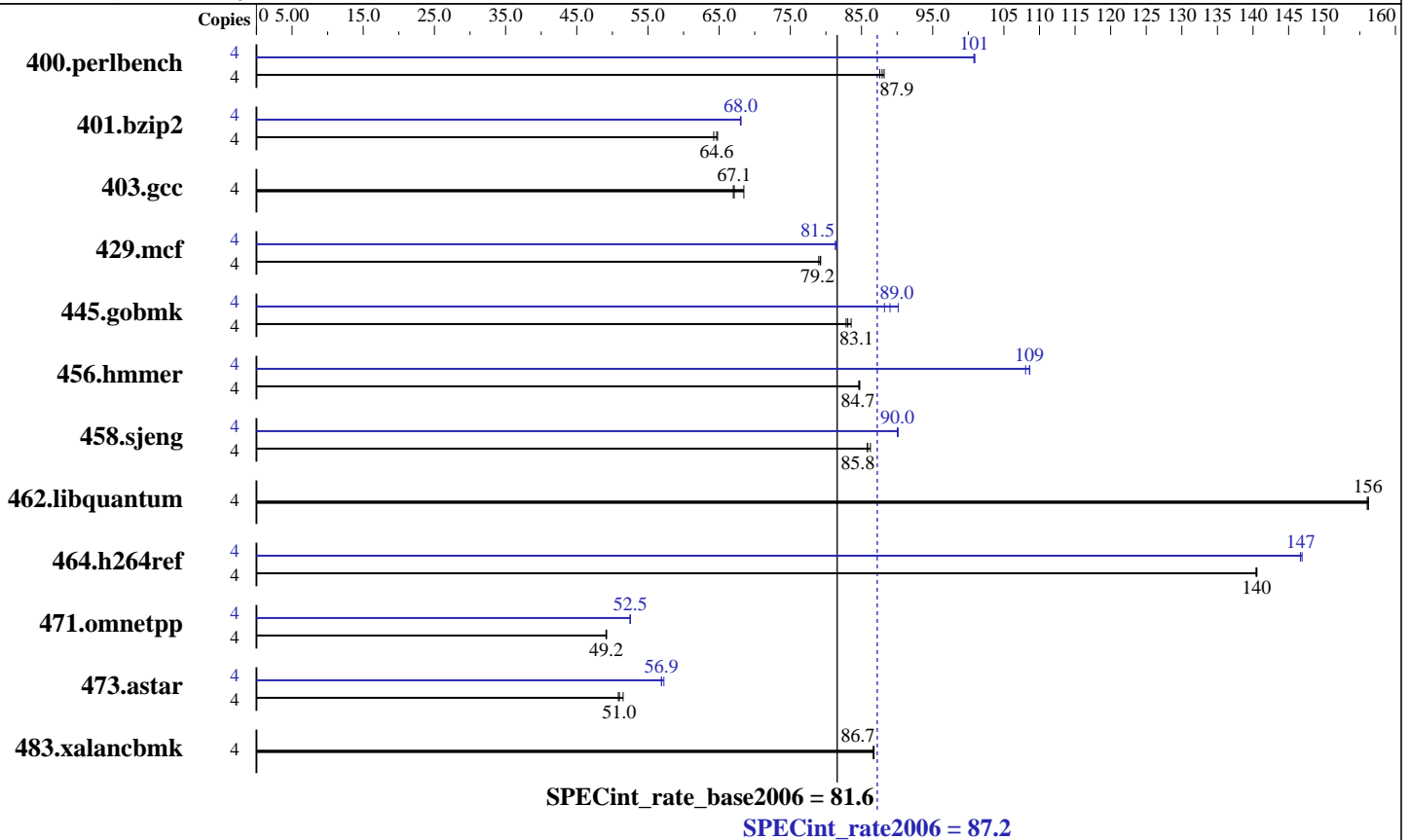
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2009

Hardware Availability: Apr-2009

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon X3380  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 3167  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 rpm  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l\_cproc\_p\_11.0.066  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>445</b>	<b>87.9</b>	446	87.6	443	88.2	4	<b>388</b>	<b>101</b>	387	101	388	101
401.bzip2	4	596	64.8	601	64.2	<b>597</b>	<b>64.6</b>	4	567	68.1	568	68.0	<b>567</b>	<b>68.0</b>
403.gcc	4	470	68.5	<b>480</b>	<b>67.1</b>	481	67.0	4	470	68.5	<b>480</b>	<b>67.1</b>	481	67.0
429.mcf	4	460	79.3	<b>461</b>	<b>79.2</b>	462	79.0	4	<b>448</b>	<b>81.5</b>	449	81.3	447	81.5
445.gobmk	4	502	83.5	507	82.8	<b>505</b>	<b>83.1</b>	4	465	90.2	<b>471</b>	<b>89.0</b>	476	88.2
456.hammer	4	440	84.8	441	84.6	<b>441</b>	<b>84.7</b>	4	345	108	<b>344</b>	<b>109</b>	344	109
458.sjeng	4	<b>564</b>	<b>85.8</b>	561	86.3	564	85.8	4	537	90.1	538	90.0	<b>538</b>	<b>90.0</b>
462.libquantum	4	<b>531</b>	<b>156</b>	531	156	531	156	4	<b>531</b>	<b>156</b>	531	156	531	156
464.h264ref	4	630	141	<b>630</b>	<b>140</b>	630	140	4	<b>603</b>	<b>147</b>	603	147	604	147
471.omnetpp	4	509	49.1	508	49.2	<b>509</b>	<b>49.2</b>	4	476	52.5	<b>476</b>	<b>52.5</b>	476	52.5
473.astar	4	552	50.8	<b>551</b>	<b>51.0</b>	545	51.5	4	491	57.2	494	56.9	<b>493</b>	<b>56.9</b>
483.xalancbmk	4	318	86.8	<b>318</b>	<b>86.7</b>	318	86.7	4	318	86.8	<b>318</b>	<b>86.7</b>	318	86.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset has been used to bind processes to cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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