



# SPEC<sup>®</sup> CINT2006 Result

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## Sun Microsystems

### SPECint<sup>®</sup>\_rate2006 = 253

### Sun Blade X6275 (Intel Xeon X5570 2.93GHz)

### SPECint\_rate\_base2006 = 238

CPU2006 license: 6

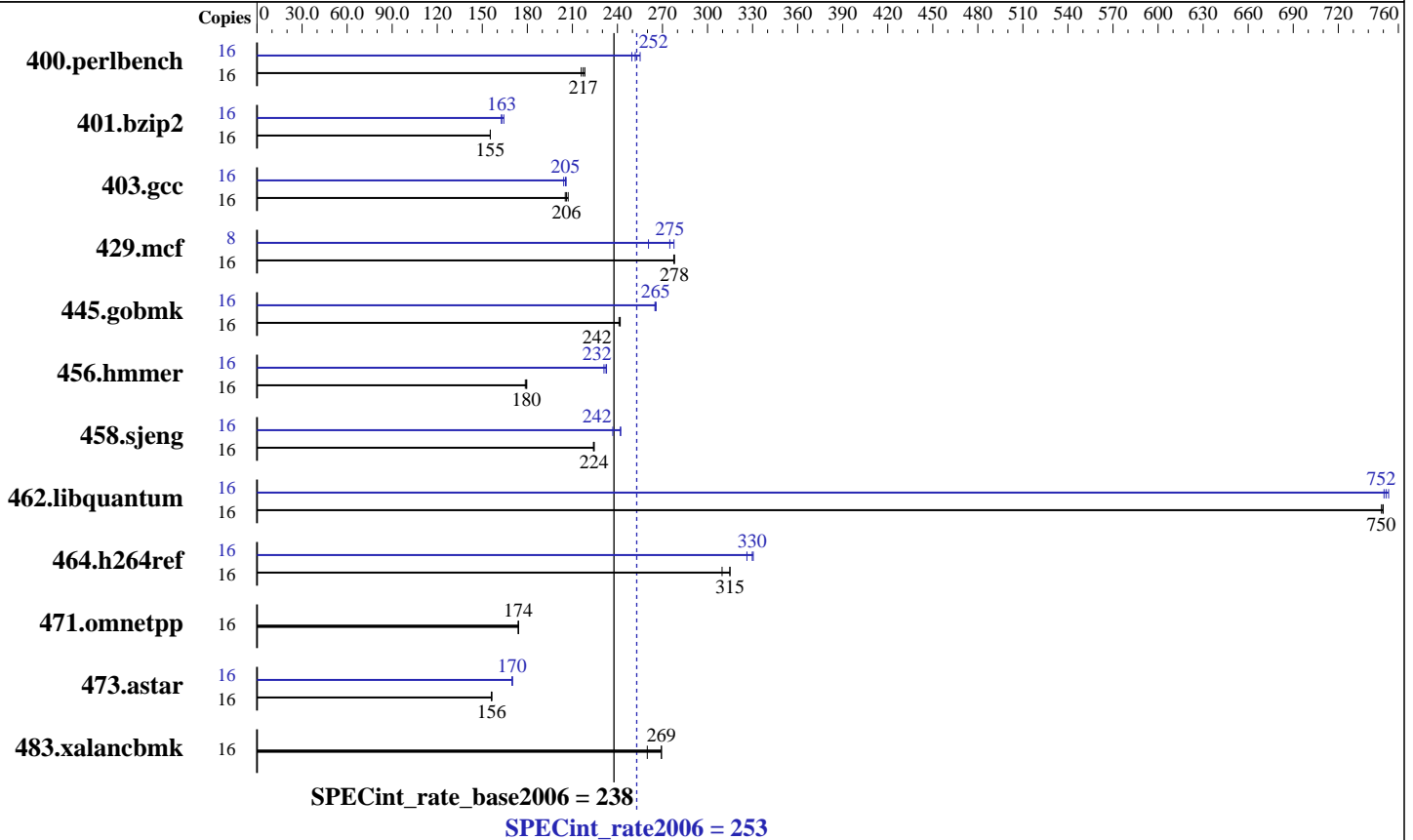
Test date: Mar-2009

Test sponsor: Sun Microsystems

Hardware Availability: Apr-2009

Tested by: Sun Microsystems

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon X5570  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 or 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB DDR3-1333)  
 Disk Subsystem: 48 x SATA 250 GB 7200 RPM via NFS for SPEC CPU2006  
 Other Hardware: None

### Software

Operating System: SuSe Linux Enterprise Server 10 (x86\_64) SP2, kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080930 Package ID: I\_cproc\_p\_11.0.066, I\_cprof\_p\_11.0.066  
 Auto Parallel: No  
 File System: NFSv3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<b>720</b>	<b>217</b>	724	216	716	218	16	626	250	613	255	<b>621</b>	<b>252</b>
401.bzip2	16	994	155	994	155	<b>994</b>	<b>155</b>	16	<b>948</b>	<b>163</b>	948	163	939	164
403.gcc	16	628	205	<b>625</b>	<b>206</b>	622	207	16	626	206	631	204	<b>627</b>	<b>205</b>
429.mcf	16	<b>525</b>	<b>278</b>	525	278	526	277	8	280	261	263	278	<b>265</b>	<b>275</b>
445.gobmk	16	696	241	694	242	<b>694</b>	<b>242</b>	16	632	266	633	265	<b>633</b>	<b>265</b>
456.hammer	16	831	180	<b>832</b>	<b>180</b>	836	179	16	<b>642</b>	<b>232</b>	641	233	646	231
458.sjeng	16	862	225	<b>863</b>	<b>224</b>	864	224	16	817	237	<b>800</b>	<b>242</b>	799	242
462.libquantum	16	443	749	442	750	<b>442</b>	<b>750</b>	16	442	750	440	754	<b>441</b>	<b>752</b>
464.h264ref	16	<b>1124</b>	<b>315</b>	1124	315	1143	310	16	<b>1074</b>	<b>330</b>	1085	326	1072	330
471.omnetpp	16	575	174	574	174	<b>574</b>	<b>174</b>	16	575	174	574	174	<b>574</b>	<b>174</b>
473.astar	16	<b>719</b>	<b>156</b>	718	156	720	156	16	<b>661</b>	<b>170</b>	661	170	660	170
483.xalancbmk	16	410	269	<b>410</b>	<b>269</b>	425	260	16	410	269	<b>410</b>	<b>269</b>	425	260

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores  
"(For details, please see the config file.)"

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

Default BIOS settings used.

## General Notes

NFS for file system: NFS server, Sun Fire X4540 equipped with 48 x 250GB SATA 7200 RPM, serves the client over Gigabit ethernet connection.

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc  
456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:  
icpc



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
 -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
 -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

473.astar (continued):

-L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.07.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.07.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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