



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = 258

IBM Power 550 (5.0 GHz, 8 core, SLES)

SPECint_rate_base2006 = 216

CPU2006 license: 11

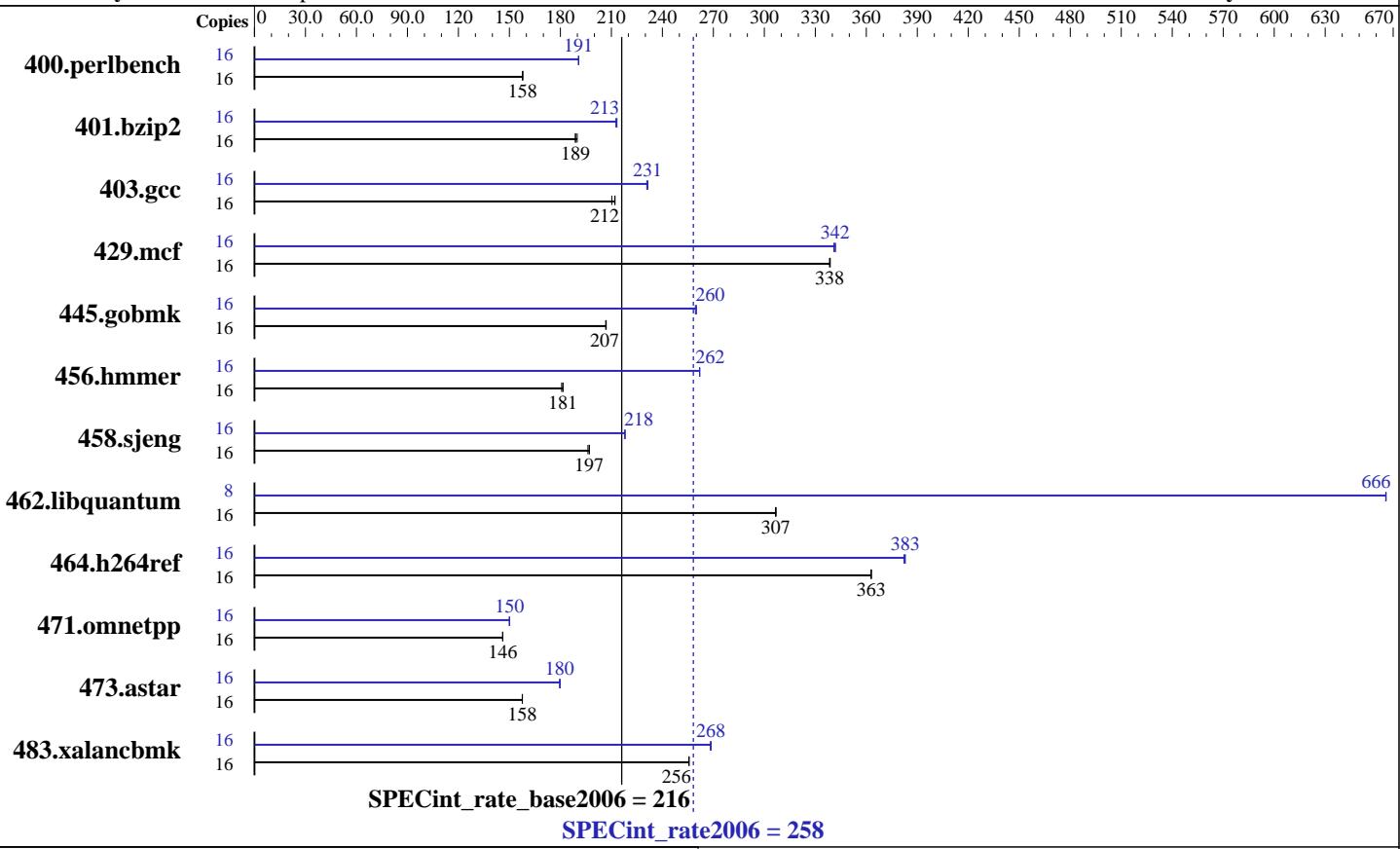
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Mar-2009

Hardware Availability: May-2009

Software Availability: Mar-2009



Hardware

CPU Name: POWER6+
CPU Characteristics:
CPU MHz: 5000
FPU: Integrated
CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable: 2,4,6,8 cores
Primary Cache: 64 KB I + 64 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per core
L3 Cache: 32 MB I+D off chip per chip
Other Cache: None
Memory: 128 GB (32x4 GB) DDR2 667 MHz
Disk Subsystem: 2x146 GB SAS 15K RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11
Compiler: IBM XL C/C++ for Linux, V10.1
Updated with the Mar2009 PTF.
Auto Parallel: No
File System: ext3
System State: Run Level 3 (Multi-User)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: -Post-Link Optimization for Linux on POWER, Version 5.4.0-21
-MicroQuill SmartHeap 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	991	158	991	158	990	158	16	820	191	820	191	820	191
401.bzip2	16	813	190	818	189	817	189	16	725	213	724	213	726	213
403.gcc	16	613	210	607	212	607	212	16	558	231	557	231	557	231
429.mcf	16	431	338	431	338	431	339	16	427	342	427	342	428	341
445.gobmk	16	812	207	811	207	811	207	16	645	260	646	260	647	259
456.hammer	16	822	182	823	181	826	181	16	570	262	570	262	570	262
458.sjeng	16	982	197	983	197	987	196	16	888	218	888	218	888	218
462.libquantum	16	1080	307	1081	307	1081	307	8	249	666	249	666	249	666
464.h264ref	16	976	363	976	363	976	363	16	925	383	926	382	925	383
471.omnetpp	16	685	146	685	146	685	146	16	666	150	667	150	666	150
473.astar	16	713	158	712	158	713	158	16	625	180	625	180	624	180
483.xalancbmk	16	432	256	432	256	432	256	16	411	268	411	269	411	268

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Benchmarks bound to a processor using numactl on the submit command.

General Notes

kernel release 2.6.27.19-5-ppc64.

See flags file for details on following settings.

ulimit -s (stack) set to 1048576.

System configured with libhugetlbfs library for application access to large pages
Large pages reserved as follows by root user:

echo 1600 > /proc/sys/vm/nr_hugepages

Environment variables set before executing benchmarks.

export HUGETLB_VERBOSE=0

export HUGETLB_MORECORE=yes

export XLRTEOPTS=intinthds=1

IBM Post-Link Optimization tool was used for these benchmarks, with options:

400.perlbench : "-imullX" (instrumentation phase), "-O4 -omullX" (optimization phase)

401.bzip2 : same as 400.perlbench

403.gcc : same as 400.perlbench

456.hammer : same as 400.perlbench

458.sjeng : same as 400.perlbench

483.xalancbmk : same as 400.perlbench

429.mcf : "-imullX" (instrumentation phase), "-bf -dp -hr -las -pca -RC -RD

-rmte -si -tlo -A 64 -isf 104 -lu 8 -rt 0.16

-hrf 0.18 -ihf 40 -sdp 6 -sdpm 128 -shci 65 -si -sidf 45 -omullX" (optimization phase)

445.gobmk : "-imullX" (instrumentation phase), "-q -O3 -A 32 -omullX" (optimization phase)

462.libquantum : "-imullX" (instrumentation phase), "-bf -dp -lro -nop -RC -RD -tb -tlo -vro -A 4

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General Notes (Continued)

```
-isf 88 -lu 8 -hrf 0.10 -sdp 4 -lun 27 -omullX" (optimization phase)
473.astar : "-imullX" (instrumentation phase), "-O4 -omullX -see 1" (optimization phase)
464.h264ref : "-O4" (optimization phase)
```

Base Compiler Invocation

C benchmarks:

```
xlc -qlanglvl=extc99
```

C++ benchmarks:

```
xlc
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_PPC

462.libquantum: -DSPEC_CPU_LINUX

464.h264ref: -qchars=signed

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

```
-O5 -qarch=pwr6 -qtune=pwr6 -qalias=noansi -qalloc -lhugetlbfs
```

C++ benchmarks:

```
-O5 -qarch=pwr6 -qtune=pwr6 -qrtti -lsmartheap
```

Base Other Flags

C benchmarks:

```
-qipa=noobject -qipa=threads
```

C++ benchmarks:

```
-qipa=noobject -qipa=threads
```

Peak Compiler Invocation

C benchmarks:

```
xlc -qlanglvl=extc99
```

C++ benchmarks:

```
xlc
```



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_PPC

403.gcc: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

464.h264ref: -qchars=signed

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -Wl,-q -qpdf1(pass 1) -qpdf2(pass 2) -O4 -qarch=pwr6
-qtune=pwr6 -qalias=noansi -lsmartheap

401.bzip2: -Wl,-q -qpdf1(pass 1) -qpdf2(pass 2) -O3 -qarch=pwr6
-qtune=pwr6 -lhugetlbfs

403.gcc: -Wl,-q -qpdf1(pass 1) -qpdf2(pass 2) -O4 -qarch=pwr6
-qtune=pwr6 -qalloc -q64 -lhugetlbfs

429.mcf: -Wl,-q -O5 -qarch=pwr6 -qtune=pwr6 -qnoenablevmx
-lhugetlbfs

445.gobmk: -Wl,-q -qpdf1(pass 1) -qpdf2(pass 2) -O4 -qarch=pwr6
-qtune=pwr6 -qnoenablevmx -lhugetlbfs

456.hmmr: -Wl,-q -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qarch=pwr6
-qtune=pwr6 -lhugetlbfs

458.sjeng: -Wl,-q -O5 -qarch=pwr6 -qtune=pwr6 -lhugetlbfs

462.libquantum: -Wl,-q -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qarch=pwr6
-qtune=pwr6 -qnoenablevmx -q64 -lhugetlbfs

464.h264ref: -Wl,-q -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qarch=pwr6
-qtune=pwr6 -q64 -lhugetlbfs

C++ benchmarks:

471.omnetpp: -qpdf1(pass 1) -qpdf2(pass 2) -O4 -qarch=pwr6 -qtune=pwr6
-qrtti -lsmartheap

473.astar: -Wl,-q -qpdf1(pass 1) -qpdf2(pass 2) -O4 -qarch=pwr6
-qtune=pwr6 -qnoenablevmx -lsmartheap

483.xalancbmk: -Wl,-q -O5 -qarch=pwr6 -qtune=pwr6 -lsmartheap



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Peak Other Flags

C benchmarks:

-qipa=noobject -qipa=threads

C++ benchmarks:

-qipa=noobject -qipa=threads

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/IBM-Linux-XL.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/IBM-Linux-XL.xml>

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For other inquiries, please contact webmaster@spec.org.

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