



SPEC® CINT2006 Result

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GIGA-BYTE Technology Co. Ltd.

(Test Sponsor: Intel Corporation)

Gigabyte MA78GM-S2H Motherboard (AMD Athlon X2 7750)

SPECint®_rate2006 = 26.2

SPECint_rate_base2006 = 25.1

CPU2006 license: 13

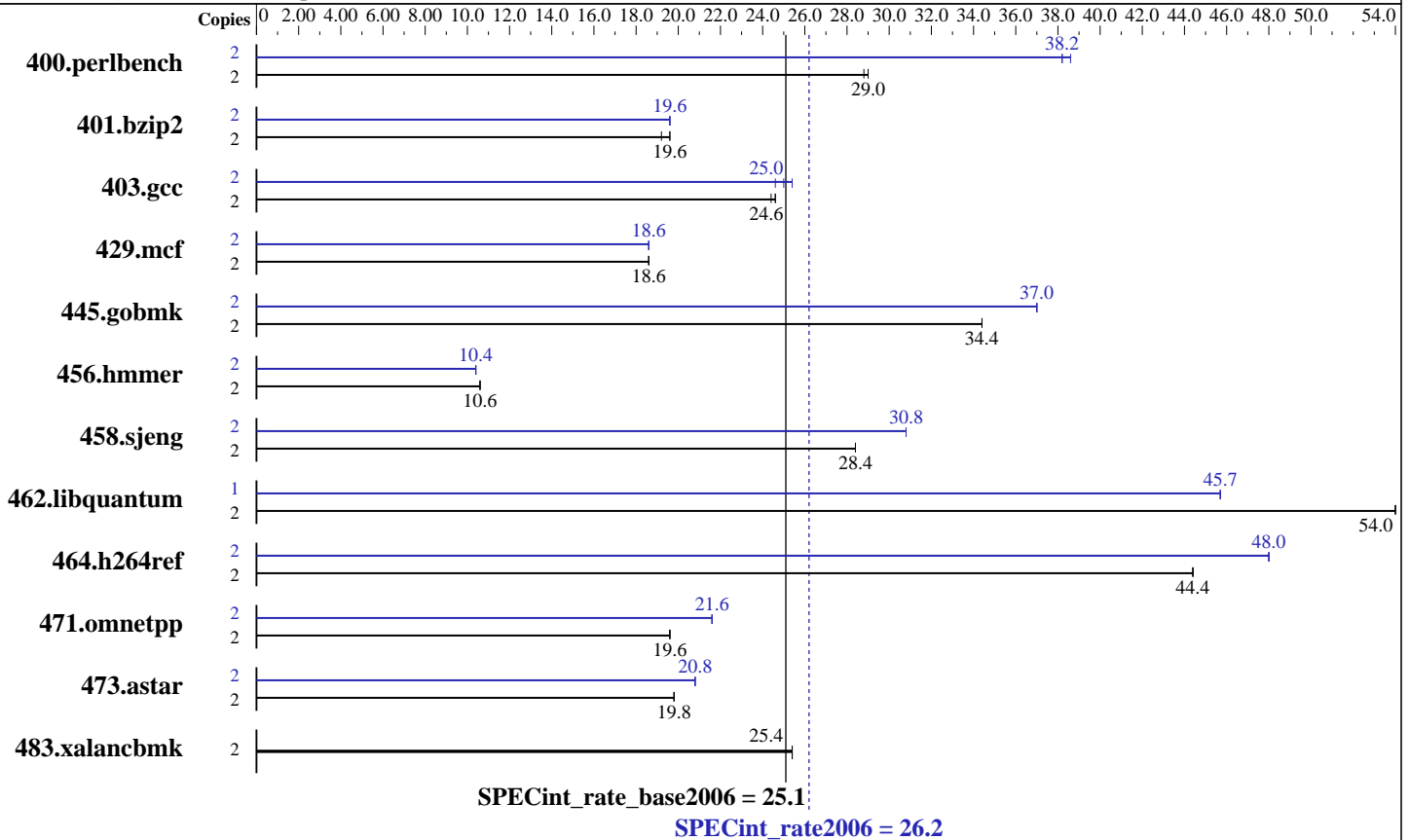
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2009

Hardware Availability: Dec-2008

Software Availability: Nov-2008



Hardware

CPU Name: AMD Athlon X2 7750
 CPU Characteristics:
 CPU MHz: 2700
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core
 L3 Cache: 2 MB I+D on chip per chip
 Other Cache: None
 Memory: 4 GB (4x1GB DDR2-800 CL5)
 Disk Subsystem: Seagate 320 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cproc_p_11.0.054
 Microsoft Visual Studio 2008 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	676	29.0	679	28.8	676	29.0	2	512	38.2	505	38.6	511	38.2
401.bzip2	2	983	19.6	987	19.6	1003	19.2	2	989	19.6	989	19.6	987	19.6
403.gcc	2	653	24.6	654	24.6	660	24.4	2	642	25.0	653	24.6	635	25.4
429.mcf	2	984	18.6	982	18.6	982	18.6	2	983	18.6	984	18.6	983	18.6
445.gobmk	2	609	34.4	609	34.4	610	34.4	2	566	37.0	566	37.0	566	37.0
456.hammer	2	1752	10.6	1754	10.6	1753	10.6	2	1779	10.4	1779	10.4	1780	10.4
458.sjeng	2	852	28.4	852	28.4	852	28.4	2	786	30.8	786	30.8	786	30.8
462.libquantum	2	768	54.0	767	54.0	767	54.0	1	453	45.7	453	45.7	453	45.7
464.h264ref	2	996	44.4	995	44.4	995	44.4	2	922	48.0	923	48.0	923	48.0
471.omnetpp	2	640	19.6	640	19.6	640	19.6	2	579	21.6	579	21.6	579	21.6
473.astar	2	709	19.8	708	19.8	708	19.8	2	674	20.8	673	20.8	674	20.8
483.xalancbmk	2	542	25.4	544	25.4	543	25.4	2	542	25.4	544	25.4	543	25.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Binaries were built on Windows Vista Ultimate (32-bit)
OMP_NUM_THREADS set to number of logical processors as seen by the OS
KMP_AFFINITY set to physical,0
submit disabled for 462.libquantum peak

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qc99
C++ benchmarks:
icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

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Base Portability Flags (Continued)

483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

C++ benchmarks:

/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

401.bzip2: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
/F512000000

403.gcc: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

429.mcf: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

445.gobmk: /arch:SSE2 -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmcr: /arch:SSE2 -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

458.sjeng: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qparallel -Qpar-runtime-control -Qvec-guard-write
/F512000000

464.h264ref: Same as 456.hmmcr

C++ benchmarks:

471.omnetpp: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

473.astar: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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