



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

### SPECint®\_rate2006 = 142

### PRIMERGY BX620 S5, Intel Xeon E5506, 2.13 GHz

### SPECint\_rate\_base2006 = 132

CPU2006 license: 19

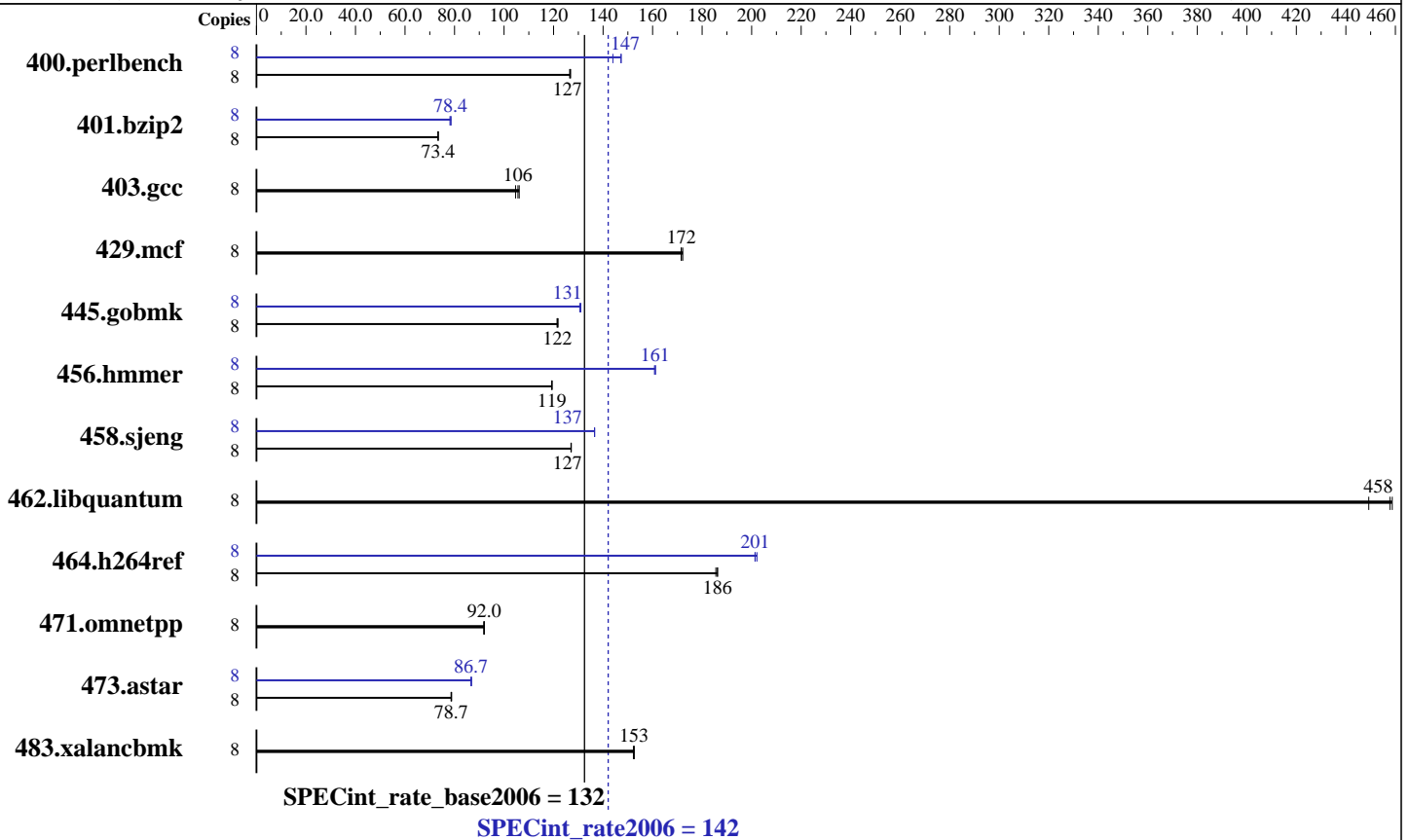
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2009

Hardware Availability: Jun-2009

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon E5506  
 CPU Characteristics:  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SAS, 73 GB, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

SPECint\_rate2006 = 142

PRIMERGY BX620 S5, Intel Xeon E5506, 2.13 GHz

SPECint\_rate\_base2006 = 132

CPU2006 license: 19  
Test sponsor: Fujitsu  
Tested by: Fujitsu

Test date: May-2009  
Hardware Availability: Jun-2009  
Software Availability: Feb-2009

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b>617</b>	<b>127</b>	618	126	616	127	8	543	144	<b>531</b>	<b>147</b>	531	147
401.bzip2	8	1053	73.3	1052	73.4	<b>1052</b>	<b>73.4</b>	8	982	78.6	<b>984</b>	<b>78.4</b>	988	78.2
403.gcc	8	607	106	<b>610</b>	<b>106</b>	615	105	8	607	106	<b>610</b>	<b>106</b>	615	105
429.mcf	8	<b>425</b>	<b>172</b>	424	172	425	172	8	<b>425</b>	<b>172</b>	424	172	425	172
445.gobmk	8	691	121	<b>691</b>	<b>122</b>	689	122	8	<b>641</b>	<b>131</b>	641	131	643	131
456.hammer	8	626	119	<b>625</b>	<b>119</b>	625	119	8	463	161	<b>463</b>	<b>161</b>	464	161
458.sjeng	8	762	127	<b>762</b>	<b>127</b>	761	127	8	<b>709</b>	<b>137</b>	709	137	709	137
462.libquantum	8	<b>362</b>	<b>458</b>	361	459	369	449	8	<b>362</b>	<b>458</b>	361	459	369	449
464.h264ref	8	951	186	<b>951</b>	<b>186</b>	954	186	8	876	202	<b>879</b>	<b>201</b>	879	201
471.omnetpp	8	<b>543</b>	<b>92.0</b>	545	91.8	543	92.1	8	<b>543</b>	<b>92.0</b>	545	91.8	543	92.1
473.astar	8	715	78.6	712	78.8	<b>714</b>	<b>78.7</b>	8	646	86.9	648	86.7	<b>648</b>	<b>86.7</b>
483.xalancbmk	8	362	153	363	152	<b>362</b>	<b>153</b>	8	362	153	363	152	<b>362</b>	<b>153</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 800 MHz.

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint\_rate2006 = 142

PRIMERGY BX620 S5, Intel Xeon E5506, 2.13 GHz

SPECint\_rate\_base2006 = 132

CPU2006 license: 19  
Test sponsor: Fujitsu  
Tested by: Fujitsu

Test date: May-2009  
Hardware Availability: Jun-2009  
Software Availability: Feb-2009

## Base Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint\_rate2006 = 142

PRIMERGY BX620 S5, Intel Xeon E5506, 2.13 GHz

SPECint\_rate\_base2006 = 132

CPU2006 license: 19  
Test sponsor: Fujitsu  
Tested by: Fujitsu

Test date: May-2009  
Hardware Availability: Jun-2009  
Software Availability: Feb-2009

## Peak Portability Flags (Continued)

473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: basepeak = yes  
429.mcf: basepeak = yes  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: basepeak = yes  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64  
483.xalancbmk: basepeak = yes



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECint\_rate2006 = 142**

PRIMERGY BX620 S5, Intel Xeon E5506, 2.13 GHz

**SPECint\_rate\_base2006 = 132**

**CPU2006 license:** 19

**Test sponsor:** Fujitsu

**Tested by:** Fujitsu

**Test date:** May-2009

**Hardware Availability:** Jun-2009

**Software Availability:** Feb-2009

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 02:44:59 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 15 September 2009.