



# SPEC<sup>®</sup> CINT2006 Result

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## Fujitsu

SPECint<sup>®</sup>\_rate2006 = 36.1

PRIMERGY BX620 S5, Intel Xeon E5502, 1.87 GHz

SPECint\_rate\_base2006 = 33.6

CPU2006 license: 19

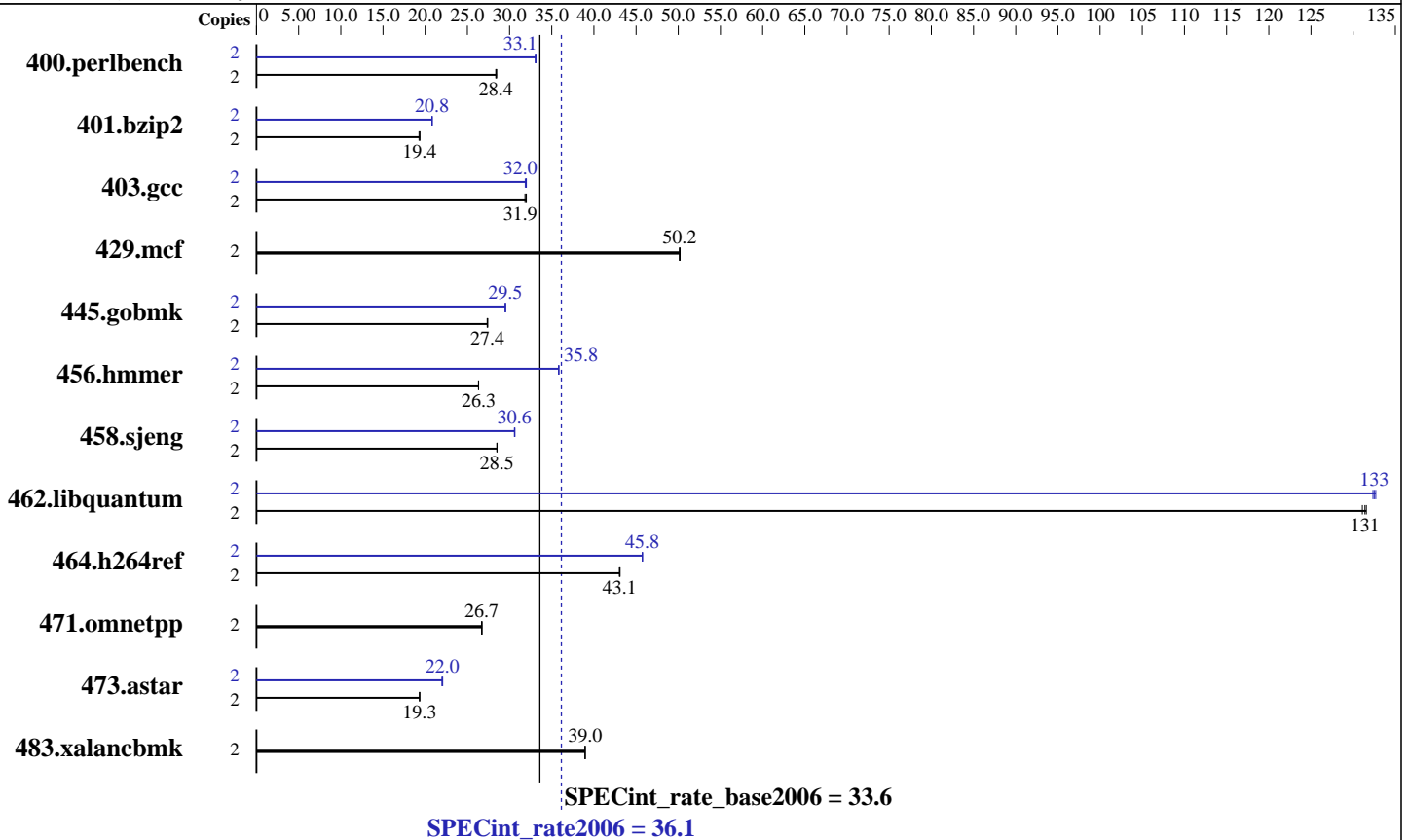
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon E5502  
 CPU Characteristics:  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6x4 GB PC3-8500R, 2 rank, CL7-7-7, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	689	28.4	686	28.5	<b>688</b>	<b>28.4</b>	2	591	33.0	590	33.1	<b>590</b>	<b>33.1</b>
401.bzip2	2	1002	19.3	<b>997</b>	<b>19.4</b>	997	19.4	2	<b>927</b>	<b>20.8</b>	926	20.8	928	20.8
403.gcc	2	<b>504</b>	<b>31.9</b>	503	32.0	506	31.8	2	503	32.0	<b>503</b>	<b>32.0</b>	505	31.9
429.mcf	2	363	50.2	364	50.1	<b>364</b>	<b>50.2</b>	2	363	50.2	364	50.1	<b>364</b>	<b>50.2</b>
445.gobmk	2	767	27.3	766	27.4	<b>766</b>	<b>27.4</b>	2	<b>711</b>	<b>29.5</b>	711	29.5	712	29.5
456.hammer	2	709	26.3	<b>709</b>	<b>26.3</b>	709	26.3	2	521	35.8	521	35.8	<b>521</b>	<b>35.8</b>
458.sjeng	2	<b>849</b>	<b>28.5</b>	848	28.5	849	28.5	2	791	30.6	790	30.6	<b>791</b>	<b>30.6</b>
462.libquantum	2	<b>315</b>	<b>131</b>	315	132	316	131	2	<b>313</b>	<b>133</b>	313	132	312	133
464.h264ref	2	1027	43.1	1029	43.0	<b>1028</b>	<b>43.1</b>	2	968	45.7	967	45.8	<b>967</b>	<b>45.8</b>
471.omnetpp	2	467	26.7	468	26.7	<b>468</b>	<b>26.7</b>	2	467	26.7	468	26.7	<b>468</b>	<b>26.7</b>
473.astar	2	726	19.3	<b>726</b>	<b>19.3</b>	726	19.3	2	639	22.0	<b>637</b>	<b>22.0</b>	636	22.1
483.xalancbmk	2	354	39.0	<b>354</b>	<b>39.0</b>	354	39.0	2	354	39.0	<b>354</b>	<b>39.0</b>	354	39.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 800 MHz.

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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