



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]2006 = 32.8

PowerEdge R510 (Intel Xeon E5530, 2.40 GHz)

SPECfp_base2006 = 30.8

CPU2006 license: 55

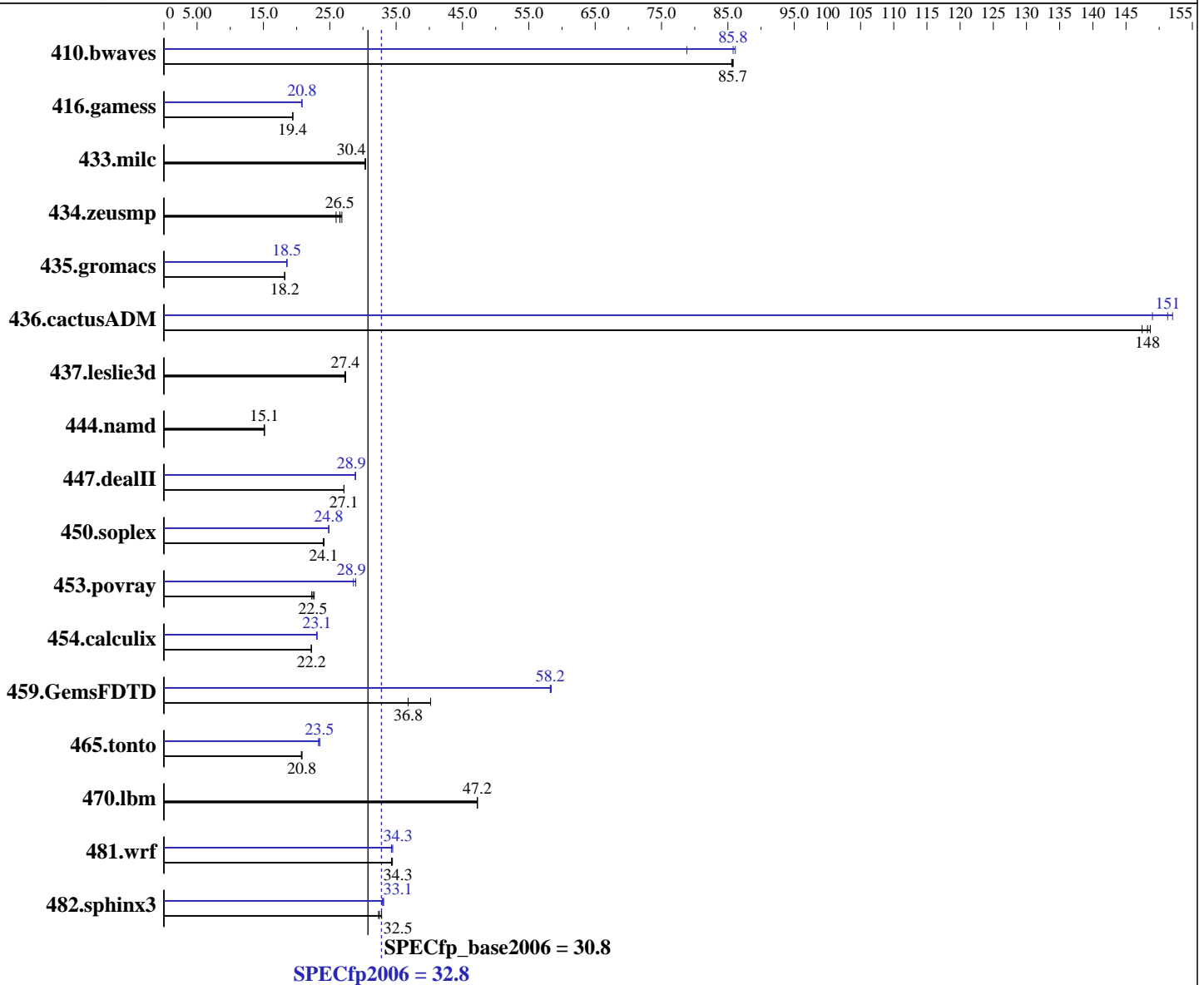
Test date: Sep-2009

Test sponsor: Dell Inc.

Hardware Availability: Oct-2009

Tested by: Dell Inc.

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5530
 CPU Characteristics: Intel Turbo Boost Technology up to 2.66 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ and Fortran Compiler Professional 11.0 for Linux
 Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4 GB DDR3-1333 DR RDIMM downclocked to 1066 MHz)
Disk Subsystem: 1 x 500 GB 7200 RPM SATA
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>159</u>	<u>85.7</u>	158	85.8	159	85.6	172	78.8	158	86.1	<u>158</u>	<u>85.8</u>
416.gamess	<u>1009</u>	<u>19.4</u>	1007	19.4	1010	19.4	<u>941</u>	<u>20.8</u>	944	20.8	941	20.8
433.milc	302	30.4	<u>302</u>	<u>30.4</u>	303	30.3	302	30.4	<u>302</u>	<u>30.4</u>	303	30.3
434.zeusmp	<u>343</u>	<u>26.5</u>	339	26.8	351	25.9	<u>343</u>	<u>26.5</u>	339	26.8	351	25.9
435.gromacs	392	18.2	<u>392</u>	<u>18.2</u>	393	18.2	<u>385</u>	<u>18.5</u>	386	18.5	385	18.6
436.cactusADM	<u>80.6</u>	<u>148</u>	80.4	149	81.1	147	80.2	149	78.6	152	<u>79.0</u>	<u>151</u>
437.leslie3d	<u>344</u>	<u>27.4</u>	343	27.4	345	27.3	<u>344</u>	<u>27.4</u>	343	27.4	345	27.3
444.namd	531	15.1	529	15.2	<u>529</u>	<u>15.1</u>	531	15.1	529	15.2	<u>529</u>	<u>15.1</u>
447.dealII	421	27.1	422	27.1	<u>422</u>	<u>27.1</u>	396	28.9	<u>396</u>	<u>28.9</u>	397	28.8
450.soplex	346	24.1	<u>347</u>	<u>24.1</u>	347	24.0	<u>336</u>	<u>24.8</u>	336	24.8	335	24.9
453.povray	239	22.3	<u>237</u>	<u>22.5</u>	235	22.7	186	28.6	<u>184</u>	<u>28.9</u>	184	28.9
454.calculix	372	22.2	371	22.2	<u>371</u>	<u>22.2</u>	357	23.1	358	23.1	<u>358</u>	<u>23.1</u>
459.GemsFDTD	264	40.2	288	36.8	<u>288</u>	<u>36.8</u>	182	58.4	<u>182</u>	<u>58.2</u>	182	58.2
465.tonto	<u>474</u>	<u>20.8</u>	473	20.8	475	20.7	423	23.3	<u>419</u>	<u>23.5</u>	419	23.5
470.lbm	<u>291</u>	<u>47.2</u>	291	47.3	291	47.2	<u>291</u>	<u>47.2</u>	291	47.3	291	47.2
481.wrf	326	34.3	324	34.4	<u>325</u>	<u>34.3</u>	324	34.5	<u>325</u>	<u>34.3</u>	326	34.3
482.sphinx3	594	32.8	603	32.3	<u>600</u>	<u>32.5</u>	<u>589</u>	<u>33.1</u>	589	33.1	592	32.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel -auto-ilp32



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090805.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090805.01.xml>

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