



SPEC® CINT2006 Result

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Bull SAS

SPECint®_rate2006 = 88.6

NovaScale T810 F2 (Intel Xeon X3430, 2.40 GHz)

SPECint_rate_base2006 = 81.1

CPU2006 license: 20

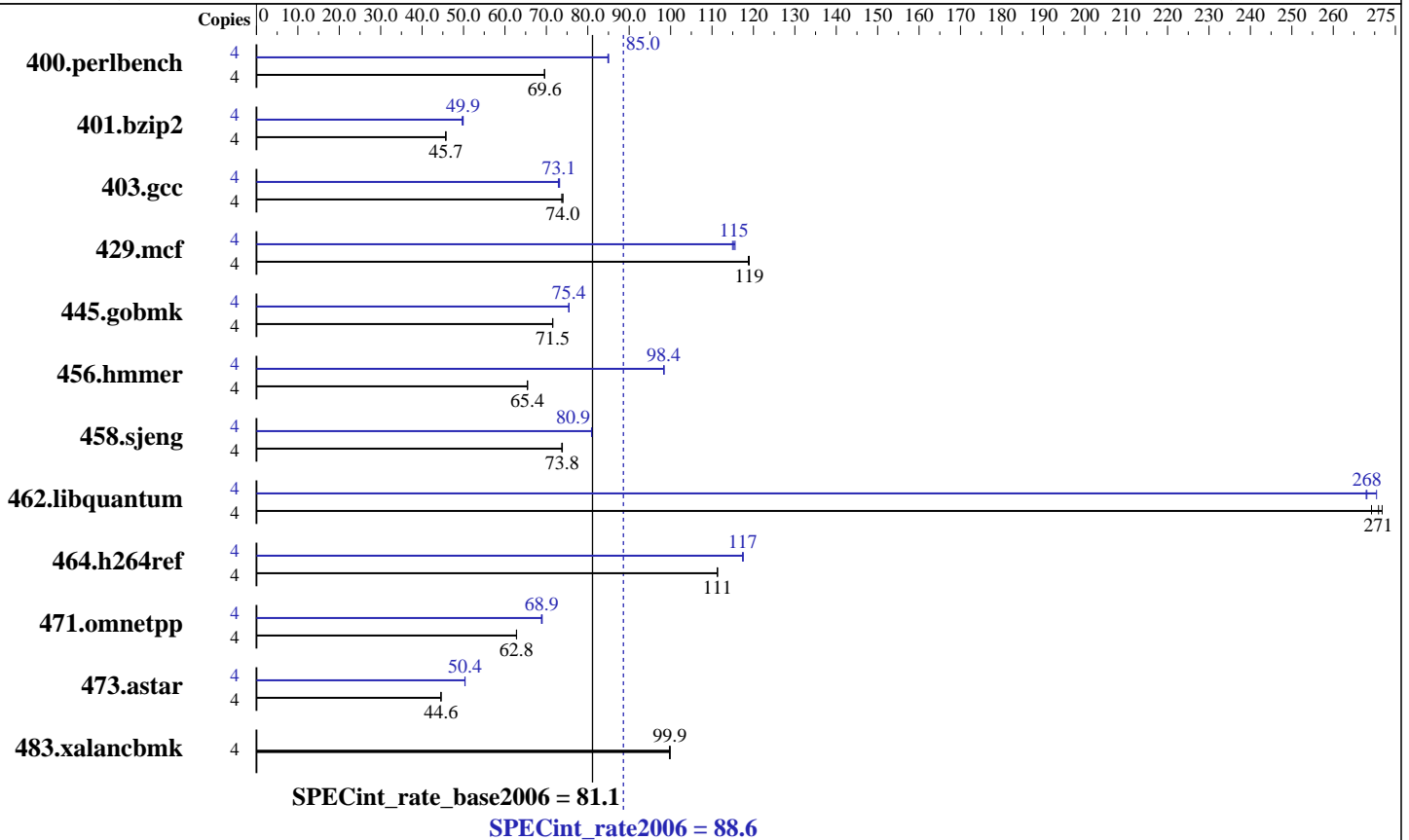
Test date: Oct-2009

Test sponsor: Bull SAS

Hardware Availability: Dec-2009

Tested by: Dell Inc.

Software Availability: Jul-2009



Hardware

CPU Name: Intel Xeon X3430
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (4 x 2 GB DDR3-1333 DR UDIMM)
 Disk Subsystem: 1 x 160 GB 7200 RPM SATA
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.3, Kernel 2.6.18-128.el5
 Compiler: Intel C++ Compiler Professional Edition 11.1 for Linux Build 20090511 Package ID: I_cproc_p_11.1.040
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	562	69.6	562	69.6	<u>562</u>	<u>69.6</u>	4	<u>460</u>	<u>85.0</u>	460	85.0	461	84.9
401.bzip2	4	845	45.7	844	45.7	<u>844</u>	<u>45.7</u>	4	<u>774</u>	<u>49.9</u>	777	49.7	773	49.9
403.gcc	4	437	73.7	435	74.0	<u>435</u>	<u>74.0</u>	4	440	73.2	<u>441</u>	<u>73.1</u>	442	72.9
429.mcf	4	307	119	307	119	<u>307</u>	<u>119</u>	4	317	115	<u>316</u>	<u>115</u>	316	116
445.gobmk	4	586	71.6	<u>587</u>	<u>71.5</u>	587	71.5	4	556	75.5	556	75.4	<u>556</u>	<u>75.4</u>
456.hammer	4	571	65.4	<u>570</u>	<u>65.4</u>	570	65.5	4	<u>379</u>	<u>98.4</u>	379	98.4	379	98.4
458.sjeng	4	<u>656</u>	<u>73.8</u>	656	73.7	655	73.9	4	598	81.0	<u>598</u>	<u>80.9</u>	598	80.9
462.libquantum	4	305	272	<u>306</u>	<u>271</u>	308	269	4	306	270	<u>309</u>	<u>268</u>	309	268
464.h264ref	4	796	111	795	111	<u>795</u>	<u>111</u>	4	754	117	<u>754</u>	<u>117</u>	753	118
471.omnetpp	4	<u>398</u>	<u>62.8</u>	398	62.8	398	62.8	4	362	69.0	363	68.8	<u>363</u>	<u>68.9</u>
473.astar	4	630	44.6	630	44.6	<u>630</u>	<u>44.6</u>	4	558	50.3	557	50.4	<u>558</u>	<u>50.4</u>
483.xalancbmk	4	277	99.7	<u>276</u>	<u>99.9</u>	276	99.9	4	277	99.7	<u>276</u>	<u>99.9</u>	276	99.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)

General Notes

The Dell PowerEdge T110 (Intel Xeon X3430, 2.40 GHz) and the Bull NovaScale T810 F2 (Intel Xeon X3430, 2.40 GHz) models are electronically equivalent. The results have been measured on a Dell PowerEdge T110 (Intel Xeon X3430, 2.40 GHz) model.

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -opt-prefetch -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
          -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
          -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
            -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
            -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
                -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

```

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Peak Optimization Flags (Continued)

471.omnetpp (continued):

`-L/spec/cpu2006.1.1/lib -lsmartheap`

473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`

`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`

`-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32`

`-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

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