



# SPEC<sup>®</sup> CINT2006 Result

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## Cisco Systems

SPECint<sup>®</sup>\_rate2006 = 243

## UCS C200 M1 (Intel Xeon X5570)

SPECint\_rate\_base2006 = 226

CPU2006 license: 9019

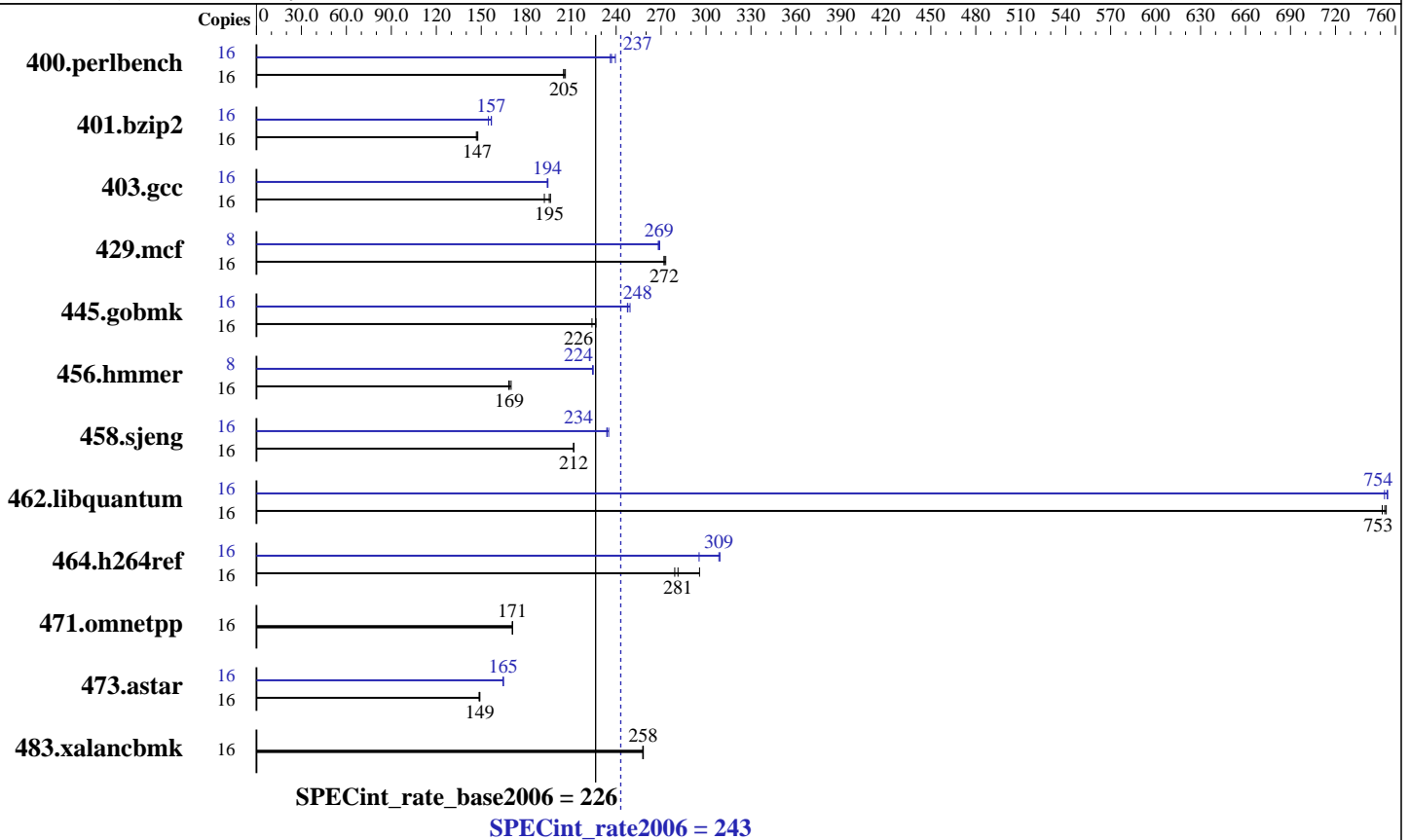
Test date: Jan-2010

Test sponsor: Cisco Systems

Hardware Availability: Oct-2009

Tested by: Cisco Systems

Software Availability: Mar-2009



### Hardware

CPU Name: Intel Xeon X5570  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 Chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 \* 4GB DDR3-1333 MHz)  
 Disk Subsystem: 73 GB SATA, 15kRPM  
 Other Hardware: None

### Software

Operating System: SuSe Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27-15-2-default, RC4  
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080, l\_cprof\_p\_11.0.080  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.18.50.0.7.20080502 and SmartHeap



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	758	206	<u>762</u>	<u>205</u>	763	205	16	662	236	653	240	<u>660</u>	<u>237</u>
401.bzip2	16	1052	147	<u>1048</u>	<u>147</u>	1045	148	16	997	155	985	157	<u>985</u>	<u>157</u>
403.gcc	16	656	196	<u>659</u>	<u>195</u>	670	192	16	<u>663</u>	<u>194</u>	662	195	663	194
429.mcf	16	<u>536</u>	<u>272</u>	534	273	536	272	8	<u>272</u>	<u>269</u>	271	269	272	268
445.gobmk	16	<u>742</u>	<u>226</u>	741	226	750	224	16	673	249	678	248	<u>677</u>	<u>248</u>
456.hammer	16	878	170	<u>884</u>	<u>169</u>	887	168	8	332	225	333	224	<u>333</u>	<u>224</u>
458.sjeng	16	<u>915</u>	<u>212</u>	914	212	915	212	16	<u>828</u>	<u>234</u>	828	234	824	235
462.libquantum	16	441	751	440	754	<u>440</u>	<u>753</u>	16	440	753	439	755	<u>439</u>	<u>754</u>
464.h264ref	16	1198	296	1269	279	<u>1259</u>	<u>281</u>	16	1145	309	<u>1147</u>	<u>309</u>	1199	295
471.omnetpp	16	585	171	<u>585</u>	<u>171</u>	586	171	16	585	171	<u>585</u>	<u>171</u>	586	171
473.astar	16	756	149	754	149	<u>754</u>	<u>149</u>	16	683	165	<u>682</u>	<u>165</u>	682	165
483.xalancbmk	16	428	258	<u>428</u>	<u>258</u>	428	258	16	428	258	<u>428</u>	<u>258</u>	428	258

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

ulimit -s unlimited was used to set the stacksize to unlimited prior to run

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

icpc

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
 -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
 -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revH.20100317.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revH.20100317.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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