



# SPEC<sup>®</sup> CINT2006 Result

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## Bull SAS

SPECint<sup>®</sup>2006 = 33.4

BL265  
(Intel Xeon X5550, 2.67 GHz)

SPECint\_base2006 = 29.6

CPU2006 license: 20

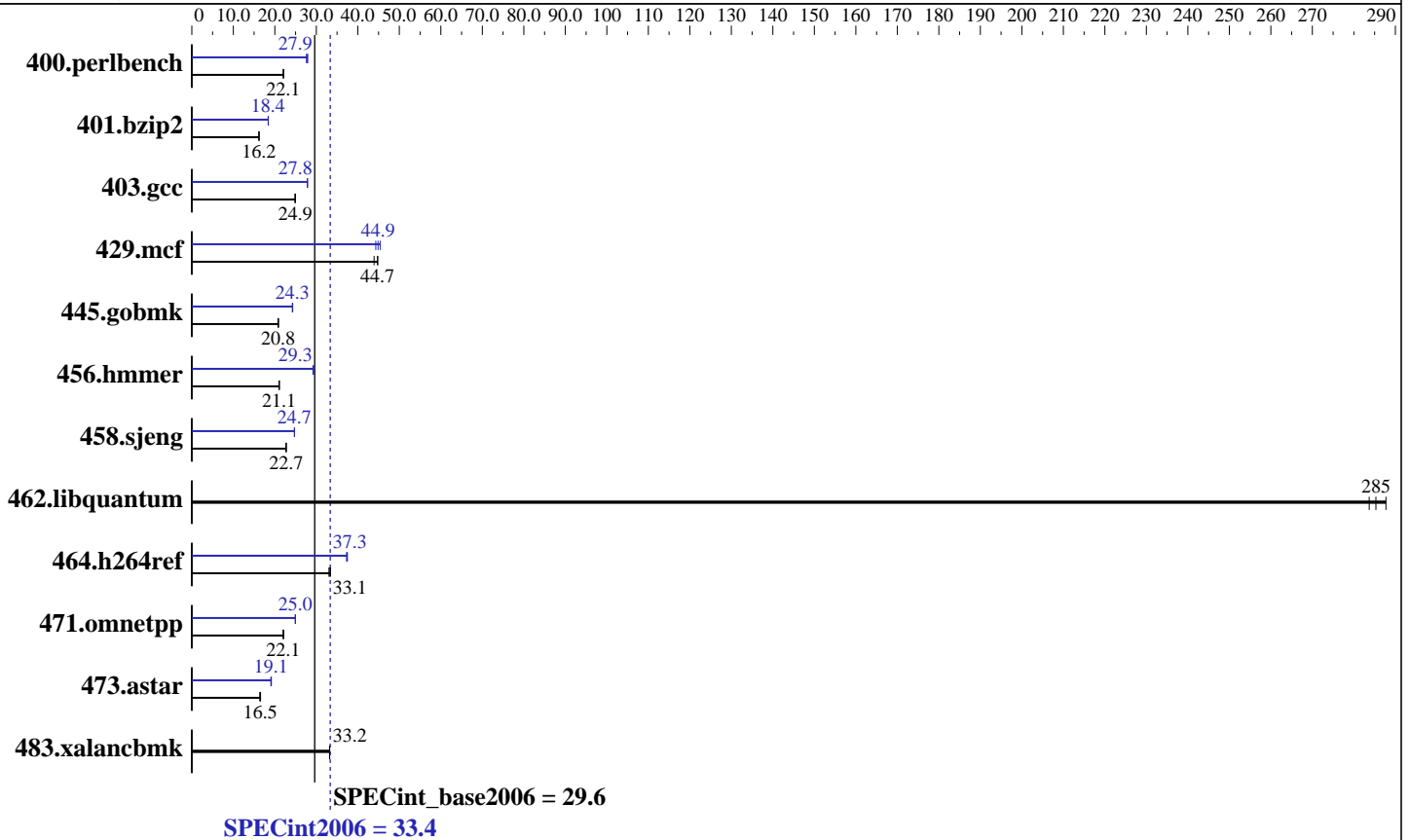
Test date: Jan-2010

Test sponsor: Bull SAS

Hardware Availability: May-2009

Tested by: Bull SAS

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon X5550  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB PC3-10600R, 2 Rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x 73 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	442	22.1	443	22.0	<b>443</b>	<b>22.1</b>	350	27.9	<b>351</b>	<b>27.9</b>	354	27.6
401.bzip2	596	16.2	<b>596</b>	<b>16.2</b>	597	16.2	525	18.4	<b>524</b>	<b>18.4</b>	523	18.4
403.gcc	324	24.9	323	24.9	<b>323</b>	<b>24.9</b>	288	27.9	<b>289</b>	<b>27.8</b>	290	27.8
429.mcf	208	43.9	204	44.8	<b>204</b>	<b>44.7</b>	201	45.4	206	44.3	<b>203</b>	<b>44.9</b>
445.gobmk	<b>503</b>	<b>20.8</b>	503	20.8	503	20.8	433	24.2	432	24.3	<b>433</b>	<b>24.3</b>
456.hammer	443	21.1	<b>443</b>	<b>21.1</b>	443	21.0	<b>319</b>	<b>29.3</b>	320	29.2	318	29.4
458.sjeng	532	22.7	533	22.7	<b>532</b>	<b>22.7</b>	489	24.7	<b>489</b>	<b>24.7</b>	489	24.7
462.libquantum	72.0	288	<b>72.6</b>	<b>285</b>	73.0	284	72.0	288	<b>72.6</b>	<b>285</b>	73.0	284
464.h264ref	662	33.4	669	33.1	<b>668</b>	<b>33.1</b>	590	37.5	593	37.3	<b>593</b>	<b>37.3</b>
471.omnetpp	<b>283</b>	<b>22.1</b>	284	22.0	283	22.1	250	25.0	251	24.9	<b>250</b>	<b>25.0</b>
473.astar	<b>426</b>	<b>16.5</b>	424	16.6	428	16.4	368	19.1	366	19.2	<b>367</b>	<b>19.1</b>
483.xalancbmk	208	33.2	<b>208</b>	<b>33.2</b>	208	33.2	208	33.2	<b>208</b>	<b>33.2</b>	208	33.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

icpc

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20100202.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20100202.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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