



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®2006 = 39.8

Dell Precision T7500 (Intel Xeon X5677, 3.46 GHz)

SPECint\_base2006 = 38.0

CPU2006 license: 55

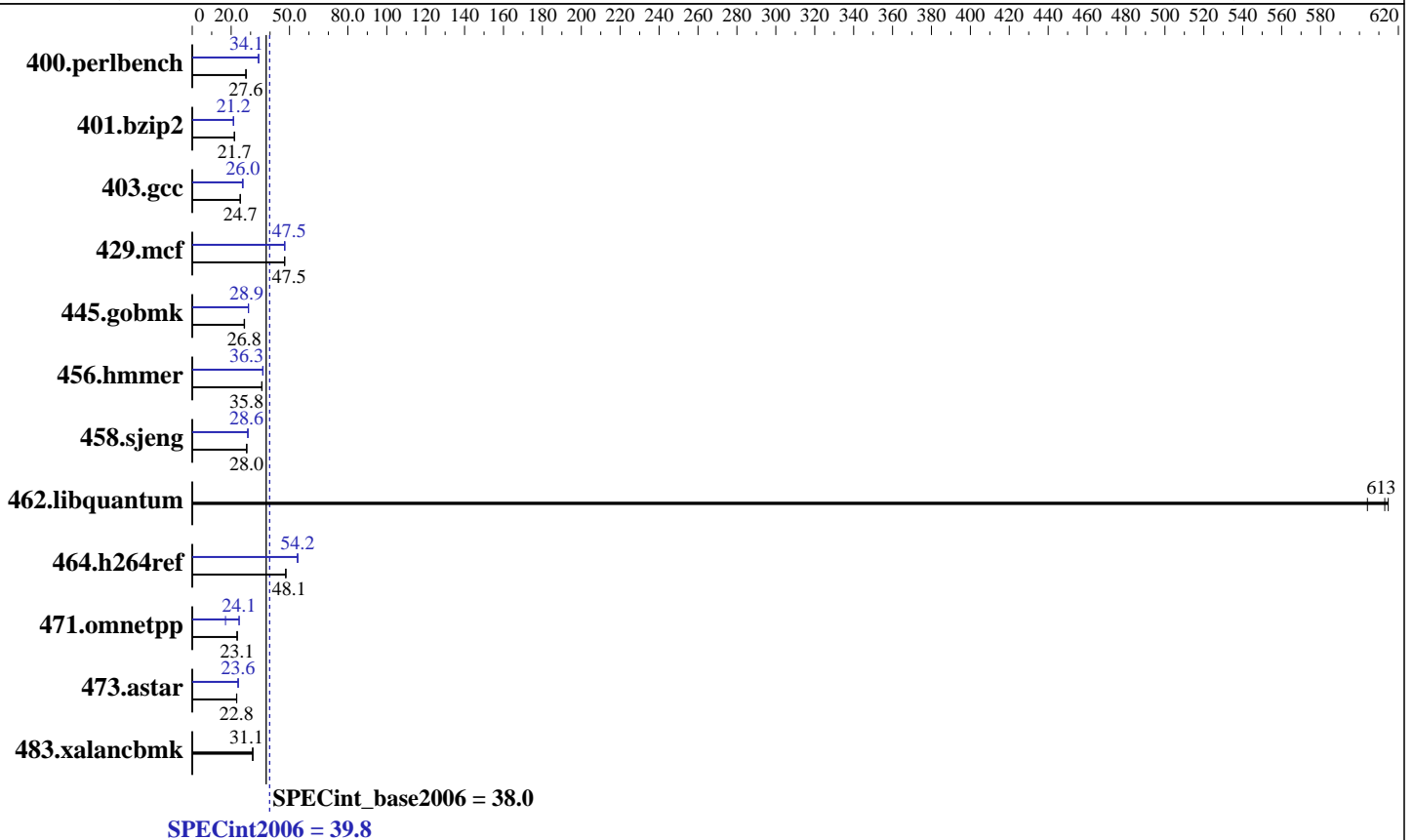
Test date: Mar-2010

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009



## Hardware

CPU Name: Intel Xeon X5677  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz  
 CPU MHz: 3466  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12x4 GB PC3-10600R)  
 Disk Subsystem: 1 x 300 GB SATA 10000 RPM  
 Other Hardware: None

## Software

Operating System: Windows 7 Professional (64-bit)  
 Compiler: Intel C++ Compiler Professional for Intel 64, Version 11.1  
 Build 20091130 Package ID: w\_eproc\_p\_11.1.054  
 Microsoft Visual Studio 2008 SP1  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 8.1 for x64



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>354</b>	<b>27.6</b>	353	27.7	354	27.6	286	34.1	<b>286</b>	<b>34.1</b>	286	34.1
401.bzip2	445	21.7	<b>445</b>	<b>21.7</b>	446	21.7	455	21.2	<b>455</b>	<b>21.2</b>	456	21.2
403.gcc	326	24.7	326	24.7	<b>326</b>	<b>24.7</b>	313	25.8	309	26.1	<b>309</b>	<b>26.0</b>
429.mcf	192	47.5	192	47.5	<b>192</b>	<b>47.5</b>	192	47.5	<b>192</b>	<b>47.5</b>	192	47.6
445.gobmk	390	26.9	<b>391</b>	<b>26.8</b>	393	26.7	363	28.9	<b>363</b>	<b>28.9</b>	363	28.9
456.hammer	261	35.8	261	35.8	<b>261</b>	<b>35.8</b>	<b>257</b>	<b>36.3</b>	257	36.3	257	36.3
458.sjeng	433	28.0	433	28.0	<b>433</b>	<b>28.0</b>	423	28.6	<b>423</b>	<b>28.6</b>	423	28.6
462.libquantum	33.7	615	34.3	604	<b>33.8</b>	<b>613</b>	33.7	615	34.3	604	<b>33.8</b>	<b>613</b>
464.h264ref	<b>460</b>	<b>48.1</b>	460	48.1	460	48.1	409	54.1	408	54.2	<b>408</b>	<b>54.2</b>
471.omnetpp	271	23.1	271	23.1	<b>271</b>	<b>23.1</b>	365	17.1	<b>260</b>	<b>24.1</b>	259	24.1
473.astar	<b>307</b>	<b>22.8</b>	307	22.9	308	22.8	<b>298</b>	<b>23.6</b>	298	23.6	297	23.6
483.xalancbmk	222	31.1	222	31.1	<b>222</b>	<b>31.1</b>	222	31.1	222	31.1	<b>222</b>	<b>31.1</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

OMP\_NUM\_THREADS=8 (number of cores)  
KMP\_AFFINITY=granularity=fine,scatter

## Platform Notes

BIOS Settings  
Memory Node Interleaving: NUMA  
Hyper-Threading: ENABLE

## General Notes

Binaries were built on Windows Vista Business (64-bit)

## Base Compiler Invocation

C benchmarks:  
icl -Qvc9 -Qstd=c99  
  
C++ benchmarks:  
icl -Qvc9



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## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_NO_NEED_VA_COPY
               -DSPEC_CPU_WIN64_X64
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword

```

## Base Optimization Flags

```

C benchmarks:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qparallel /F512000000

C++ benchmarks:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qcxx_features /F512000000 shlw64m.lib
-link /FORCE:MULTIPLE

```

## Base Other Flags

```

C benchmarks:

403.gcc: -Dalloca=_alloca

```

## Peak Compiler Invocation

```

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

```



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## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof\_gen(pass 1) -QxSSE4.2 -Qauto-ilp32  
-Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias  
-Qopt-prefetch /F512000000 shlw64m.lib  
-link /FORCE:MULTIPLE

401.bzip2: -Qprof\_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)  
-Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias  
-Qopt-prefetch /F512000000

403.gcc: -Qprof\_gen(pass 1) -QxSSE4.2 -Qauto-ilp32  
-Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- /F512000000

429.mcf: -QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div-  
-Qopt-prefetch /F512000000

445.gobmk: -Qprof\_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)  
-Qprof\_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias  
/F512000000

456.hmmer: -QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qunroll2  
-Qansi-alias /F512000000

458.sjeng: -Qprof\_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)  
-Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4  
/F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof\_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)  
-Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2  
-Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof\_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)  
-Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias  
-Qopt-ra-region-strategy=block /F512000000 shlw64m.lib  
-link /FORCE:MULTIPLE

473.astar: -Qprof\_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)  
-Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias  
-Qopt-ra-region-strategy=routine /F512000000 shlw64m.lib  
-link /FORCE:MULTIPLE

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.flags.ic11.1.win.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.flags.ic11.1.win.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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