



# SPEC<sup>®</sup> CFP2006 Result

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## Acer Incorporated

## SPECfp<sup>®</sup>\_rate2006 = 71.5

## Gateway GT150 F1 (Intel Xeon E5502)

## SPECfp\_rate\_base2006 = 69.3

CPU2006 license: 97

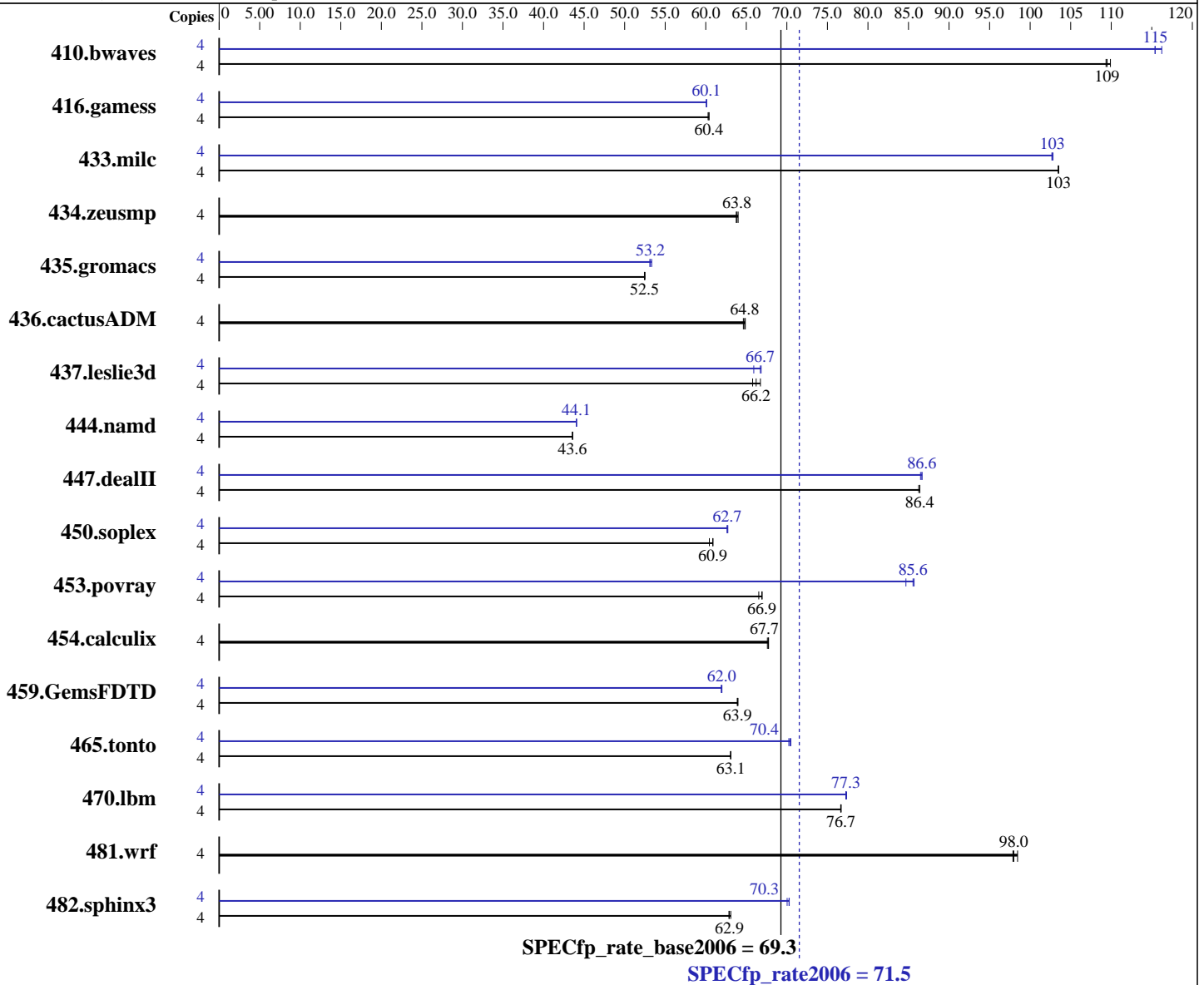
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2010

Hardware Availability: May-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon E5502  
 CPU Characteristics:  
 CPU MHz: 1866  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cprof\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)

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L3 Cache: 4 MB I+D on chip per chip  
Other Cache: None  
Memory: 24 GB (12 x 2GB DDR3-1333 RDIMM, running at 800 MHz)  
Disk Subsystem: 1000 GB SATAII, 7200 RPM  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	495	110	<b>496</b>	<b>109</b>	497	109	4	471	115	468	116	<b>471</b>	<b>115</b>		
416.gamess	4	<b>1297</b>	<b>60.4</b>	1299	60.3	1297	60.4	4	1304	60.1	<b>1303</b>	<b>60.1</b>	1303	60.1		
433.milc	4	<b>355</b>	<b>103</b>	355	104	355	103	4	357	103	<b>357</b>	<b>103</b>	358	103		
434.zeusmp	4	<b>570</b>	<b>63.8</b>	569	64.0	571	63.8	4	<b>570</b>	<b>63.8</b>	569	64.0	571	63.8		
435.gromacs	4	<b>544</b>	<b>52.5</b>	545	52.4	544	52.5	4	535	53.3	538	53.1	<b>537</b>	<b>53.2</b>		
436.cactusADM	4	<b>738</b>	<b>64.8</b>	740	64.6	737	64.9	4	<b>738</b>	<b>64.8</b>	740	64.6	737	64.9		
437.leslie3d	4	572	65.8	563	66.7	<b>568</b>	<b>66.2</b>	4	570	65.9	563	66.8	<b>563</b>	<b>66.7</b>		
444.namd	4	736	43.6	736	43.6	<b>736</b>	<b>43.6</b>	4	728	44.1	<b>728</b>	<b>44.1</b>	728	44.1		
447.dealII	4	530	86.4	<b>530</b>	<b>86.4</b>	531	86.3	4	528	86.7	529	86.5	<b>528</b>	<b>86.6</b>		
450.soplex	4	548	60.9	<b>548</b>	<b>60.9</b>	552	60.4	4	533	62.6	<b>532</b>	<b>62.7</b>	532	62.7		
453.povray	4	318	67.0	<b>318</b>	<b>66.9</b>	320	66.5	4	<b>249</b>	<b>85.6</b>	248	85.7	251	84.7		
454.calculix	4	488	67.6	487	67.7	<b>487</b>	<b>67.7</b>	4	488	67.6	487	67.7	<b>487</b>	<b>67.7</b>		
459.GemsFDTD	4	664	63.9	663	64.0	<b>664</b>	<b>63.9</b>	4	685	62.0	686	61.9	<b>685</b>	<b>62.0</b>		
465.tonto	4	624	63.1	<b>624</b>	<b>63.1</b>	624	63.1	4	<b>559</b>	<b>70.4</b>	560	70.2	558	70.5		
470.lbm	4	<b>717</b>	<b>76.7</b>	717	76.7	717	76.6	4	710	77.4	<b>711</b>	<b>77.3</b>	711	77.3		
481.wrf	4	456	97.9	<b>456</b>	<b>98.0</b>	454	98.4	4	456	97.9	<b>456</b>	<b>98.0</b>	454	98.4		
482.sphinx3	4	<b>1239</b>	<b>62.9</b>	1240	62.9	1236	63.1	4	1109	70.3	1113	70.0	<b>1110</b>	<b>70.3</b>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

This result was measured on the Gateway GT150 F1.  
The Acer AT150 F1 and Gateway GT150 F1 are electronically equivalent.



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -noFOR\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -noFOR\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -noFOR\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3:icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex:icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -opt-malloc-options=3 -ansi-alias -auto-ilp32

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## Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto -inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

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## Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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