



# SPEC® CINT2006 Result

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## Supermicro

**SPECint®2006 = 34.2**

Motherboard X8DT3-LN4F (Intel Xeon E5640, 2.66GHz)

**SPECint\_base2006 = 31.9**

CPU2006 license: 001176

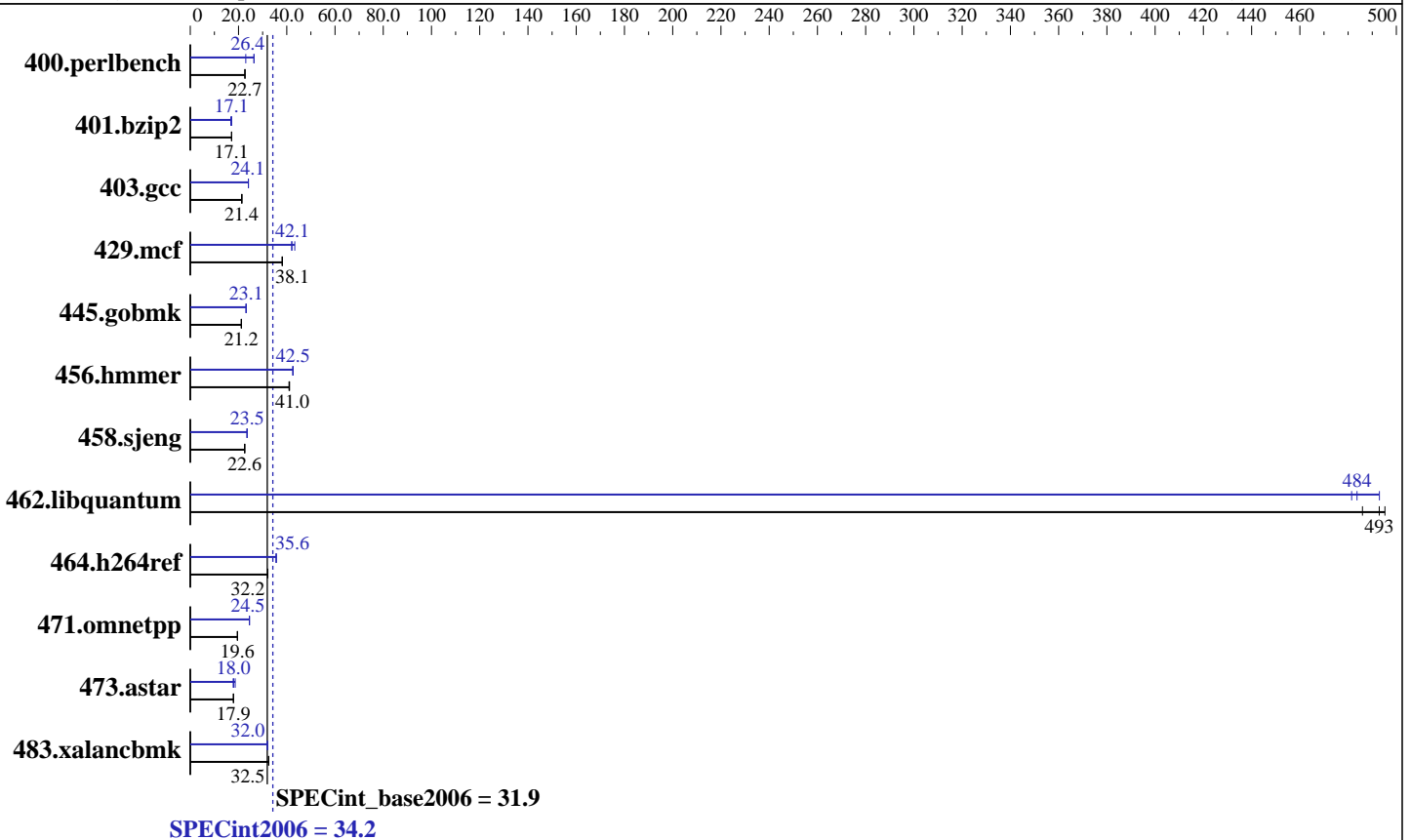
Test date: Jun-2010

Test sponsor: Supermicro

Hardware Availability: Mar-2010

Tested by: Supermicro

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon E5640  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 DDR3-1066 RDIMM, ECC, CL7)  
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 x86\_64  
 Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>431</u></b>	<b><u>22.7</u></b>	431	22.7	431	22.7	<b><u>370</u></b>	<b><u>26.4</u></b>	425	23.0	370	26.4
401.bzip2	563	17.1	563	17.1	<b><u>563</u></b>	<b><u>17.1</u></b>	574	16.8	<b><u>563</u></b>	<b><u>17.1</u></b>	563	17.2
403.gcc	377	21.4	377	21.4	<b><u>377</u></b>	<b><u>21.4</u></b>	<b><u>334</u></b>	<b><u>24.1</u></b>	334	24.1	334	24.1
429.mcf	<b><u>239</u></b>	<b><u>38.1</u></b>	239	38.1	239	38.2	<b><u>217</u></b>	<b><u>42.1</u></b>	217	42.0	211	43.3
445.gobmk	494	21.2	<b><u>496</u></b>	<b><u>21.2</u></b>	496	21.1	453	23.1	<b><u>453</u></b>	<b><u>23.1</u></b>	453	23.1
456.hammer	228	40.9	<b><u>227</u></b>	<b><u>41.0</u></b>	227	41.2	219	42.5	<b><u>219</u></b>	<b><u>42.5</u></b>	219	42.6
458.sjeng	536	22.6	537	22.5	<b><u>536</u></b>	<b><u>22.6</u></b>	514	23.6	514	23.5	<b><u>514</u></b>	<b><u>23.5</u></b>
462.libquantum	<b><u>42.0</u></b>	<b><u>493</u></b>	42.6	486	41.8	495	<b><u>42.8</u></b>	<b><u>484</u></b>	43.0	481	42.0	493
464.h264ref	688	32.2	690	32.1	<b><u>688</u></b>	<b><u>32.2</u></b>	621	35.6	622	35.6	<b><u>621</u></b>	<b><u>35.6</u></b>
471.omnetpp	<b><u>320</u></b>	<b><u>19.6</u></b>	319	19.6	320	19.5	255	24.5	<b><u>255</u></b>	<b><u>24.5</u></b>	255	24.5
473.astar	392	17.9	392	17.9	<b><u>392</u></b>	<b><u>17.9</u></b>	395	17.8	377	18.6	<b><u>390</u></b>	<b><u>18.0</u></b>
483.xalancbmk	212	32.5	212	32.5	<b><u>212</u></b>	<b><u>32.5</u></b>	216	32.0	<b><u>216</u></b>	<b><u>32.0</u></b>	216	31.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter

## Platform Notes

Fan speed set to Full Speed in BIOS Setup.  
As tested, the system used a Supermicro CSE-745TQ-920B chassis.  
The chassis is bundled with a PWS-920P-1R power supply, 2 SNK-P0038P heatsinks,  
as well as 2 FAN-0082L4 and 3 FAN-0074L4 cooling fans.

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64



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## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32
```

```
429.mcf: icc -m32
```

```
445.gobmk: icc -m32
```

```
464.h264ref: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m32
```

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## Peak Compiler Invocation (Continued)

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)  
 -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
 -opt-prefetch -par-schedule-static=32768 -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100915.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100915.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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