



# SPEC® CFP2006 Result

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## Bull SAS

### SPECfp®\_rate2006 = 195

NovaScale R450 F2 (Intel Xeon L5640, 2.26 GHz)

### SPECfp\_rate\_base2006 = 188

CPU2006 license: 20

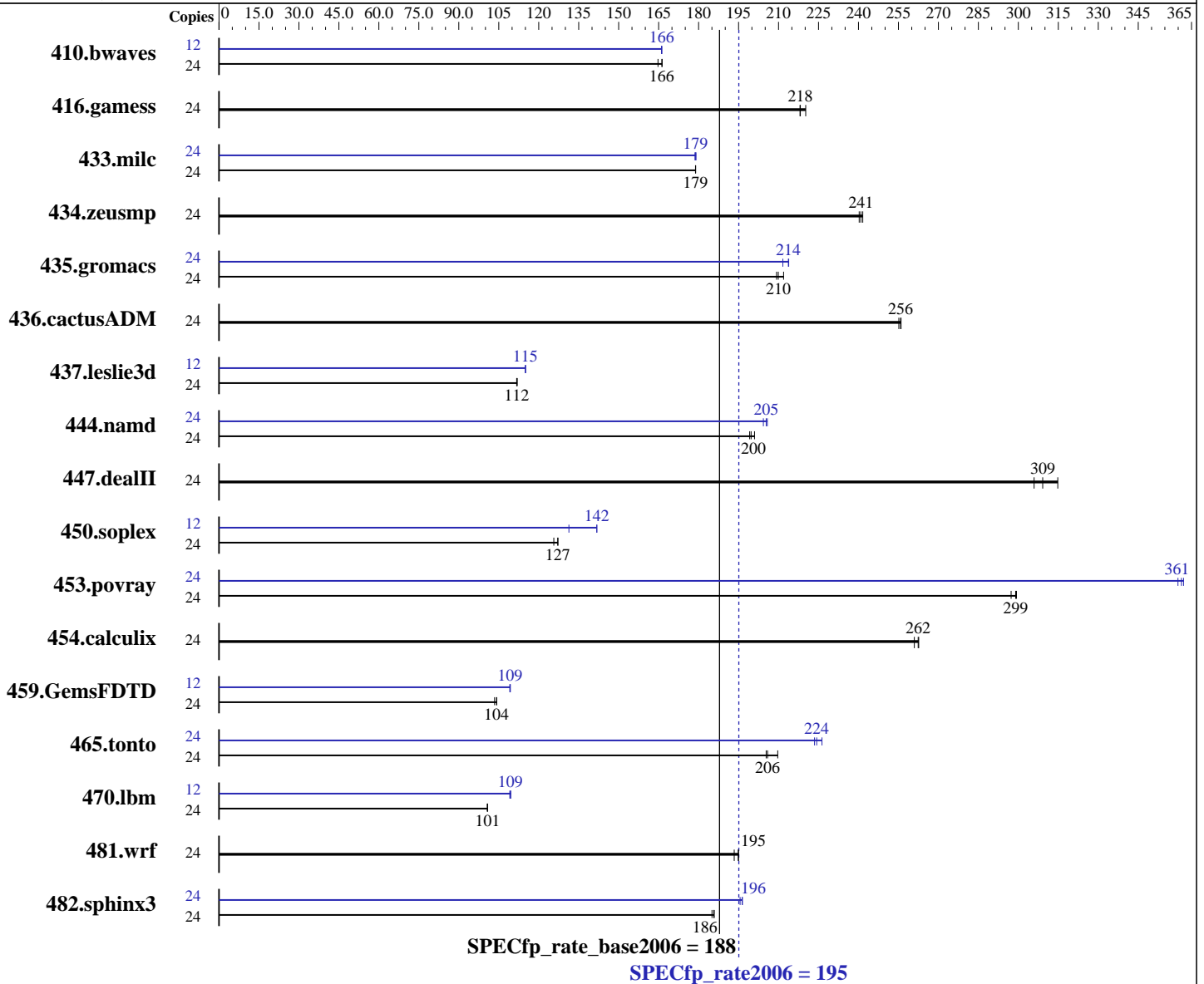
Test date: Jun-2010

Test sponsor: Bull SAS

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009



### Hardware

CPU Name: Intel Xeon L5640  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2267  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 24 GB (6 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1980	165	<u>1962</u>	<u>166</u>	1961	166	12	<u>982</u>	<u>166</u>	981	166	982	166
416.gamess	24	<u>2154</u>	<u>218</u>	2155	218	2134	220	24	<u>2154</u>	<u>218</u>	2155	218	2134	220
433.milc	24	1232	179	<u>1232</u>	<u>179</u>	1232	179	24	<u>1232</u>	<u>179</u>	1231	179	1233	179
434.zeusmp	24	904	242	909	240	<u>907</u>	<u>241</u>	24	904	242	909	240	<u>907</u>	<u>241</u>
435.gromacs	24	809	212	819	209	<u>816</u>	<u>210</u>	24	801	214	<u>802</u>	<u>214</u>	810	212
436.cactusADM	24	<u>1121</u>	<u>256</u>	1120	256	1124	255	24	<u>1121</u>	<u>256</u>	1120	256	1124	255
437.leslie3d	24	2016	112	2019	112	<u>2018</u>	<u>112</u>	12	<u>981</u>	<u>115</u>	980	115	981	115
444.namd	24	958	201	966	199	<u>964</u>	<u>200</u>	24	942	204	935	206	<u>937</u>	<u>205</u>
447.dealII	24	872	315	<u>888</u>	<u>309</u>	897	306	24	872	315	<u>888</u>	<u>309</u>	897	306
450.soplex	24	1593	126	1573	127	<u>1574</u>	<u>127</u>	12	762	131	706	142	<u>706</u>	<u>142</u>
453.povray	24	427	299	<u>427</u>	<u>299</u>	429	297	24	355	360	<u>353</u>	<u>361</u>	353	362
454.calculix	24	<u>755</u>	<u>262</u>	759	261	754	263	24	<u>755</u>	<u>262</u>	759	261	754	263
459.GemsFDTD	24	2462	103	2443	104	<u>2444</u>	<u>104</u>	12	<u>1165</u>	<u>109</u>	1164	109	1167	109
465.tonto	24	1126	210	<u>1147</u>	<u>206</u>	1150	205	24	<u>1053</u>	<u>224</u>	1057	224	1044	226
470.lbm	24	<u>3271</u>	<u>101</u>	3270	101	3276	101	12	1505	110	<u>1507</u>	<u>109</u>	1511	109
481.wrf	24	1375	195	<u>1376</u>	<u>195</u>	1386	193	24	1375	195	<u>1376</u>	<u>195</u>	1386	193
482.sphinx3	24	2528	185	<u>2519</u>	<u>186</u>	2516	186	24	2392	196	2381	196	<u>2382</u>	<u>196</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)  
Data Reuse = Disabled (Default = Enabled)



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## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502  
The Dell PowerEdge R510 and  
the Bull NovaScale R450 F2 models are electronically equivalent.  
The results have been measured on a Dell PowerEdge R510 model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

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## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`  
 416.gamess: `-DSPEC_CPU_LP64`  
 433.milc: `-DSPEC_CPU_LP64`  
 434.zeusmp: `-DSPEC_CPU_LP64`  
 435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`  
 436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`  
 437.leslie3d: `-DSPEC_CPU_LP64`  
 444.namd: `-DSPEC_CPU_LP64`  
 447.dealII: `-DSPEC_CPU_LP64`  
 453.povray: `-DSPEC_CPU_LP64`  
 454.calculix: `-DSPEC_CPU_LP64 -nofor_main`  
 459.GemsFDTD: `-DSPEC_CPU_LP64`  
 465.tonto: `-DSPEC_CPU_LP64`  
 470.lbm: `-DSPEC_CPU_LP64`  
 481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`



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## Peak Optimization Flags

### C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto -inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

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## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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