



# SPEC<sup>®</sup> CFP2006 Result

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## Fujitsu

SPECfp<sup>®</sup>2006 = 27.6

PRIMERGY TX200 S6, Intel Xeon L5609, 1.86 GHz

SPECfp\_base2006 = 25.8

CPU2006 license: 19

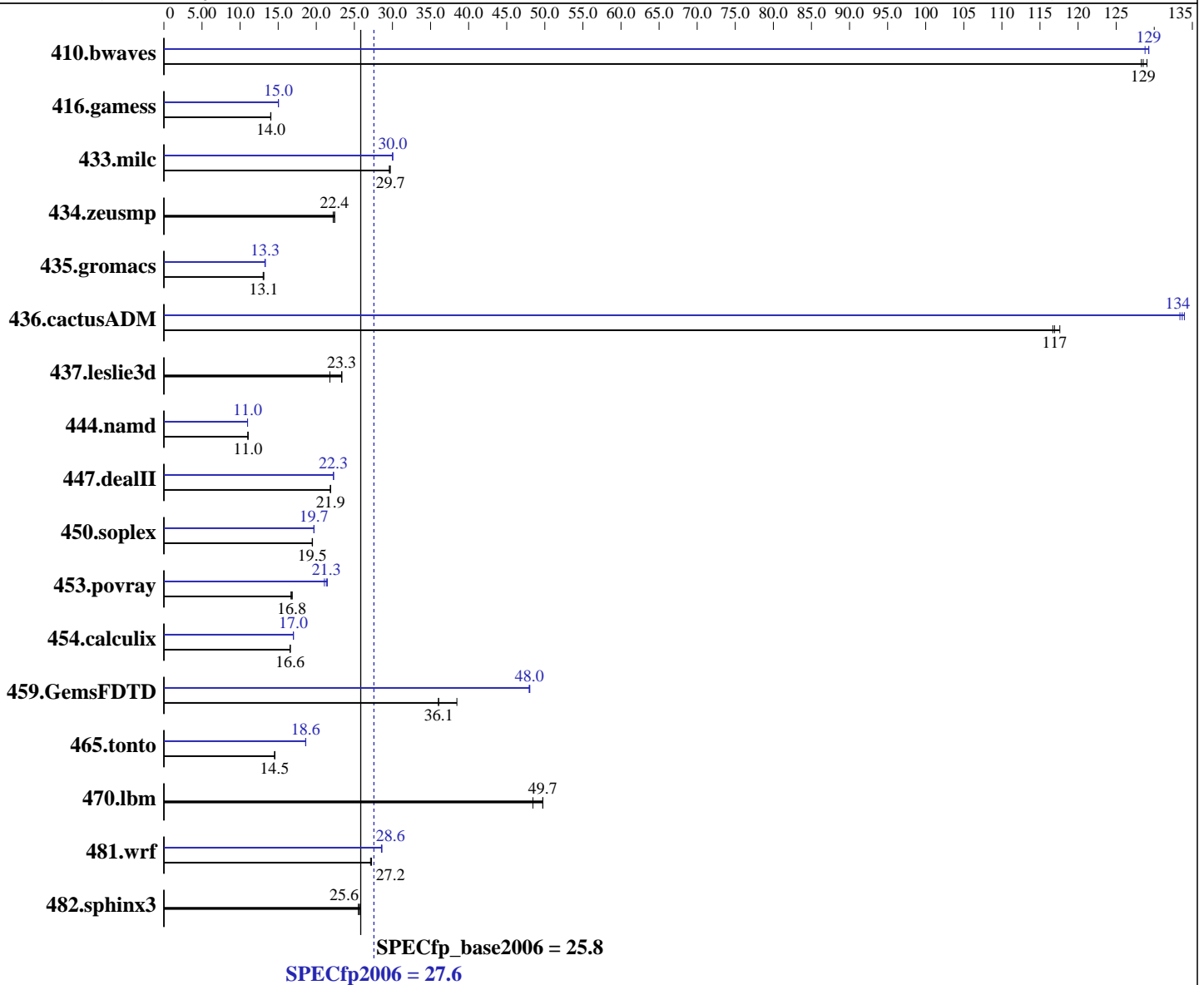
Test date: Aug-2010

Test sponsor: Fujitsu

Hardware Availability: Jul-2010

Tested by: Fujitsu

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon L5609  
 CPU Characteristics:  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Multi-User Run Level 3

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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
Disk Subsystem: 1 x SATA, 160 GB, 5.4 krpm  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b>106</b>	<b>129</b>	106	128	105	129	<b>105</b>	<b>129</b>	106	129	105	129
416.gamess	1394	14.0	1398	14.0	<b>1394</b>	<b>14.0</b>	<b>1303</b>	<b>15.0</b>	1303	15.0	1303	15.0
433.milc	<b>309</b>	<b>29.7</b>	309	29.7	310	29.6	306	30.0	<b>306</b>	<b>30.0</b>	306	30.0
434.zeusmp	<b>406</b>	<b>22.4</b>	406	22.4	410	22.2	<b>406</b>	<b>22.4</b>	406	22.4	410	22.2
435.gromacs	545	13.1	<b>545</b>	<b>13.1</b>	548	13.0	536	13.3	539	13.3	<b>537</b>	<b>13.3</b>
436.cactusADM	102	117	102	118	<b>102</b>	<b>117</b>	89.6	133	<b>89.4</b>	<b>134</b>	89.2	134
437.leslie3d	<b>403</b>	<b>23.3</b>	402	23.4	431	21.8	<b>403</b>	<b>23.3</b>	402	23.4	431	21.8
444.namd	727	11.0	727	11.0	<b>727</b>	<b>11.0</b>	<b>730</b>	<b>11.0</b>	731	11.0	730	11.0
447.dealII	523	21.9	523	21.9	<b>523</b>	<b>21.9</b>	514	22.3	<b>514</b>	<b>22.3</b>	514	22.2
450.soplex	<b>428</b>	<b>19.5</b>	428	19.5	428	19.5	423	19.7	423	19.7	<b>423</b>	<b>19.7</b>
453.povray	<b>317</b>	<b>16.8</b>	319	16.7	316	16.8	248	21.5	<b>249</b>	<b>21.3</b>	253	21.1
454.calculix	498	16.6	<b>498</b>	<b>16.6</b>	497	16.6	485	17.0	486	17.0	<b>486</b>	<b>17.0</b>
459.GemsFDTD	295	36.0	276	38.5	<b>294</b>	<b>36.1</b>	<b>221</b>	<b>48.0</b>	221	48.0	221	47.9
465.tonto	676	14.6	677	14.5	<b>677</b>	<b>14.5</b>	<b>528</b>	<b>18.6</b>	529	18.6	528	18.6
470.lbm	276	49.7	284	48.4	<b>276</b>	<b>49.7</b>	276	49.7	284	48.4	<b>276</b>	<b>49.7</b>
481.wrf	<b>410</b>	<b>27.2</b>	412	27.1	410	27.3	<b>391</b>	<b>28.6</b>	391	28.6	390	28.6
482.sphinx3	<b>762</b>	<b>25.6</b>	763	25.5	757	25.8	<b>762</b>	<b>25.6</b>	763	25.5	757	25.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 1066 MHz.  
BIOS configuration:  
Data Reuse Optimization = Disable

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
KMP\_STACKSIZE set to 200M

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## General Notes (Continued)

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32`

447.dealIII: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32`

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## Peak Optimization Flags (Continued)

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>



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