



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECfp[®]_rate2006 = 491

Cisco UCS B440 M1 (Intel Xeon L7555, 1.87 GHz)

SPECfp_rate_base2006 = 474

CPU2006 license: 9019

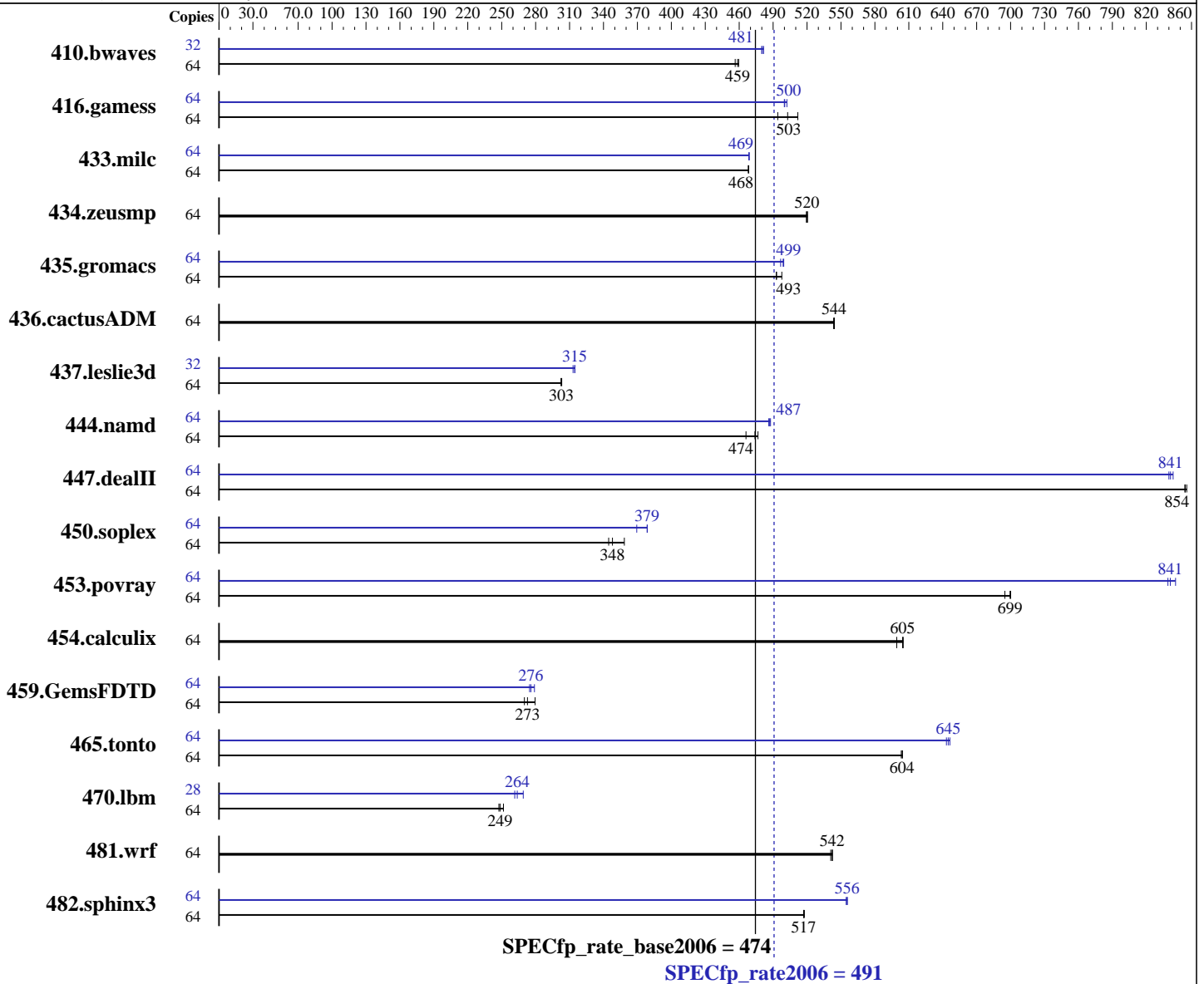
Test date: Sep-2010

Test sponsor: Cisco Systems

Hardware Availability: May-2010

Tested by: Cisco Systems

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon L7555
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,3,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SuSe Linux Enterprise Server 11 (x86_64), Kernel 2.6.27-19-5-default
 Compiler: Intel C++ and Fortran Compiler 11.1 IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064 L_cprof_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECfp_rate2006 = 491

Cisco UCS B440 M1 (Intel Xeon L7555, 1.87 GHz)

SPECfp_rate_base2006 = 474

CPU2006 license: 9019

Test date: Sep-2010

Test sponsor: Cisco Systems

Hardware Availability: May-2010

Tested by: Cisco Systems

Software Availability: Jan-2010

L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (64 x 8 GB 4Rx4 PC3-8500R, ECC)
Disk Subsystem: 146 GB SAS, 15K RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|------------|-------------|------------|-------------|------------|--------|-------------|------------|-------------|------------|-------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 64 | 1905 | 456 | 1893 | 460 | <u>1897</u> | <u>459</u> | 32 | <u>905</u> | <u>481</u> | 903 | 482 | 906 | 480 |
| 416.gamess | 64 | <u>2492</u> | <u>503</u> | 2448 | 512 | 2536 | 494 | 64 | <u>2505</u> | <u>500</u> | 2507 | 500 | 2495 | 502 |
| 433.milc | 64 | 1255 | 468 | <u>1255</u> | <u>468</u> | 1255 | 468 | 64 | <u>1254</u> | <u>469</u> | 1253 | 469 | 1254 | 469 |
| 434.zeusmp | 64 | 1119 | 520 | 1121 | 519 | <u>1120</u> | <u>520</u> | 64 | 1119 | 520 | 1121 | 519 | <u>1120</u> | <u>520</u> |
| 435.gromacs | 64 | 918 | 498 | 927 | 493 | <u>927</u> | <u>493</u> | 64 | <u>916</u> | <u>499</u> | 920 | 497 | 915 | 499 |
| 436.cactusADM | 64 | 1406 | 544 | 1407 | 544 | <u>1406</u> | <u>544</u> | 64 | 1406 | 544 | 1407 | 544 | <u>1406</u> | <u>544</u> |
| 437.leslie3d | 64 | 1985 | 303 | <u>1987</u> | <u>303</u> | 1989 | 302 | 32 | 961 | 313 | <u>956</u> | <u>315</u> | 956 | 315 |
| 444.namd | 64 | 1101 | 466 | <u>1083</u> | <u>474</u> | 1077 | 477 | 64 | <u>1054</u> | <u>487</u> | 1056 | 486 | 1052 | 488 |
| 447.dealII | 64 | 857 | 854 | 856 | 856 | <u>857</u> | <u>854</u> | 64 | <u>870</u> | <u>841</u> | 868 | 844 | 872 | 840 |
| 450.soplex | 64 | 1549 | 345 | <u>1534</u> | <u>348</u> | 1489 | 358 | 64 | <u>1410</u> | <u>379</u> | 1409 | 379 | 1445 | 369 |
| 453.povray | 64 | 490 | 695 | 486 | 700 | <u>487</u> | <u>699</u> | 64 | 406 | 839 | <u>405</u> | <u>841</u> | 403 | 846 |
| 454.calculix | 64 | 873 | 605 | <u>873</u> | <u>605</u> | 881 | 599 | 64 | 873 | 605 | <u>873</u> | <u>605</u> | 881 | 599 |
| 459.GemsFDTD | 64 | 2429 | 280 | <u>2489</u> | <u>273</u> | 2514 | 270 | 64 | <u>2462</u> | <u>276</u> | 2473 | 275 | 2434 | 279 |
| 465.tonto | 64 | <u>1042</u> | <u>604</u> | 1042 | 604 | 1044 | 603 | 64 | 974 | 646 | 979 | 643 | <u>977</u> | <u>645</u> |
| 470.lbm | 64 | <u>3538</u> | <u>249</u> | 3553 | 248 | 3496 | 252 | 28 | 1430 | 269 | <u>1459</u> | <u>264</u> | 1471 | 262 |
| 481.wrf | 64 | 1322 | 541 | 1318 | 543 | <u>1319</u> | <u>542</u> | 64 | 1322 | 541 | 1318 | 543 | <u>1319</u> | <u>542</u> |
| 482.sphinx3 | 64 | <u>2412</u> | <u>517</u> | 2413 | 517 | 2410 | 518 | 64 | 2245 | 556 | <u>2245</u> | <u>556</u> | 2249 | 555 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

ulimit -s unlimited was used to set the stacksize to unlimited prior to run

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECfp_rate2006 = 491

Cisco UCS B440 M1 (Intel Xeon L7555, 1.87 GHz)

SPECfp_rate_base2006 = 474

CPU2006 license: 9019

Test date: Sep-2010

Test sponsor: Cisco Systems

Hardware Availability: May-2010

Tested by: Cisco Systems

Software Availability: Jan-2010

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECfp_rate2006 = 491

Cisco UCS B440 M1 (Intel Xeon L7555, 1.87 GHz)

SPECfp_rate_base2006 = 474

CPU2006 license: 9019

Test date: Sep-2010

Test sponsor: Cisco Systems

Hardware Availability: May-2010

Tested by: Cisco Systems

Software Availability: Jan-2010

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-malloc-options=3 -ansi-alias -auto-ilp32

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECfp_rate2006 = 491

Cisco UCS B440 M1 (Intel Xeon L7555, 1.87 GHz)

SPECfp_rate_base2006 = 474

CPU2006 license: 9019

Test date: Sep-2010

Test sponsor: Cisco Systems

Hardware Availability: May-2010

Tested by: Cisco Systems

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECfp_rate2006 = 491

Cisco UCS B440 M1 (Intel Xeon L7555, 1.87 GHz)

SPECfp_rate_base2006 = 474

CPU2006 license: 9019

Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: Sep-2010

Hardware Availability: May-2010

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 14:20:00 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 October 2010.